

# C1005X7S0J474M050BB

TDK Item Description : C1005X7S0J474MT\*\*\*\*



## Application & Main Feature

Commercial Grade ( General (Up to 50V) )

## Series

C1005 [EIA CC0402]



## Dimensions

L 1.00mm +/-0.05mm  
 W 0.50mm +/-0.05mm  
 T 0.50mm +/-0.05mm  
 B 0.10mm Min.  
 G 0.30mm Min.

## Temperature Characteristic

X7S (-55 to 125 degC +/-22%)

## Rated Voltage

0J (6.3Vdc)

## Capacitance

470nF

## Capacitance Tolerance

M (+/-20%)

## Dissipation Factor

10% Max.

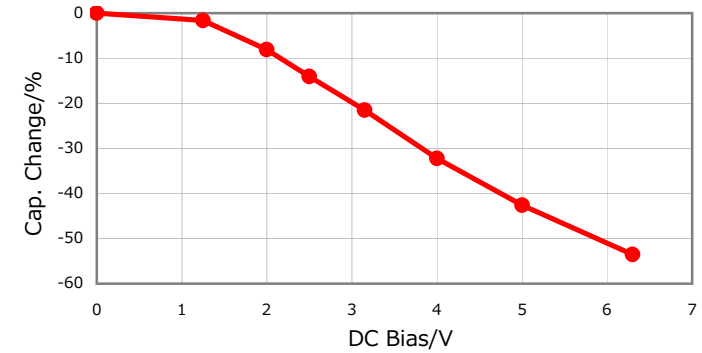
## Insulation Resistance

212Mohm Min.

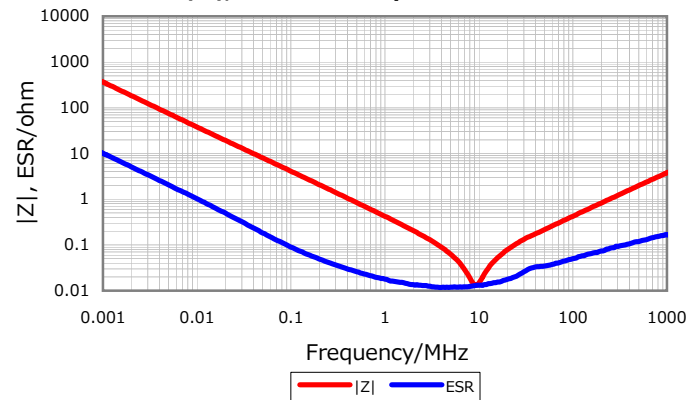
## AEC-Q200

Not Applicable

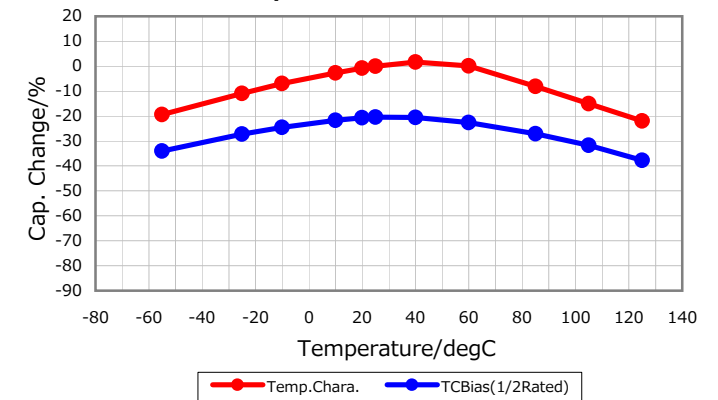
## DC Bias Characteristic



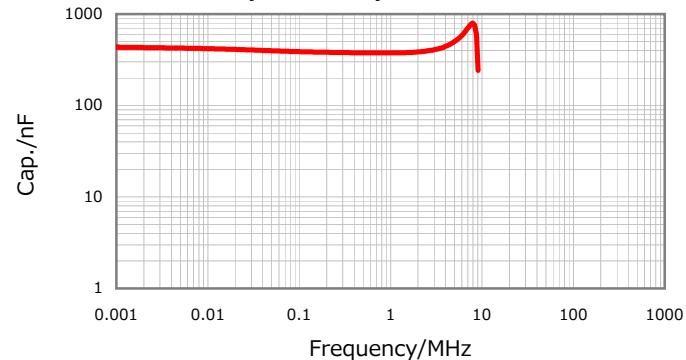
## |Z|, ESR vs. Freq. Characteristics



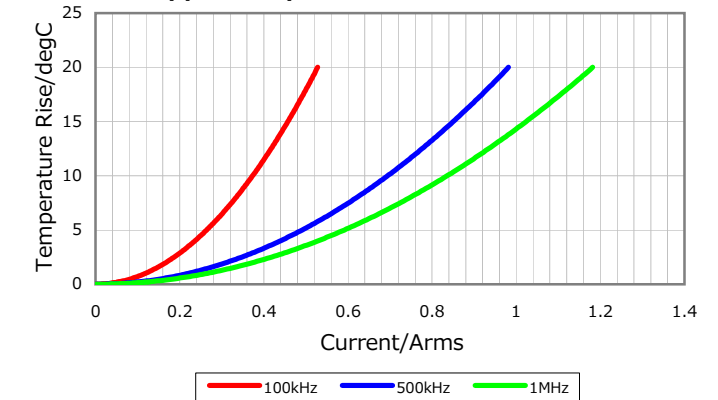
## Temperature Characteristics



## Cap. vs. Freq. Characteristic



## Ripple Temperature Rise Characteristics



Characterization Sheet ( Multilayer Ceramic Chip Capacitors )



All specifications are subject to change without notice.

January 3, 2016