Ordering number : EN6460C

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

P-Channel Silicon MOSFET

MCH6605 — General-Purpose Switching Device Applications

Features

- Low ON-resistance
- Ultrahigh-speed switching
- 4V drive
- Composite type with 2 MOSFETs contained in a single package, facilitating high-density mounting

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		−50	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		−0.14	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	−0.56	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (900mm²×0.8mm)1unit	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

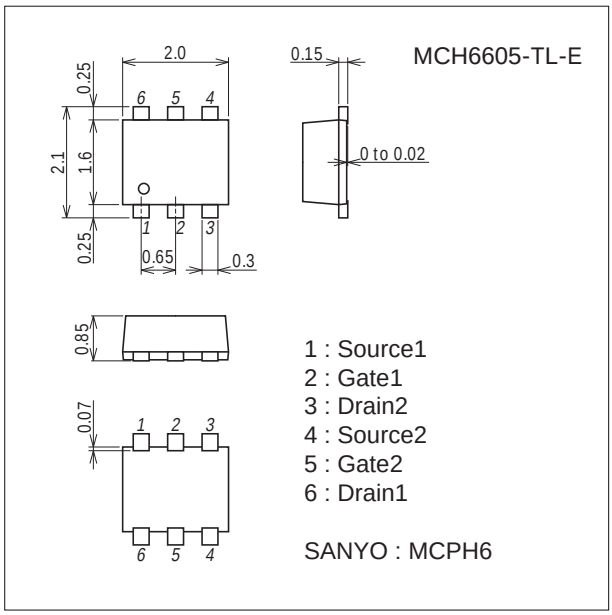
This product is designed to "ESD immunity < 200V*", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

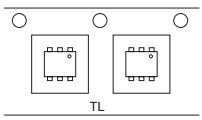
7022A-006



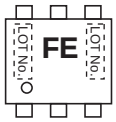
Product & Package Information

- Package : MCPH6
- JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

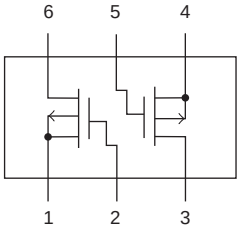
Packing Type : TL



Marking



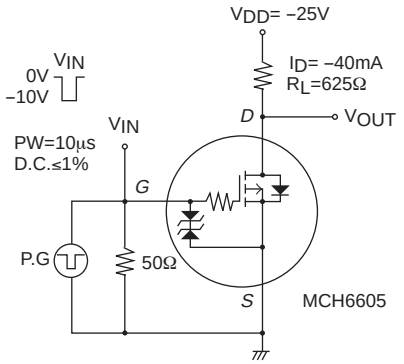
Electrical Connection



Electrical Characteristics at Ta=25°C

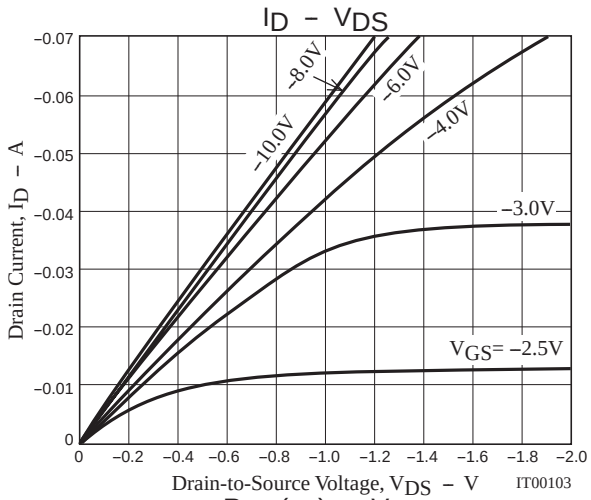
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	−50			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=−50V, V_{GS}=0V			−1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=−10V, I_D=−100μA	−1		−2.5	V
Forward Transfer Admittance	| yfs |	V_{DS}=−10V, I_D=−40mA	50	70		mS
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=−40mA, V_{GS}=−10V		17	22	Ω
	R_{DS}(on)2	I_D=−20mA, V_{GS}=−4V		23	32	Ω
Input Capacitance	Ciss	V_{DS}=−10V, f=1MHz		6.2		pF
Output Capacitance	Coss			4.0		pF
Reverse Transfer Capacitance	Crss			1.3		pF
Turn-ON Delay Time	t_d(on)	See specified Test Circuit.		13		ns
Rise Time	t_r			10		ns
Turn-OFF Delay Time	t_d(off)			100		ns
Fall Time	t_f			150		ns
Total Gate Charge	Qg	V_{DS}=−10V, V_{GS}=−10V, I_D=−70mA		1.32		nC
Gate-to-Source Charge	Qgs			0.17		nC
Gate-to-Drain “Miller” Charge	Qgd			0.34		nC
Diode Forward Voltage	V_{SD}	I_S=−70mA, V_{GS}=0V		−0.85	−1.2	V

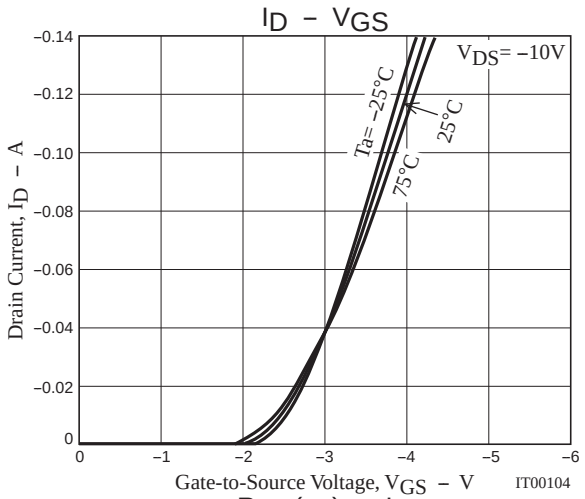
Switching Time Test Circuit

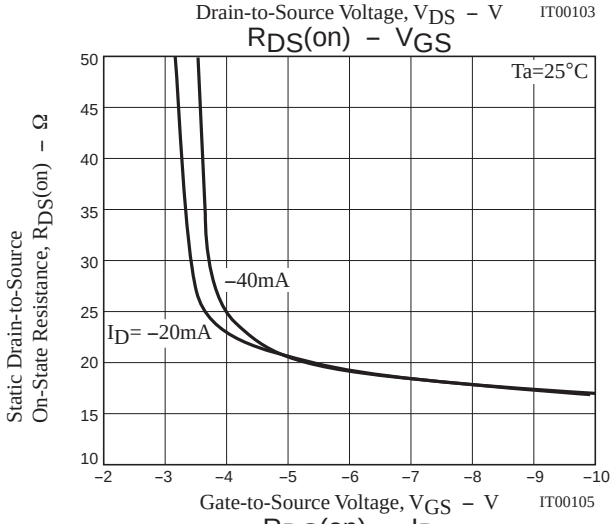


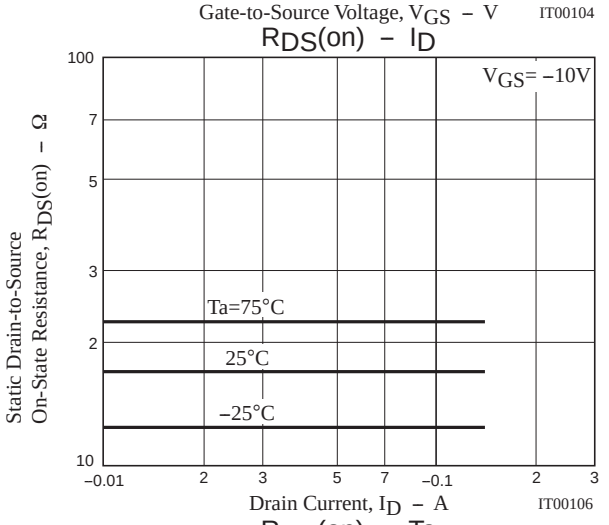
Ordering Information

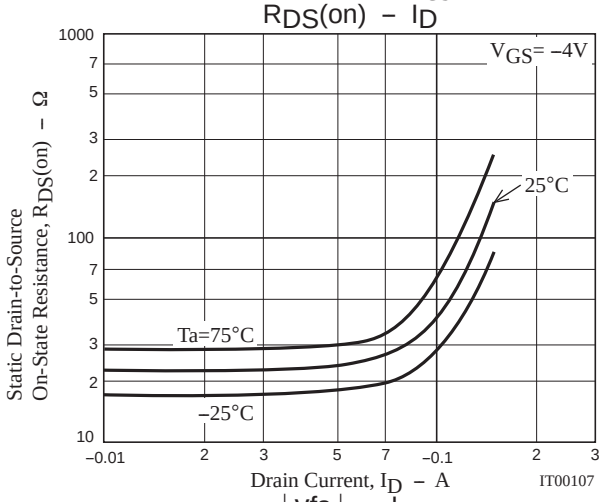
Device	Package	Shipping	memo
MCH6605-TL-E	MCPH6	3,000pcs./reel	Pb Free

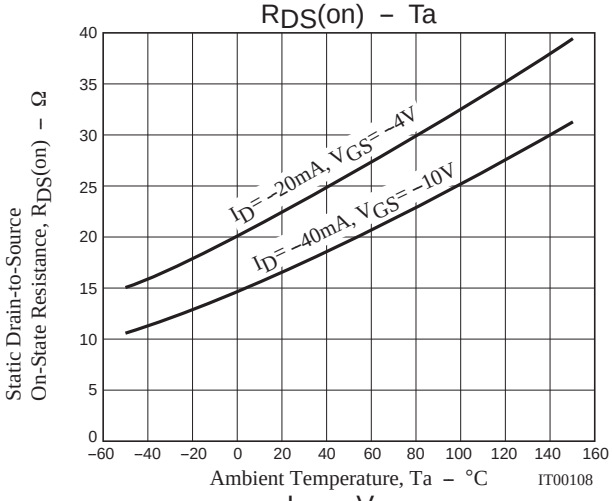

ID – VDS
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT00103
–8.0V
–6.0V
–10.0V
–4.0V
–3.0V
VGS= –2.5V

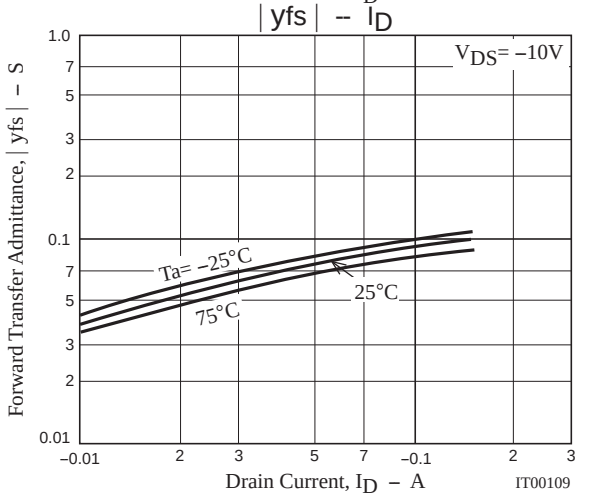

ID – VGS
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
IT00104
VDS= –10V
Ta= –25°C
25°C
75°C

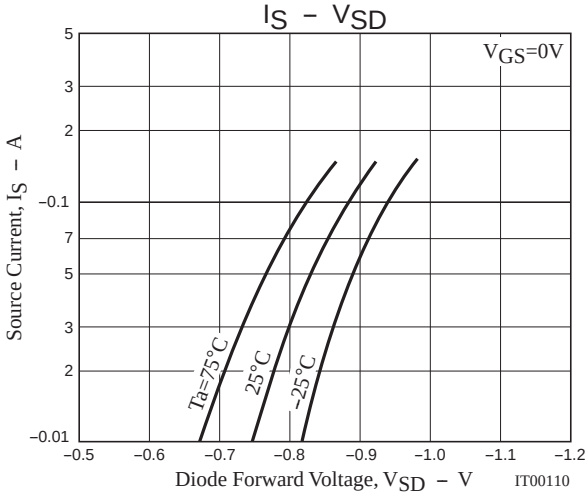

RDS(on) – VGS
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Gate-to-Source Voltage, VGS – V
IT00105
Ta=25°C
–40mA
ID= –20mA


RDS(on) – ID
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Drain Current, ID – A
IT00106
VGS= –10V
Ta=75°C
25°C
–25°C

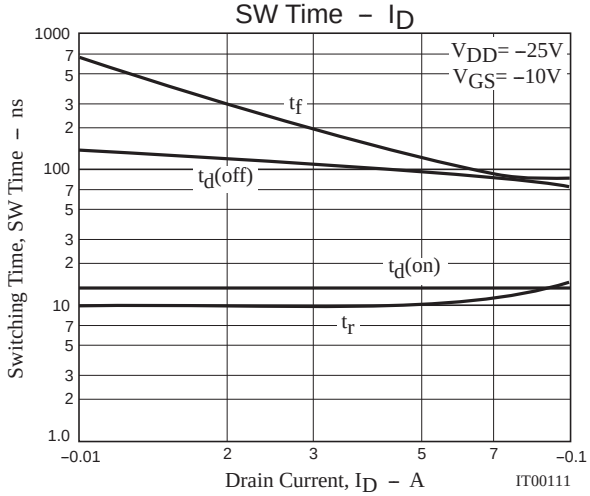

RDS(on) – ID
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Drain Current, ID – A
IT00107
VGS= –4V
25°C
Ta=75°C
–25°C


RDS(on) – Ta
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Ambient Temperature, Ta – °C
IT00108
ID= –20mA, VGS= –4V
ID= –40mA, VGS= –10V

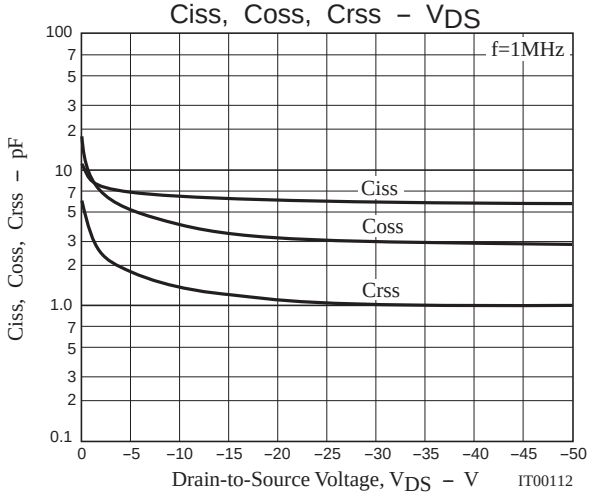

| yfs | – ID
Forward Transfer Admittance, | yfs | – S
Drain Current, ID – A
IT00109
VDS= –10V
Ta= –25°C
25°C
75°C


IS – VSD
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT00110
VGS=0V
Ta=75°C
25°C
–25°C

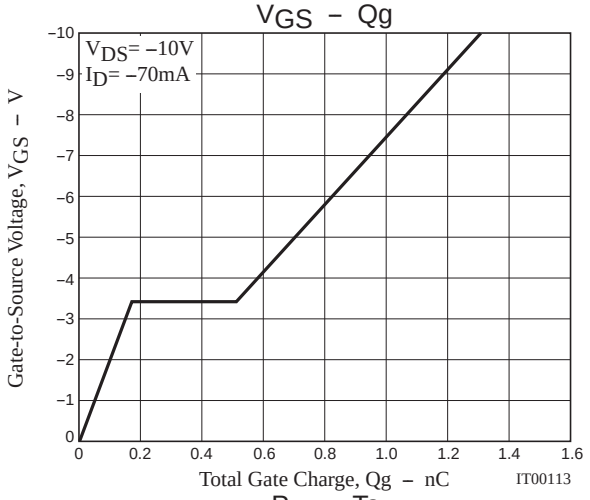
SW Time – I_D



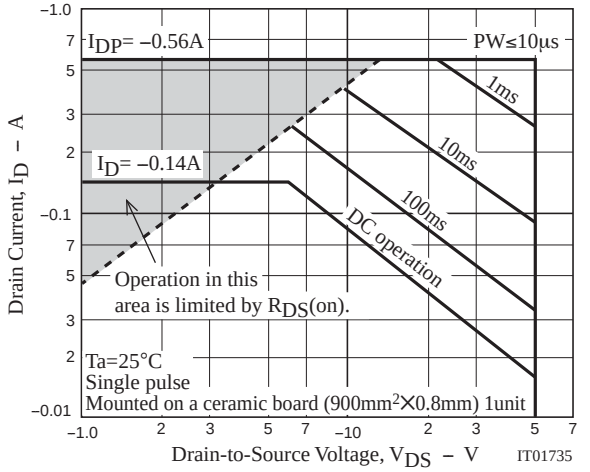
Ciss, Coss, Crss – V_{DS}



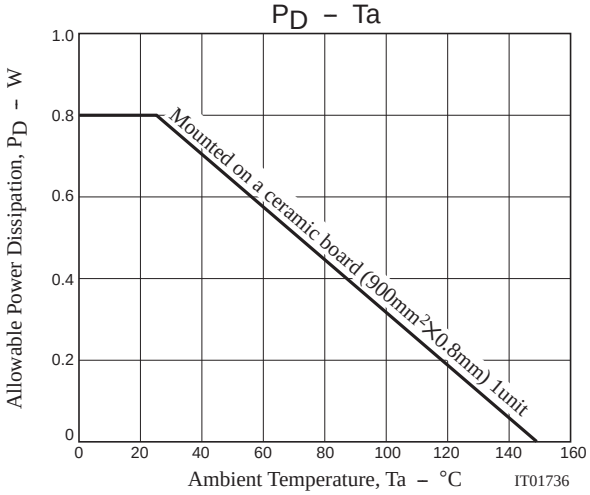
V_{GS} – Qg



A S O



P_D – Ta



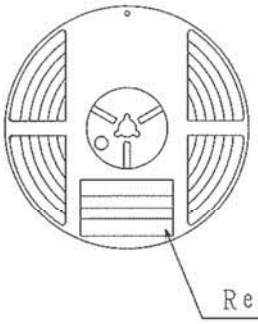
Taping Specification

MCH6605-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximun Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

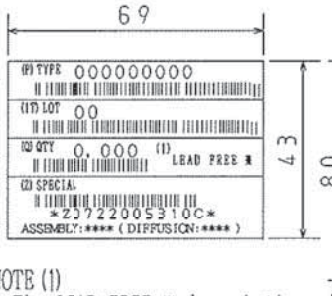
Packing method



Reel label

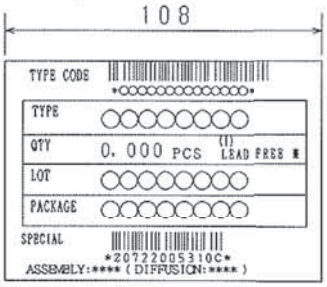
Reel label, Inner box label

(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

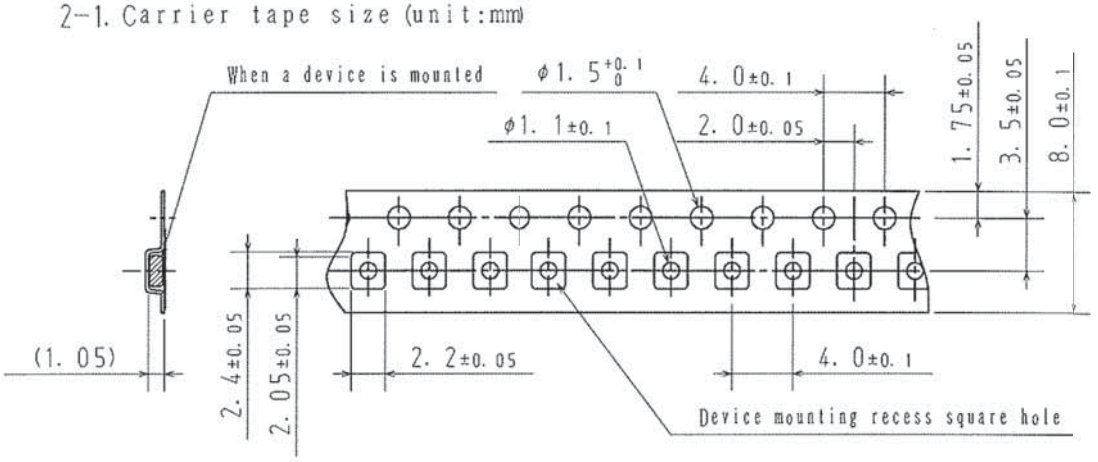


NOTE (1)
The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

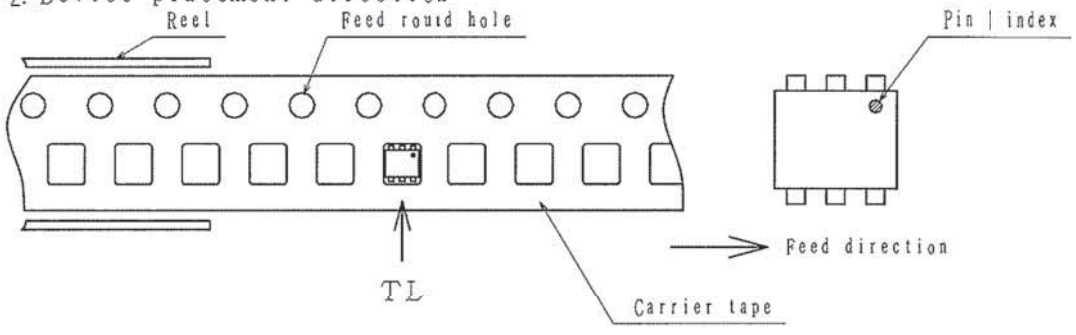
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



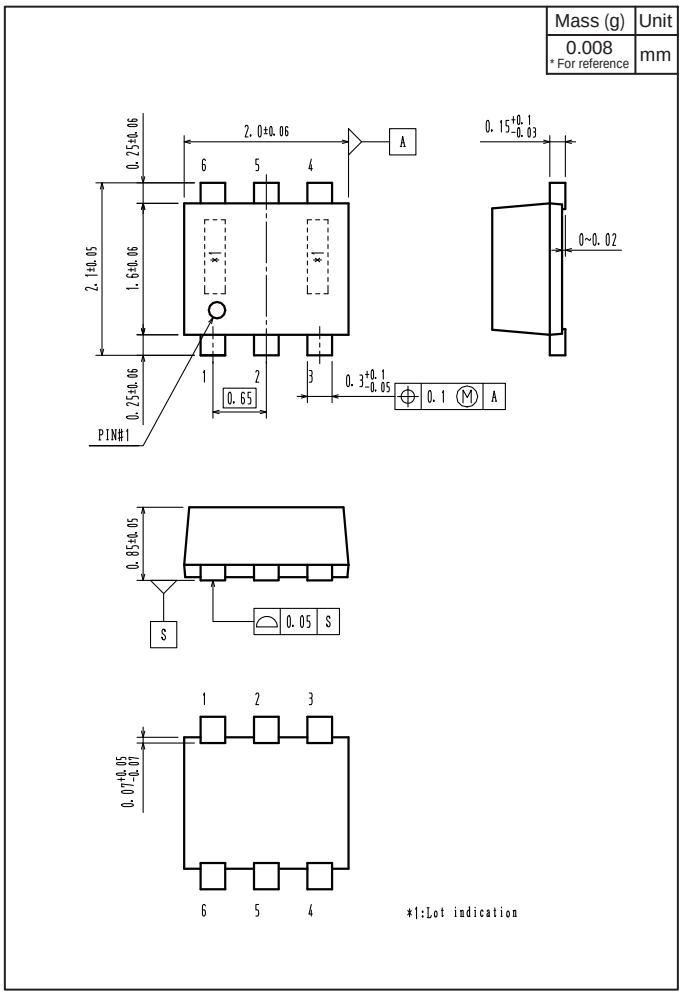
2-2. Device placement direction



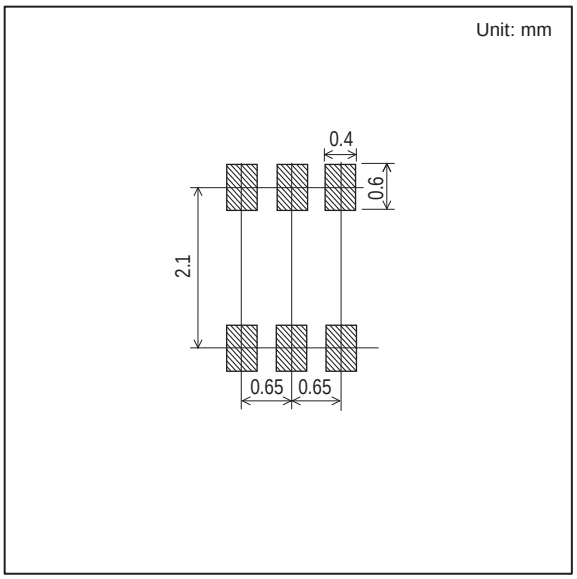
Those with pin 1 index on the feed hole side·····TL

Outline Drawing

MCH6605-TL-E



Land Pattern Example



Note on usage : Since the MCH6605 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of July, 2012. Specifications and information herein are subject to change without notice.