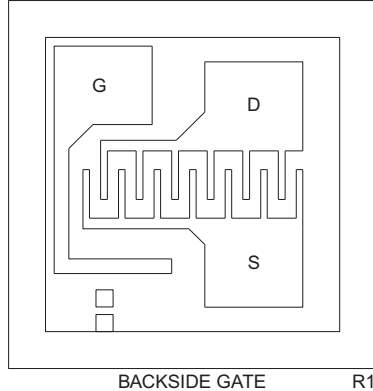


The CP210-2N4416 is a silicon N-Channel JFET designed for VHF amplifier and mixer applications.



MECHANICAL SPECIFICATIONS:

Die Size	15 x 15 MILS
Die Thickness	8.0 MILS
Drain Bonding Pad Size	3.2 x 4.0 MILS
Source Bonding Pad Size	3.2 x 4.0 MILS
Gate Bonding Pad Size	3.2 x 4.0 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 6,000Å
Scribe Alley Width	3.0 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	72,000

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

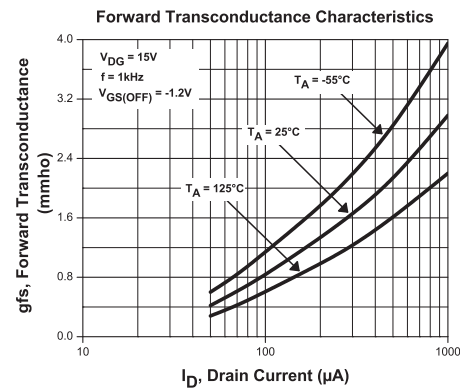
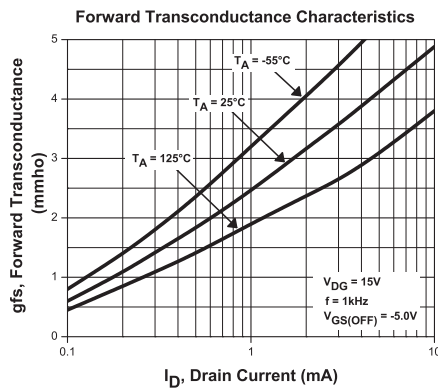
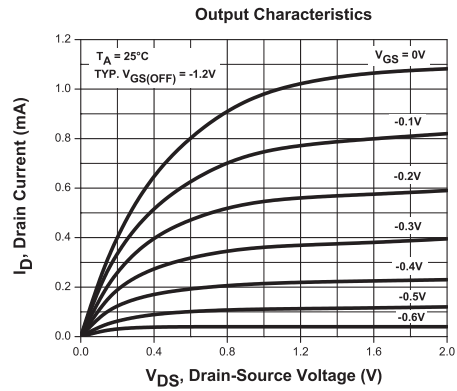
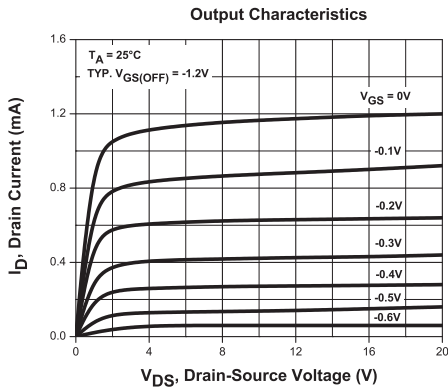
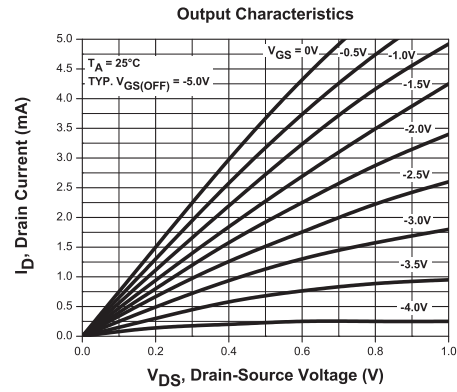
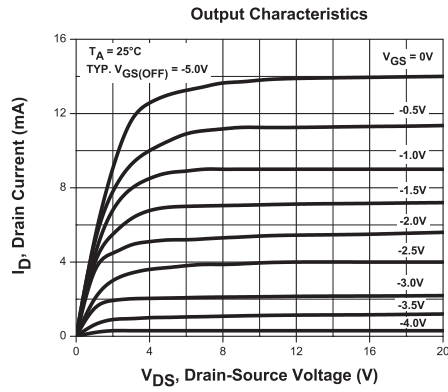
	SYMBOL		UNITS
Gate-Drain Voltage	V_{GD}	30	V
Gate-Source Voltage	V_{GS}	30	V
Drain-Source Voltage	V_{DS}	30	V
Gate Current	I_G	10	mA
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{GSS}	$V_{GS}=20\text{V}, V_{DS}=0$		0.1	nA
I_{DSS}	$V_{DS}=20\text{V}, V_{GS}=0$	5.0	15	mA
BV_{GSS}	$I_G=1.0\mu\text{A}$	30		V
$V_{GS(OFF)}$	$V_{DS}=15\text{V}, I_D=1.0\text{nA}$		6.0	V
gFS	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{kHz}$	4.5	7.5	mS
gOS	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{kHz}$		50	μS
C_{rss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		1.0	pF
C_{iss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		4.0	pF
C_{oss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		2.0	pF
NF	$V_{DS}=15\text{V}, I_D=5.0\text{mA}, R_G=1.0\text{k}\Omega, f=100\text{MHz}$		2.0	dB

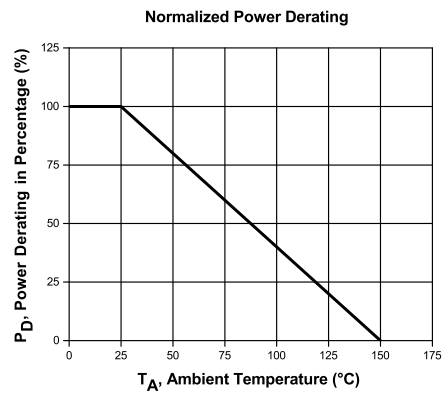
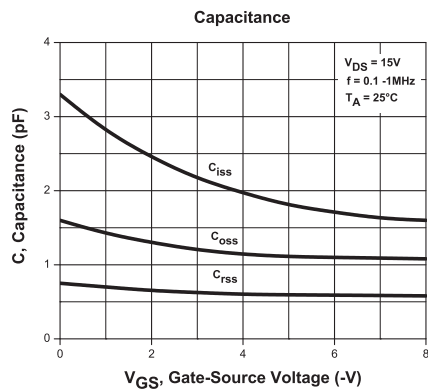
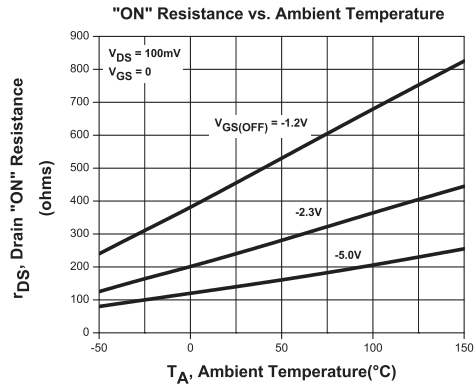
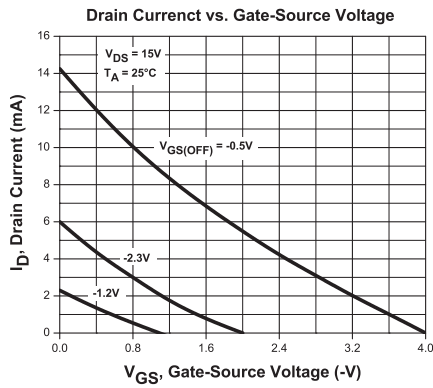
CP210-2N4416

Typical Electrical Characteristics



CP210-2N4416

Typical Electrical Characteristics



R1 (9-February 2016)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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