



PC Board Layout Component Side Shown

| REVISION RECORD | | | | |
|-----------------|-----|-------------|------|------|
| REV | ECO | DESCRIPTION | DRFT | CHKD |
| | | | | |

NOTES:

ELECTRICAL:

- VOLTAGE RATING : 125 VAC RMS.
- CURRENT RATING : 1.5 AMP.
- CONTACT RESISTANCE : 45 MILLIOHMS MAX.
- INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
- DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

- HOUSING MATERIAL : PBT+30%G.F UL94V-0.
- INSETER MATERIAL : PBT+30%G.F UL94V-0.
- CONTACT MATERIAL : PHOSPHOR BRONZE t=0.35mm.
- PLATING : GOLD PLATING OVER NICKEL.
- OPERATING LIFE : 750 CYCLES MIN.
- PCB RETENTION PRE-SOLDER : 1 LB MIN.
- PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

- STORAGE : -40°C TO +85°C.
 - OPERATION: 0°C TO 70°C.
 - WAVE SOLDER TEMPERATURE: 230°C TO 250°C 5~10 SECONDS.
- MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.
CUL FILE NO. E163191
ROHS PART NUMBER: E5688-XSC08X-L

- 1- 3U" 2- 6U"
 - 3- 15U" 4- 30U"
 - 5- 50U"
- SHIELD
2 w/o logo TIN
3 w/o logo NICKEL

| | | | | | | |
|------------------------|----------------------------|----------------------|-------------------------|---|------|-----|
| DETACHED LISTS | MM (INCH) | DFTO ANKUE | DATE 11/25/05 | FULL RISE ELECTRONIC CO., LTD | | |
| | TOLERANCES EXCEPT AS NOTED | CHKDASHELLY | DATE 2005/11/25 | | | |
| | MM | MFO | DATE | TITLE 11.5 SIDE ENTRY PCB JACK 8P (W/TABS) | | |
| | .0 ±0.20 | APPVLLUSHENG | DATE 2005/11/25 | | | |
| .00 ±0.15 | MATERIAL : | QT'Y : | DRAWING NO. GE563135-LC | | SIZE | REV |
| .000 ±0.075 | ANGLES ± | FINISH : | /PART NO. SEE NOTE | | A3 | 0 |
| THIRD ANGLE PROJECTION | SCALE : | DO NOT SCALE DRAWING | SHEET X | | OF Y | |