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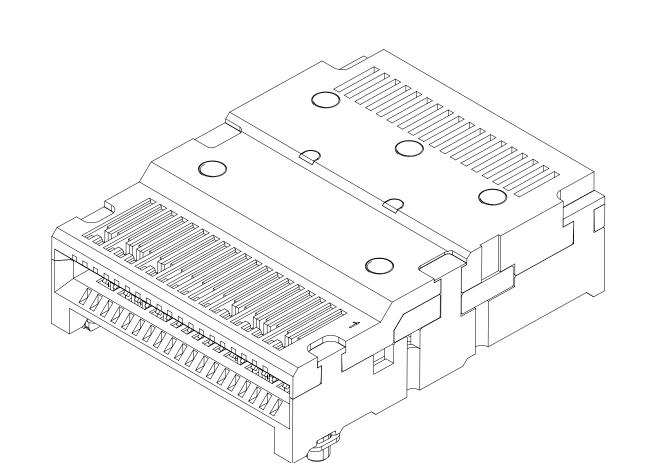
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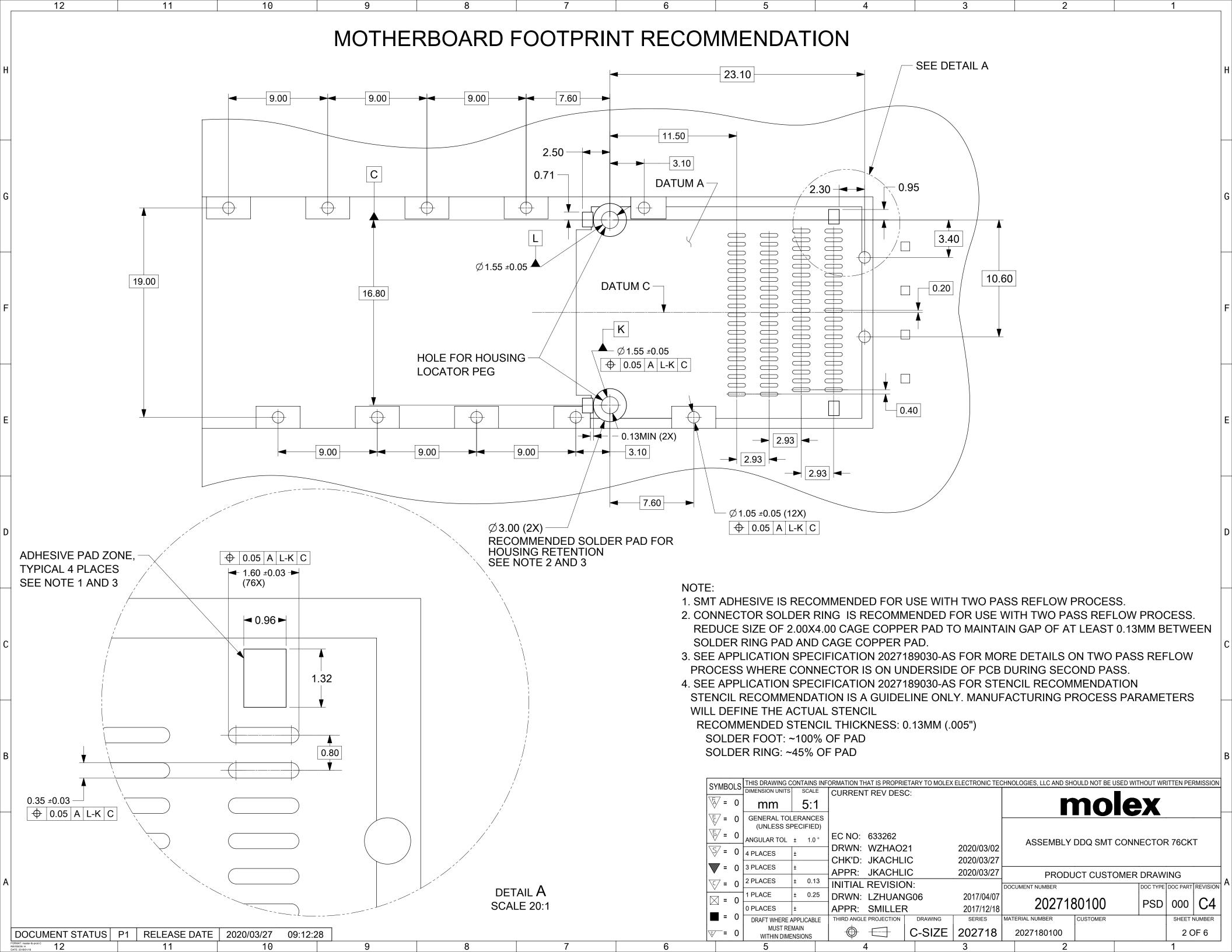
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- 1. MATERIAL: HOUSING - HIGH TEMPERATURE THERMOPLASTIC GLASS FILLED. UL 94V-0. BLACK. **TERMINALS - COPPER ALLOY** SOLDER RING / NAIL: NICKEL SILVER
- 2. PLATING: CONTACT AREA - 0.76um MIN GOLD OVER 2.54um MIN NICKEL SOLDER AREA - 2.54um MIN TIN OVER 2.54um MAX NICKEL
- 3. TERMINAL SOLDER FEET TO BE COPLANAR WITHIN 0.10/.004 MEASURED FROM FRONT HOUSING STAND OFF (DATUM -BB-)
- 4. DATE CODE: 4 DIGIT (3 DIGIT DATE, 1 DIGIT YEAR)
- 5. CIRCUIT IDENTIFIER: SEE APPROPRIATE INDUSTRY SPECIFICATION FOR LOCATION OF PIN 1
- 6. PACKAGED PER PACKING SPECIFICATION: 2027188000
- 7. CONFORMS TO PRODUCT SPECIFICATION: 2027189010-PS
- 8. APPLICATION SPECIFICATION: 2027189030-AS
- 9. THIS PART CONFORMS TO CLASS C REQUIREMENTS OF COSMETIC SPECIFICATION PS-45499-002



sv	MBC		THIS DRAWING C	ONTAINS	SINFORMATION T	HAT IS PROPRIE	ETAR	Y TO MOLEX	ELECTRONIC TEC	CHNC	DLOGIES, LLC AND SH	OULD NOT BE	USED WIT	THOUT WR	ITTEN PER	RMISSION	1
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PIN OUT TABLE

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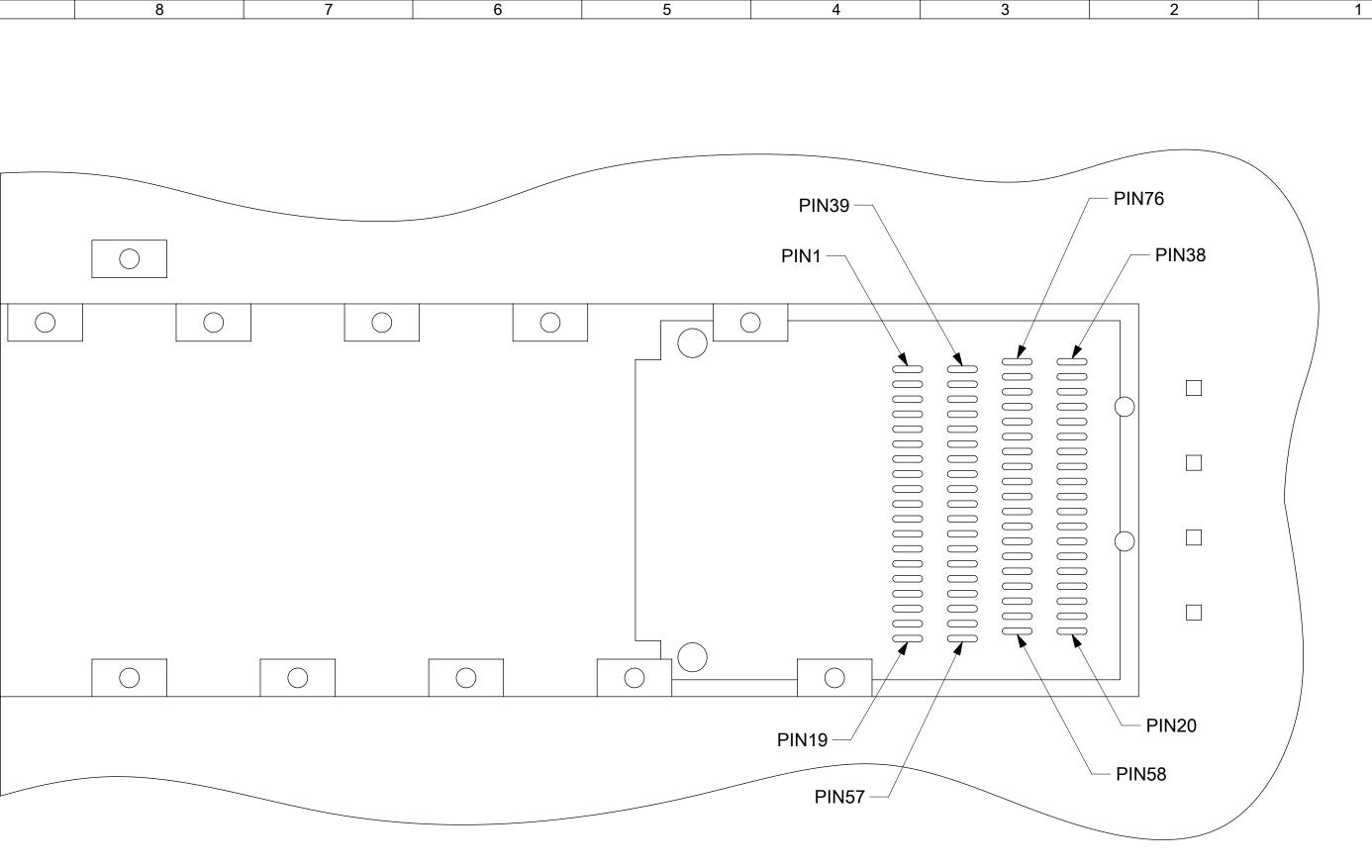
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	Symbol	Description	Not
1 2	GND TX2n	Ground	
2	TX2n	Transmitter Inverted Data Input	
3	TX2p	Transmitter Non-Inverted Data Input	
4	GND	Ground	
5	TX4n	Transmitter Inverted Data Input	
6	TX4p	Transmitter Non-Inverted Data Input	
7	GND	Ground	
8	ModSelL	Module Select	
9	ResetL	Module Reset	
10	VCC RX	+3.3V Power Supply Receiver	
11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	
14	RX3p	Receiver Non-Inverted Data Output	
15	, RX3n	Receiver Inverted Data Output	
16	GND	Ground	
17	RX1p	Receiver Non-Inverted Data Output	
18	RX1n	Receiver Inverted Data Output	
19	GND	Ground	
20	GND	Ground	
20	RX2n		
		Receiver Inverted Data Output	
22	RX2p	Receiver Non-Inverted Data Output	
23	GND	Ground	
24	RX4n	Receiver Inverted Data Output	
25	RX4p	Receiver Non-Inverted Data Output	
26	GND	Ground	
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	VCC TX	+3.3V Power supply transmitter	
30	VCC1	+3.3V Power supply	
31	LPMode	Low Power Mode	
32	GND	Ground	
33	TX3p	Transmitter Non-Inverted Data Input	
34	TX3p	Transmitter Inverted Data Input	
<u>34</u> 35	GND	Ground	
35 36	TX1p	Transmitter Non-Inverted Data Input	
36 37	TX1p TX1n	· · ·	
	-	Transmitter Inverted Data Input	
38	GND	Ground	
39	GND	Ground	
40	TX6n	Transmitter Inverted Data Input	
41	ТХ6р	Transmitter Non-Inverted Data Input	
42	GND	Ground	
43	TX8n	Transmitter Inverted Data Input	
44	TX8p	Transmitter Non-Inverted Data Input	
45	GND	Ground	
46	TBD	For future use	
47	TBD	For future use	
48	VCC	+3.3V Power supply	
49	TBD	For future use	
50	TBD	For future use	
51	GND	Ground	
52	RX7p	Receiver Non-Inverted Data Output	
52	RX7n	Receiver Inverted Data Output	
<u>53</u> 54	GND	Ground	
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55	RX5p	Receiver Non-Inverted Data Output	
56	RX5n	Receiver Inverted Data Output	
57	GND	Ground	
58	GND	Ground	
59	RX6n	Receiver Inverted Data Output	
60	RX6p	Receiver Non-Inverted Data Output	
61	GND	Ground	
62	RX8n	Receiver Inverted Data Output	
63	RX8p	Receiver Non-Inverted Data Output	
64	GND	Ground	
65	NC	No Connect	
66	TBD	For future use	
67	VCC	+3.3V Power supply	
68	VCC	+3.3V Power supply	
69	TBD	For future use	
<u>89</u> 70	GND	Ground	
71	TX7p	Transmitter Non-Inverted Data Input	
72	TX7n	Transmitter Inverted Data Input	
73	GND	Ground	
74	TX5p	Transmitter Non-Inverted Data Input	
75	TX5n	Transmitter Inverted Data Input	
76	GND	Ground	



<u>Notes:</u>
* Reference only.

** Please align QSFP-DD MSA for pin mapping

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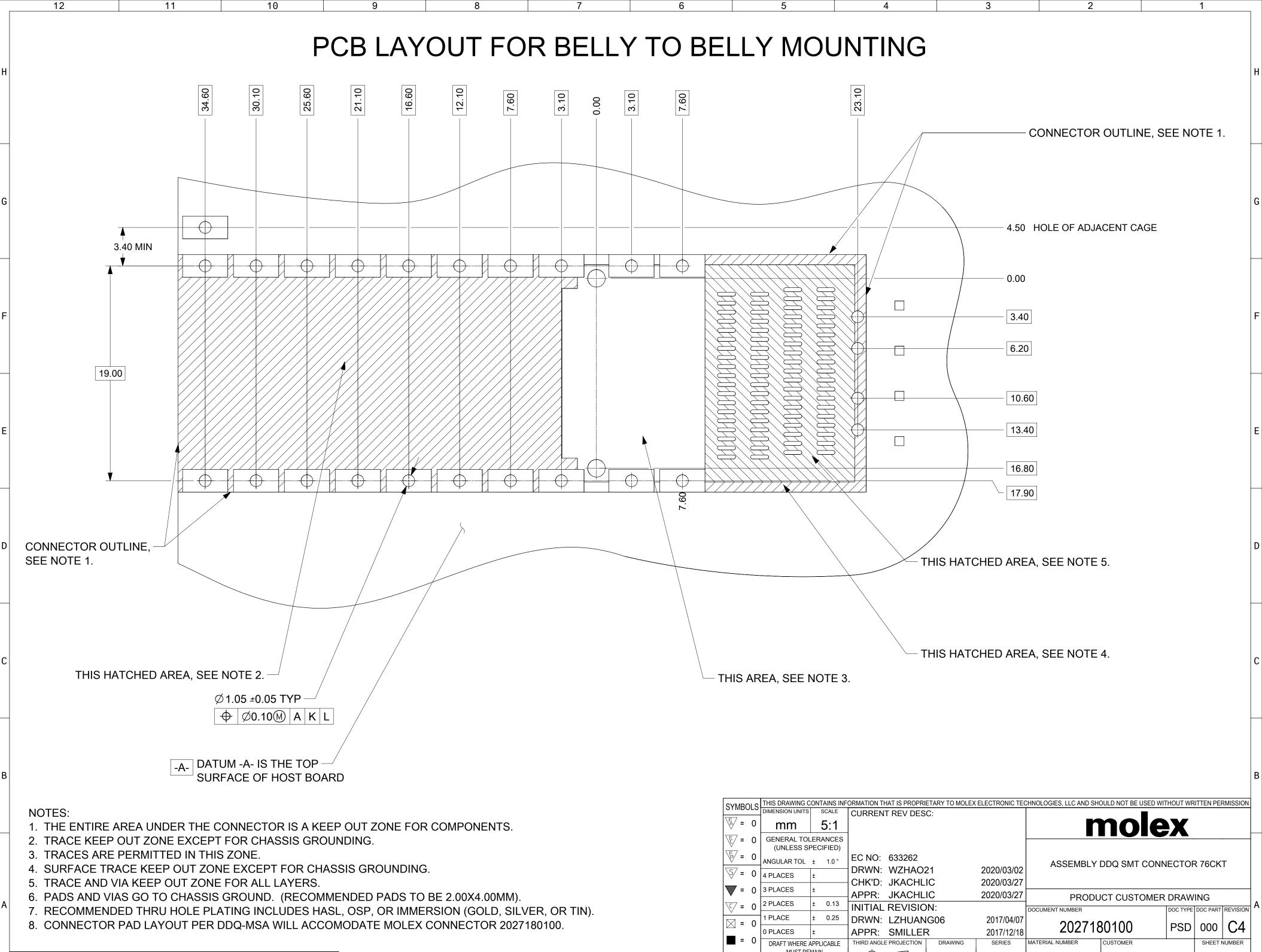
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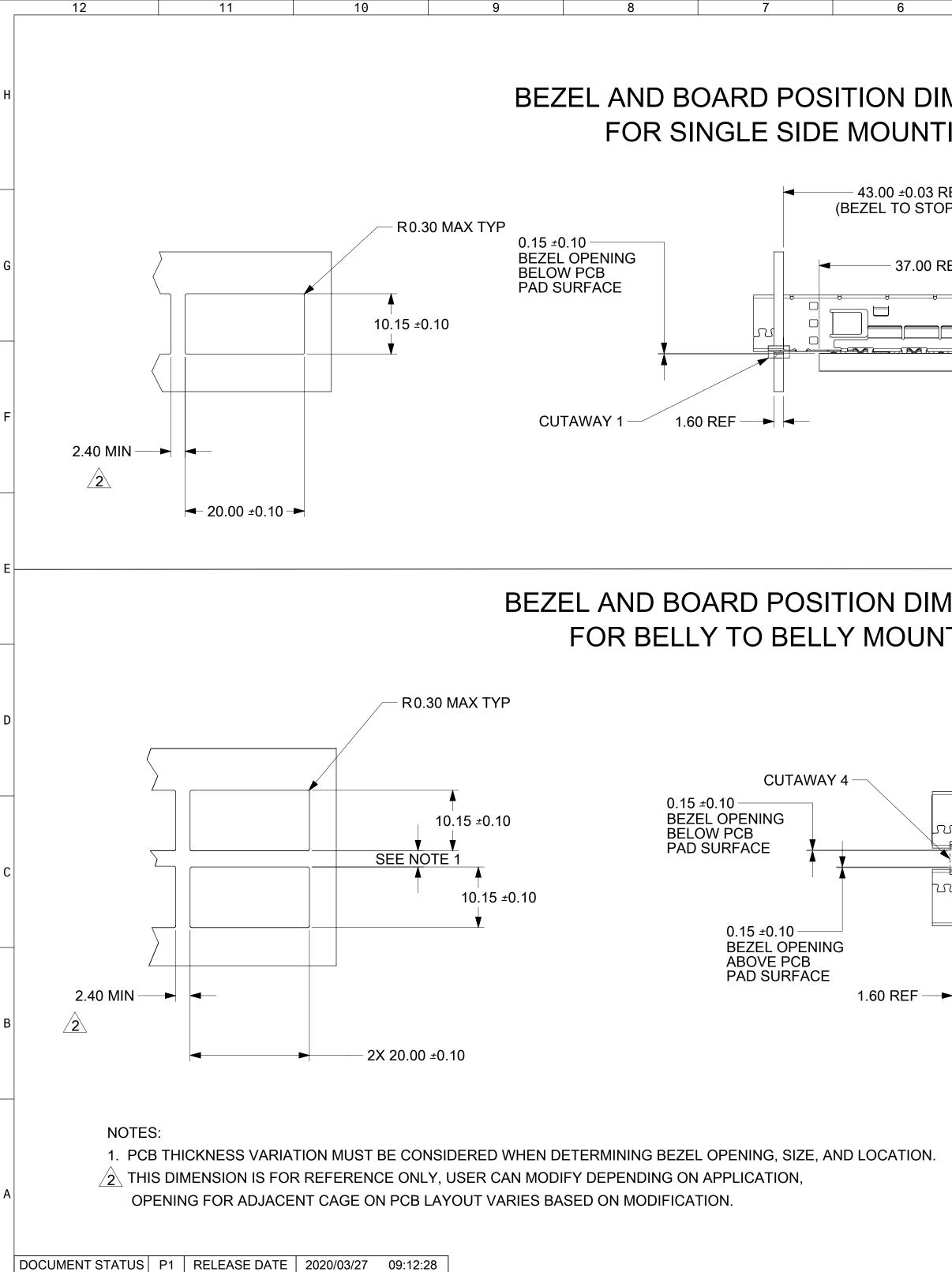
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DOCUMENT STATUS	P1	RELEASE DATE	2020/03/27	09:12:28				
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MENSIONS FING	H
REF DPPER) CUTAWAY 2 REF	G
9.77 REF (TOP OF CAGE TO PAD SURFACE)	F
MENSIONS ITING	- E
2X 43.00 ±0.03 (BEZEL TO STOPPER) 2X 37.00 REF CUTAWAY 3	D
2X 9.77 REF (TOP OF CAGE TO PAD SURFACE)	С
SYMBOLS THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX ELECTRONIC TECHNOLOGIES, LLC AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION DIMENSION UNITS SCALE CURRENT REV DESC:	B
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2020/03/27

2017/04/07

2017/12/18

SERIES

202718

DRAWING

C-SIZE

DOCUMENT NUMBER

MATERIAL NUMBER

2027180100

2027180100

CUSTOMER

PRODUCT CUSTOMER DRAWING

DOC TYPE DOC PART REVISION

PSD 000 C4

1

SHEET NUMBER

5 OF 6

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2 PLACES

1 PLACE

0 PLACES

DRAFT WHERE APPLICABLE

MUST REMAIN

WITHIN DIMENSIONS

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APPR: JKACHLIC

INITIAL REVISION:

APPR: SMILLER

THIRD ANGLE PROJECTION

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DRWN: LZHUANG06

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REVISION	DATE	DISCRIPTION
1	04/07/2017	INITIAL CONCEPT
2	09/27/2017	ADDED SHEET SHOWING VIEWS OF BOTH SINGLE SIDE AND BELLY TO BELLY MOUNTING, ADDED SOLDER PAD FOR HOUSING RETENTION, AND ADHESIVE PADS
2	09/27/2017	ADDED EXPLODED VIEWS
А	12/06/2017	INITIAL PROPOSAL - (ER)
A1	12/18/2017	INITIAL RELEASE (NEW PRODUCT)
В	12/21/2017	UPDATE VIEWS
B1	02/02/2018	UPDATE VIEWS, ADD PK AS PS SPEC, ADD ADHESIVE PADS NOTE, CORRECT BELLY TO BELLY PCB LAYOUT
B2	4/11/2018	ALIGN DIMENSIONAL CALLOUTS TO MATCH MSA
С	4/25/2018	ADD TWO PASS REFLOW PROCESS NOTE
C1	10/23/2018	DRAWING CLEANUP: SH04 SIZE C WAS D. SH05 SIZE C WAS D.
C2	01/15/2019	SH04/B12,ADDEDTRACEKEEPOUTNOTES1THRU5;SH04/C3,CHANGEDUHATCHEDREGIONSURFACETRACEPERMISSION;SH04,ADDEDNEWTRACEKEEPOUTREGIONS.
C3	02/13/2020	UPDATE SHEET 1 HEAT STAKED
C4	03/02/2020	1.ADD SOLDER RING NOTCH ;
04	03/02/2020	2.UPDATE HEAT STAKE POST FINAL STATUS.

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ACEPERMISSION;SH04,ADDEDNEWTRACEKEEPOUTREGIONS.	

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