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Jameco Part Number 668529



PRODUCT SPECIFICATION



LANGUAGE

ENGLISH

1.0 SCOPE

This specification covers the 1.27mm (.050 inch) centerline DIMM socket board to board interconnect for 1.27+/-0.10 (.050+/-0.004 in.) thick memory modules.

2.0 PRODUCT DESCRIPTION

2.1 PRODUCT NAME AND PART NUMBER

Product Name

Dual In-Line Memory Module (DIMM)

Series Numbers

71243, 71251, 71481, 71729, 71736, 73705, 73817, 73818, 73822, 74080, 74081, 70482 Series.

2.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKINGS

See the appropriate Sales Drawings for information on dimensions, materials, platings and markings, recommended module outlines and footprint patterns.

2.3 UL/CSA CERTIFICATION

UL file: E29179

CSA file: LR-19980A-366

3.0 APPLICABLE DOCUMENTS AND SPECIFICATIONS

See the Sales Drawings and the other sections of this Specification for the necessary referenced Documents and Specifications.

4.0 RATINGS

4.1 VOLTAGE 100 Volts AC (RMS)/DC

4.2 CURRENT 1.0 Amps

4.3 TEMPERATURE

Operating: - 40 °C to + 85 °C

Nonoperating: - 55 °C to + 85 °C

	REVISE ON PC ONLY		TITLE 1.27 mm (.050 IN.) PITCH DUAL IN-LINE MEMORY MODULE CONNECTOR		
	C	SEE SHEET 1			
	REV	DESCRIPTION	THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
DOCUMENT NO. PS-71243-9999			DIMENSION CLASS: CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM	SHEET 2
ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM					



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5.0 PERFORMANCE

5.1 ELECTRICAL PERFORMANCE

Item	Test Condition	Requirement
Contact Resistance (Low Level)	Mate Connectors with a maximum voltage of 20mV and a current of 100 mA. (Measurement Locations in Section 8.3)	30 milliohm Maximum Initial
Insulation Resistance	Mate Connectors with a voltage of 500 VDC between adjacent terminals and between terminals and ground.	1000 Megohms Minimum
Dielectric Withstanding Voltage	Mate Connectors with a voltage of 500 VAC for 1 minimum between adjacent terminals and between terminals and ground.	No breakdown
Capacitance	Measure between adjacent terminals at 1 MHz. (Loaded: 50 ohms impedance)	Loaded: 2 picofarad maximum. Unloaded: 0.5 picofarad maximum.

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REV	DESCRIPTION		
DOCUMENT NO. PS-71243-9999		DIMENSION CLASS: C CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM
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5.1 ELECTRICAL PERFORMANCE CONTINUED

5.1.1 Characteristics: Loaded condition (Module inserted)

	1:1 S:G	Configuration	3:1 S:G	Configuration
PARAMETER	Average	Range	Average	Range
Capacitance(pF)	1.06	nominal	1.05	1.01-1.06
Inductance(nH)	3.18	nominal	3.65	3.45.-3.94
Propagation Delay (psec)	57.5	nominal	59.3	58.3-60.6
Risetime Degradation (psec)	16.41	NA	28.96	NA
Bandwidth (GHz)	21.3	NA	12.1	NA
Impedance (ohms) @ 45 psec	54.4	nominal	58.3	nominal
% Crosstalk (1v/1ns)	N.E.	F.E.	N.E.	F.E.
1 Drive	0.33%	-0.28%	1.26%	-0.75%
4 Drives	0.90%	-0.82%	3.33%	-2.35%
7 Drives	1.10%	-0.92%	4.57%	-3.50%

5.1.2 Characteristics: Unloaded condition

	1:1 S:G	Configuration	3:1 S:G	Configuration
PARAMETER	Average	Range	Average	Range
Capacitance (pF)	0.36	nominal	0.34	0.29-0.36

Note: All data is based on analytical analysis. Contact Molex Inc. Corporate Headquarters for Spice model and additional information..

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			SHEET 4
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5.2 MECHANICAL PERFORMANCE

Item	Test Condition	Requirement
Total Insertion and Withdrawal Force (excl. latches)	Insert and withdraw a steel blade at a rate of 25 +/- 6mm (1+/- 1/4 inch) per minute. Latches shall be excluded in the test. (Gage dimensions in Section 8.1)	Insertion force shall be 0.78 N (2.8 oz) maximum with maximum blade, and withdrawal 0.07 N (0.25 oz) minimum. with minimum. blade per contact respectively x the total contact population
Total Insertion Force (w/ latches)	Insert a nominal thick PCB w/o chamfer at a rate of 25 +/- 6mm (1+/- 1/4 inch) per minute. Latches shall be included in the test.	Maximum Insertion force shall be 0.83 N (3.3 oz) max. per contact respectively x the total contact population
Terminal Retention Force in Housing	Axial pullout force on the terminal in the housing at a rate of 25 +/- 6mm (1 +/- 1/4 inch) per minute.	Contact : 4.45 N (1.0 lbs.) minimum Forklock : 22.24 N. (5.0 lbs) minimum.
Durability	Mate connectors up to 25 cycles at a maximum rate of 10 cycles per minute prior to defined Environmental Tests.	Contact Resistance : 10 milliohms Maximum Change from Initial
Vibration Mil-Std-1344 Method 2005.1 Condition I	Amplitude: 1.50mm (.060 inch) peak to peak Sweep: 10-55-10 Hz in one minute Duration: 2 hours in each X-Y-Z axis. (Test module shall be per Section 8.2)	Contact Resistance: 10 milliohms Maximum Change from Initial Discontinuity: not greater than one microsecond
Mechanical Shock Mil-Std-1344 Method 2004.1 Condition A	50 g's with three 1/2 sine waveform shocks in each X-Y-Z axis. (Test module shall be per Section 8.2)	Contact Resistance: 10 milliohms Maximum Change from Initial Discontinuity: not greater than one microsecond
Latch Overstress Force	Apply an actuation force on the latch at a rate of 25 +/- 6mm (1 +/- 1/4 inch) per minute in the fully open position and hold for 10 sec.	66.72 N (15 lbs) minimum. force held for 10 sec. , no damage.
Latch Actuation Force	Apply an actuation force on the latch at a rate of 25 +/- 6mm (1 +/- 1/4 inch) per minute with recommended test module inserted into connector.	The force to fully actuate the latch open shall be 44.48 N (10 lbs) maximum per latch.
Normal Force	Apply a perpendicular force at a rate of 25+/- 6mm (1 +/- 1/4 inch) per minute on the contacts in a manner simulating actual use.	0.49 N (50 grams) minimum end of life .

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REV	DESCRIPTION		
DOCUMENT NO. PS-71243-9999	DIMENSION CLASS: C CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM	SHEET 5
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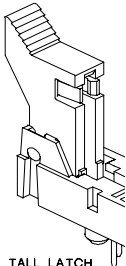
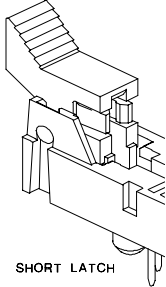
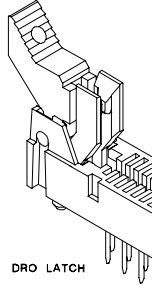
PRODUCT SPECIFICATION



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5.2 MECHANICAL PERFORMANCE CONTINUED

Item	Test Condition	Requirement
Module Ripout Force	Pull up from the center of the module with the latches closed at a rate of 25 +/- 6mm (1 +/- 1/4 inch) per minute.	<div style="display: flex; justify-content: space-around;">   </div> <p>The module ripout forces is 88.96 N (20 lbs) minimum for a DIMM connector that has a tall or short latch at both ends. See figures above.</p> <hr/>  <p>The module ripout forces is 35.58 N (8 lbs) minimum for a DIMM connector that has a DRO latch at both ends, and the tower options that have only one tall or short latch and a tower at the other end. See</p>

REVISE ON PC ONLY		TITLE		
C	SEE SHEET 1	1.27 mm (.050 IN.) PITCH DUAL IN-LINE MEMORY MODULE CONNECTOR		
REV	DESCRIPTION	THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
DOCUMENT NO. PS-71243-9999		DIMENSION CLASS: C CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM	SHEET 6
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5.2 MECHANICAL PERFORMANCE CONTINUED

Item	Test Condition	Requirement
Normal Force	Apply a perpendicular force at a rate of 25+/- 6mm (1+/- ¼ inch per minute on the contacts in a manner simulating actual use.	0.49 N (50 grams) minimum end of life.
Retention of connector to PCB	Pull connector out of max. recommended diameter holes. PCB: 1.57+/-0.18 mm (.062+/-0.007 inch) thick. Rate: 25.4+/-6 mm (1.0+/-1/4 inch) per minute.	The connector shall not lift off the PCB when pulling up either end with a force of 4.45 N (1.0 lbs) minimum. 22.24 N (5 lbs) max.
Insertion force of connector into PCB	Push connector into min. recommended diameter holes. PCB: 1.57+/-0.18 mm (.062+/-0.007 inch) thick. Rate: 25.4+/-6mm (1.0+/-1/4inch) per minute.	Plastic peg: 44.48 N (10.0 lbs.) per peg maximum. Metal forklock: 26.69 N (6.0 lbs) per peg maximum.
Durability of marking	Brush connector per Mil-Std-202, Method 215 with isopropyl alcohol solution.	No degradation of the marking

5.3 ENVIROMENTAL PERFORMANCE

Item	Test Condition	Requirement
Thermal Shock Mil-Std-202F Method 107 E	Mate connectors exposed to 5 cycles of: <u>Temperature °C</u> <u>Duration (Min)</u> -40 +0/-3 15 +25 +/-10 5 Max +65 +3/-0 15 +25 +/-10 5 Max	Appearance: No Damage Contact Resistance: 10 milliohms maximum change from initial
Thermal Aging Mil-Std-202F Method 108	Mated connector exposed to 240+/-10 hrs. at 85+/- 3° C	Appearance: No Damage Contact Resistance: 10 milliohms maximum change from initial
Humidity (Steady State) Mil-Std-202F Method 103	Mated connectors exposed to a temperature of : 50 +/- 2°C with a Relative of 80+/-3% for 300 hours. Remove surface moisture and air dry for 24 hours prior to measurements.	Appearance: No Damage Contact Resistance: 10 milliohms maximum change from initial. Dielectric Withstanding Voltage: No Breakdown Insulation Resistance: 1000 Megohms Minimum

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DOCUMENT NO. PS-71243-9999		DIMENSION CLASS: C CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM
			SHEET 7
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5.3 ENVIROMENTAL PERFORMANCE CONTINUED

Item	Test Condition	Requirement
Temperature Cycling	Mated connectors exposed for 335 cycles Relative Humidity uncontrolled with a temperature transition of 10°C per minute. <u>Temperature °C</u> <u>Duration (Min)</u> +0 +/- 3 15 +75 +/-3 15 Allow to air dry for 24 hours prior to measurements.	Appearance: No Damage Contact Resistance: 10 milliohms maximum change from initial. Dielectric Withstanding Voltage: No Breakdown Insulation Resistance: 1000 Megohms Minimum
Temperature Rise	Mate the connectors, series 4 contacts and measure the temperature rise at the rated current after 4 hours. (Schematic per Section 8.3)	Maximum Temperature Rise: 30°C above ambient
Solderability Molex SMES-152	Steam age 1 hr. Solder time 5 +/- 0.5 seconds. Solder temperature: 245 +/- 5°C Nonactivated flux.	95% of the immersed area must show no voids, pin holes
IR Process	Exposure: Molex IR Profile per Section 8.4	Appearance: No damage, blister- ing or solder bridging. Dimensional: Conformance to Sales Drawing requirements
Flowing Mixed Gas (FMG)	Battelle Class II, 10 ppm Cl ₂ , 10 ppm H ₂ S, 100 ppm NO ₂ , 70+/-1% R.H., 25 deg. C. 50-60 CFM, 10 days mated and 7 days unmated exposure.	Contact Resistance: 10 milliohms Maximum change from Initial
Resistance to Solder Heats	Solder Time 3 +/- 0.5 seconds Solder Temperature: 260 +/- 5°C Immerse leads to a depth of 1.57mm (.062 in.) from connector body.	Appearance: No damage or discoloration of connector materials.

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	DESCRIPTION		
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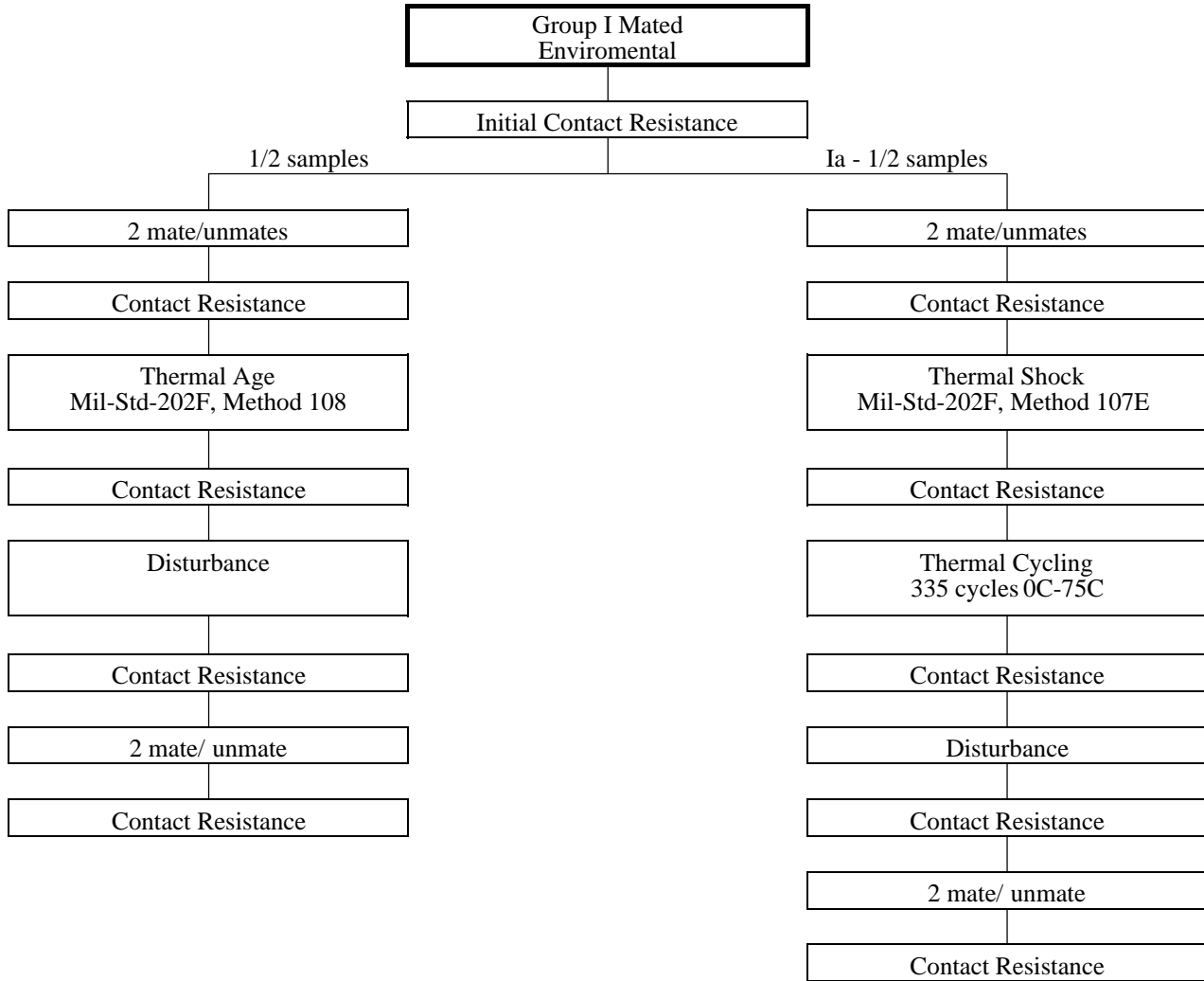


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6.0 TEST SEQUENCE AND QUALIFICATION

Note: Disturbance consists of a 10 degree rotation of the module in the connector.

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			SHEET 9
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Group II
Environmental

IIa

- 1/2 Unmated Connectors
(Do not expose modules)
- Initial Contact Resistance
- 7 days FMG Class II unmated
- 1 mate
- Contact Resistance
- 3 mate/unmates
- Contact Resistance

- 1/2 Mated
- Initial Contact Resistance
- 25 mate/unmate cycles
- Contact Resistance
- 10 days FMG Class II mated
- Contact Resistance
- 1 mate / unmate cycle
- Contact Resistance

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C	SEE SHEET 1		
REV	DESCRIPTION	THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	
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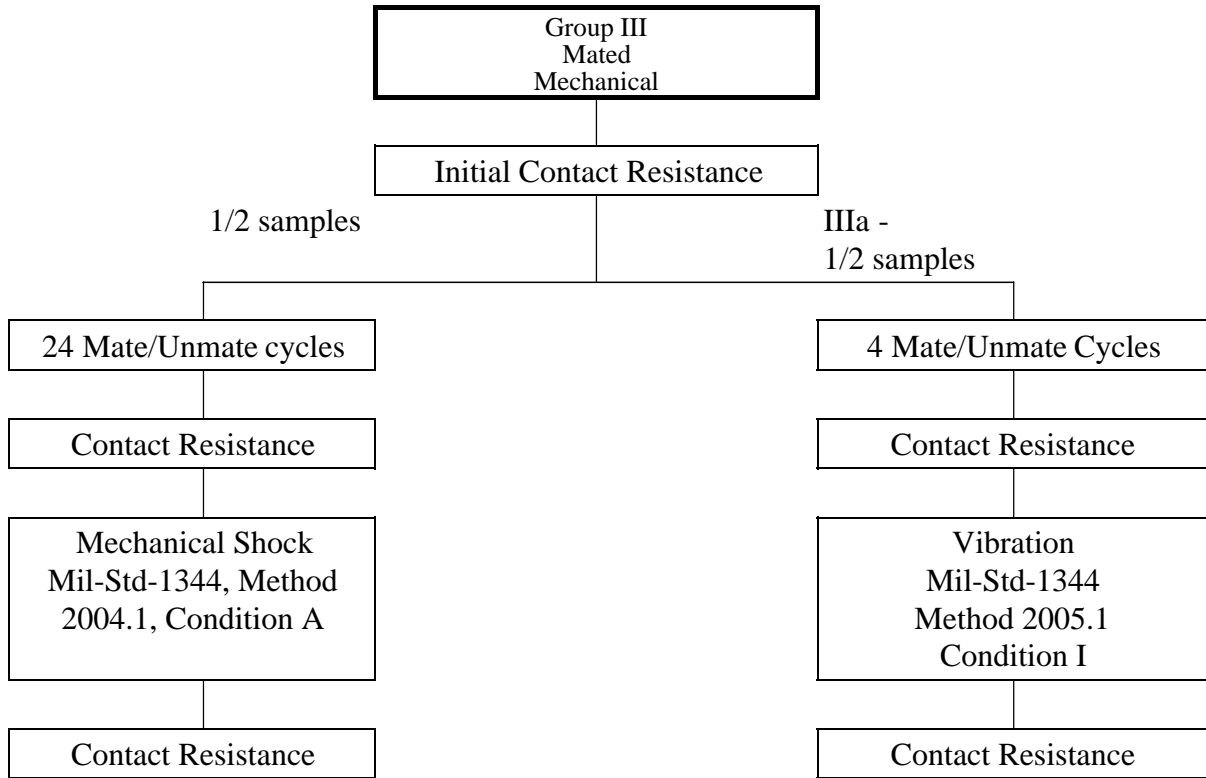


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REVISE ON PC ONLY		TITLE 1.27 mm (.050 IN.) PITCH DUAL IN-LINE MEMORY MODULE CONNECTOR	THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION
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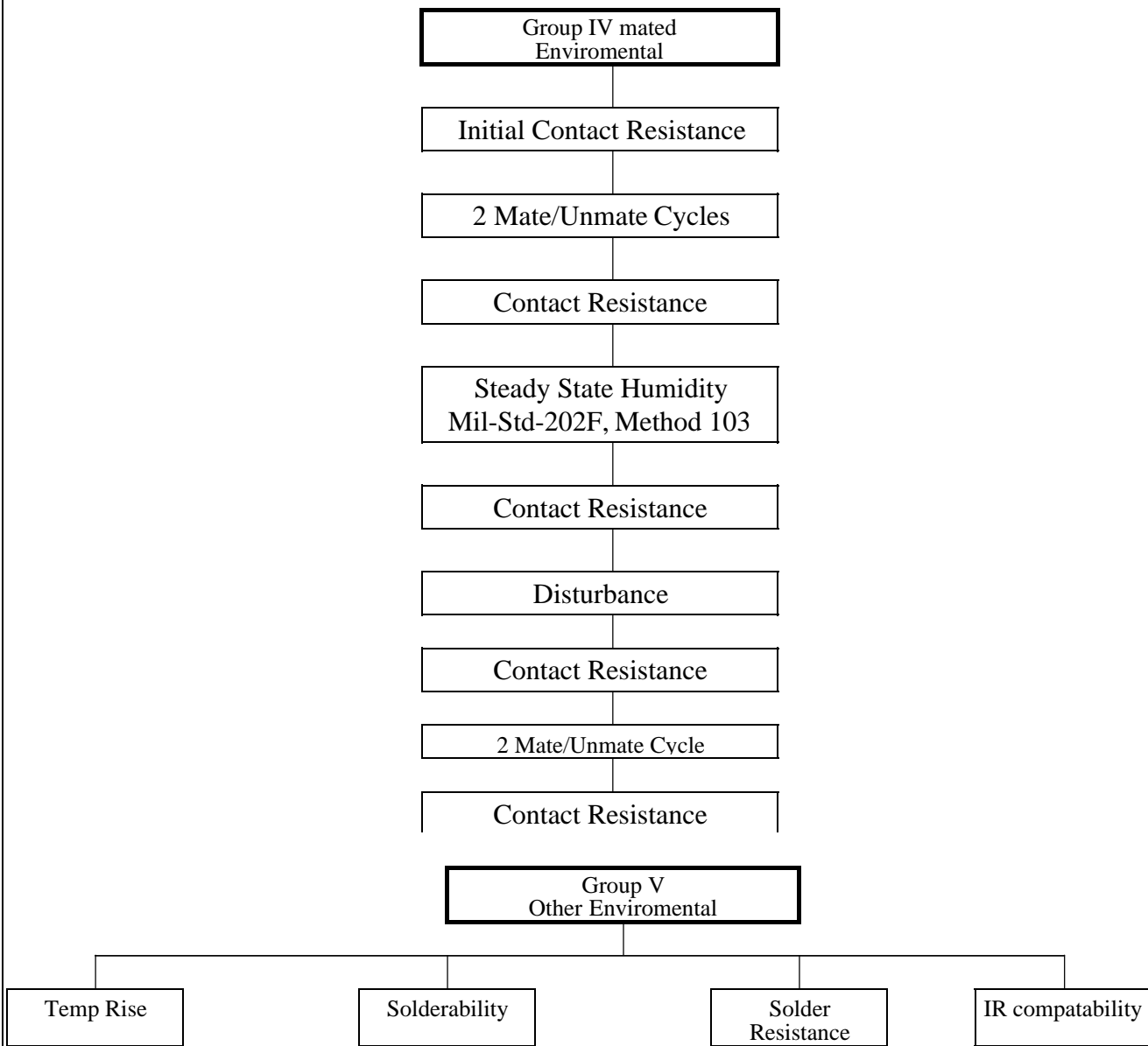


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Note: Disturbance consists of a 10 degree rotation of the module in the connector.

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			SHEET 12
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Group VI - Mechanical

V1a

Connector Mate/Unmate Forces (1,2,5,25 cycles)

V1c

Latch Actuation Force

Module Ripout Force

V1d

Connector Insertion and Retention to PCB

Contact Retention

Durability of Marking

Latch Overstress Force

V1b

Normal Forces

Measure Initial Gaps

5 Mate/Unmates

Measure Gaps

85 C / 240hrs Thermal Aging

Measure Gaps

Derive Normal Forces use spring rate and gap

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			SHEET 13
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6.1 QUALIFICATION REQUIREMENTS

6.1.1 Samples shall be taken from approved production processes.

6.1.2 A minimum of 1000 contact points and 5 connectors per group shall be tested typically from the smallest and largest circuit size.

6.1.3 Acceptance criteria shall be as defined in the applicable test requirement in sections 5.1 - 5.3.

7.0 PACKAGING

7.1 METHOD

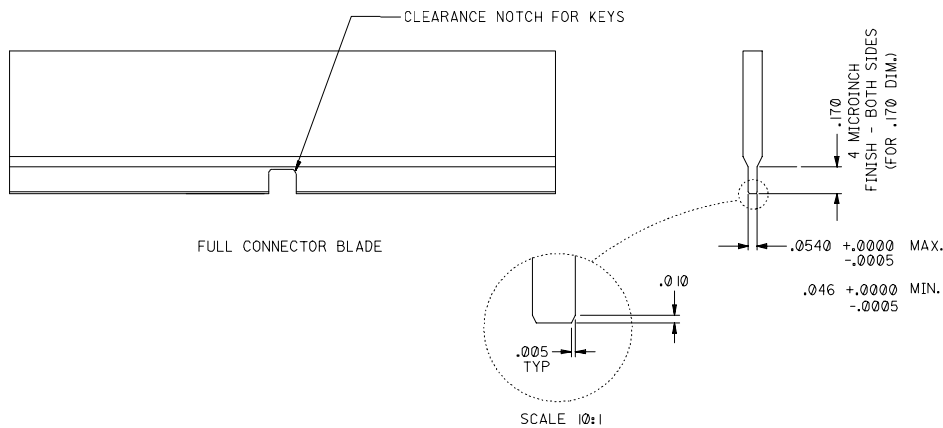
7.1.1 Product shall be packaged in trays per the packaging specification as called out on the applicable assembly print.

7.2 REQUIREMENTS

7.2.1 Packaging shall meet the requirements and be tested per Molex specification PK-70180-5001.

8.0 GAGES, FIXTURES AND SCHEMATICS

8.1 Contact Insertion and Withdrawal Blades



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REV	DESCRIPTION			
DOCUMENT NO. PS-71243-9999		DIMENSION CLASS: C CRITICAL = 0 MAJOR = 0	FILE NAME PS71243.SAM	SHEET 14
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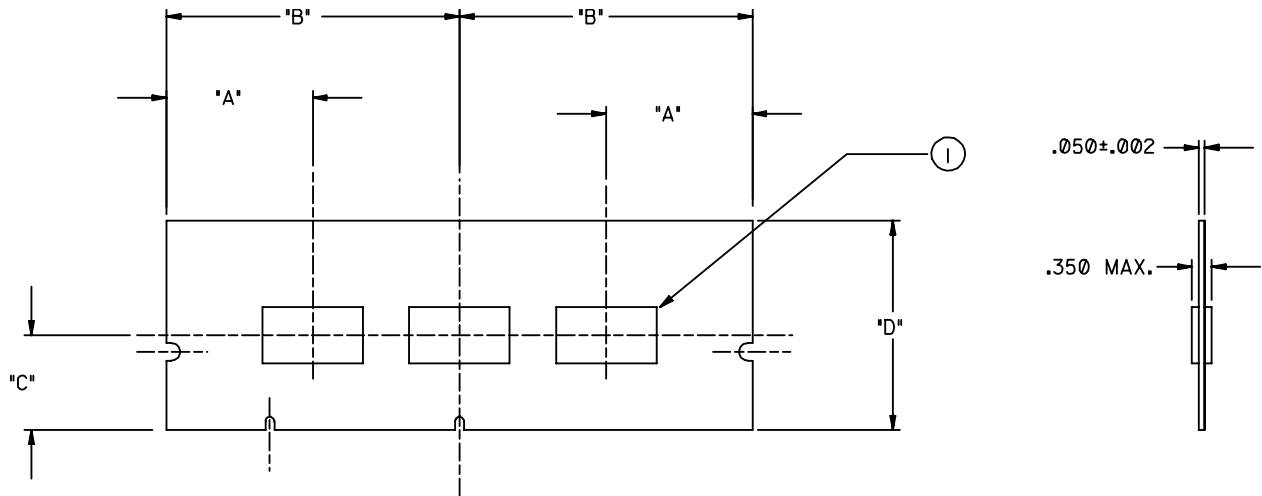


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CIRCUIT SIZE	JEDEC MODULE OUTLINE	"A" mm(in.)	"B" mm(in.)	"C" mm(in.)	"D" mm(in.)	SOJ WEIGHT (gm+/-10%)	
						SHOCK TEST (weighted)	VIBRATION TEST (unweighted)
112	MO-172	21.91(.863)	48.89(1.925)	21.97(.865)	38.10(1.5)	18.00	10.10
128	MO-167	24.45(.963)	48.90(1.925)	21.97(.865)	25.40(1.0)	14.05	6.73
136	NA	25.72(1.013)	51.44(2.025)	21.97(.865)	25.40(1.0)	15.74	7.08
144	NA	26.99(1.063)	53.98(2.125)	21.97(.865)	25.40(1.0)	17.62	7.43
160	NA	29.53(2.325)	59.06(2.325)	21.97(.865)	25.40(1.0)	22.11	8.13
168	MO-161	30.80(1.213)	61.60(2.425)	21.97(.865)	38.10(1.5)	24.76	13.77
200	MO-172	35.88(1.413)	76.83(3.025)	21.97(.865)	38.10(1.5)	38.96	15.87

8.2 Shock and Vibration Test Modules

Notes

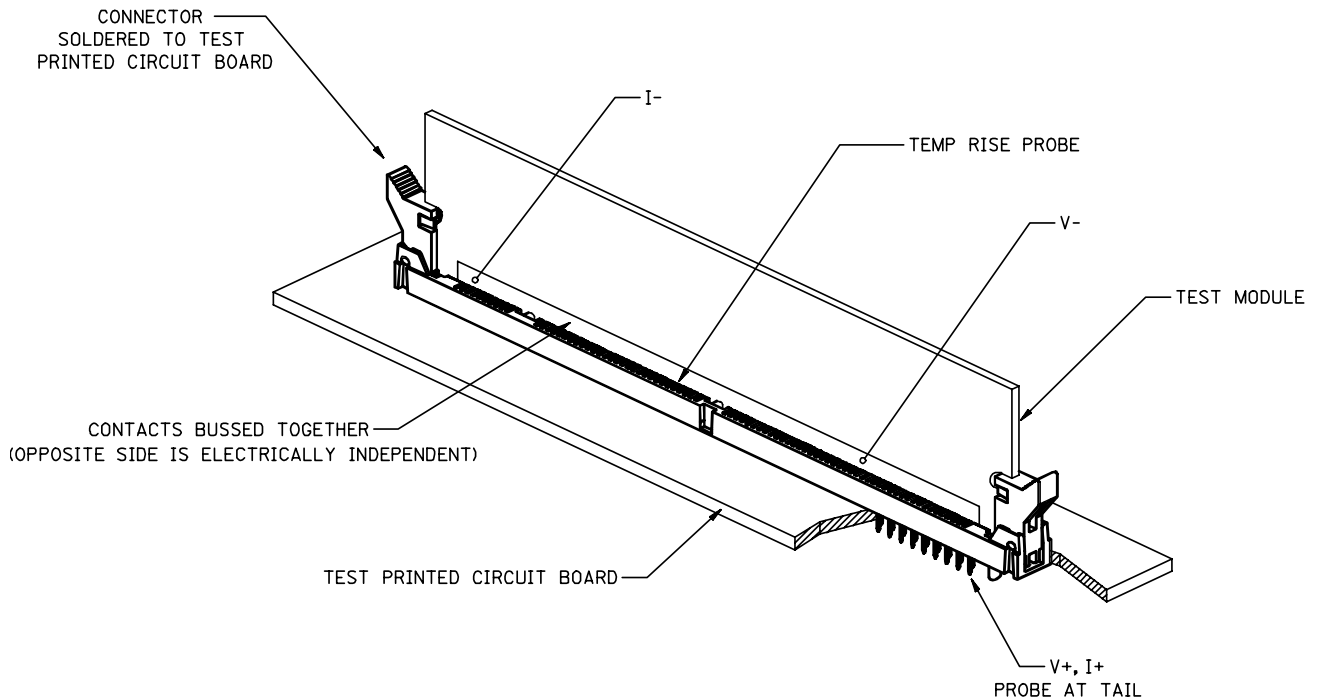
- Item 1 (weights) shall beepoxied to recommended module test board. Material shall be aluminum.
- Total weight of finished test module shall be per the table.

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ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM			



8.3 Contact Resistance and Temperature Rise Set-up.

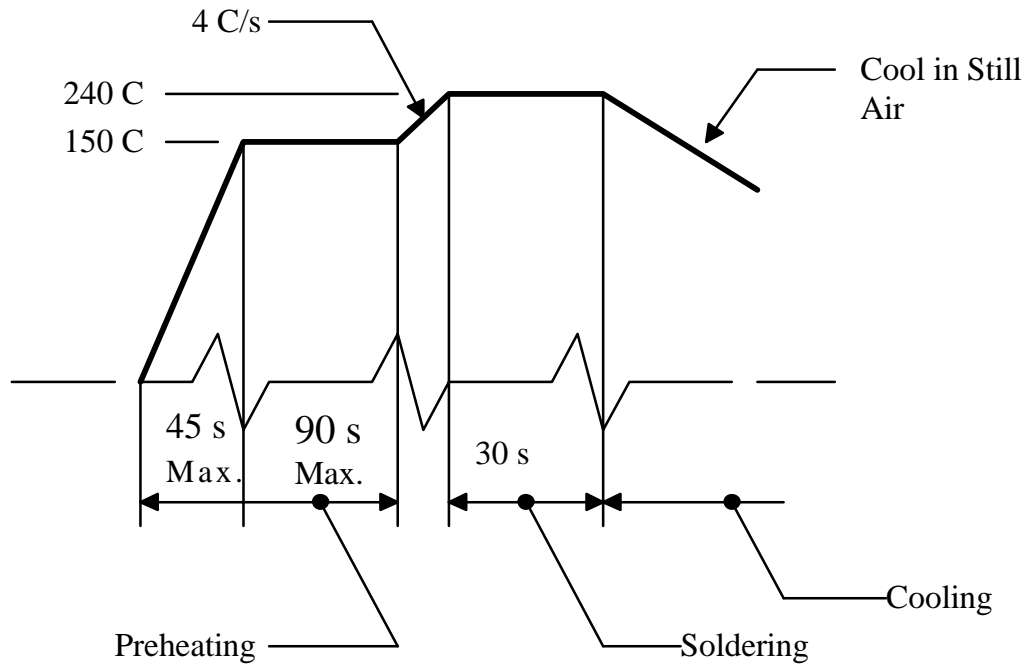
CONTACT RESISTANCE TEST ARRANGEMENT AND TEMP RISE MEASUREMENT LOCATION



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C	SEE SHEET 1		
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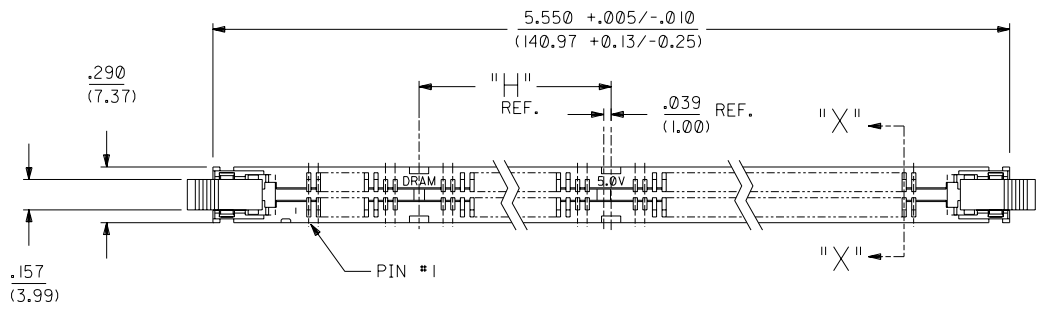
8.4 Reflow Solder Profile



NOTES:

1. Reflow solder Preheat at 3 C/s to 150 C.
2. Reflow at 240 C using 60/40 solder for 30 s per figure.
3. Component must withstand (2) reflow solder cycles with a cool down between.

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ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM				

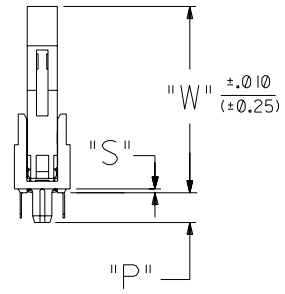


- NOTES:
- CARD SLOT ACCEPTS $.050 \pm .004$ (1.27 ± 0.10) MODULE THICKNESS. (MEASURED OVER P.C. PADS).
 - ALL PEGS ARE INTERFERENCE FITS TO PCB UNLESS NOTED ON THE DWG.
 - REFER TO PRODUCT SPEC PS-71243-9999 FOR PERFORMANCE SPECIFICATIONS.
 - PRODUCT IS PACKAGED IN TRAYS.
 - RECOMMENDED MODULE LAYOUT SHALL BE PER JEDEC MO-161.
 - RECOMMENDED PLATING ON MODULE PADS: 30 MICROINCH/(0.76 MICROMETER) MINIMUM HARD GOLD (Au) OVER 79 MICROINCH/(2.0 MICROMETER) MINIMUM NICKEL (Ni).
 - SEE CHART FOR HOLE SIZE AND PRESENCE AND PLATING OPTION.
 - PRODUCT WILL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

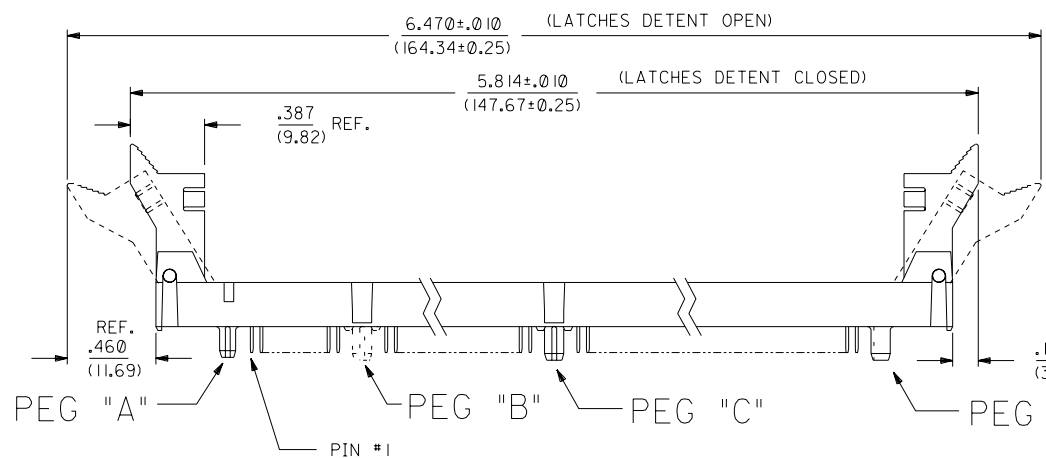
MATERIALS:
 HOUSING - GLASS FILLED LIQUID CRYSTAL POLYMER (LCP), UL 94V-0, COLOR: BLACK.
 TERMINAL - PHOSPHOR BRONZE
 LATCHES - GLASS FILLED HIGH TEMPERATURE NYLON, UL 94V-0, COLOR: IVORY.

PLATING:
 CONTACT AREA: OPTION A: GOLD (Au) FLASH; THICKNESS=2-10 MICROINCH/(0.05-0.25 (0.05-0.25 MICROMETER), OVER PALLADIUM-NICKEL (Pd-Ni); THICKNESS=30 MICROINCH/(0.76 MICROMETER) MINIMUM.
 OPTION B: GOLD (Au) FLASH; THICKNESS=2-10 MICROINCH/(0.05-0.25 MICROMETER), OVER PALLADIUM-NICKEL (Pd-Ni); THICKNESS=20 MICROINCH/(0.51 MICROMETER) MINIMUM.

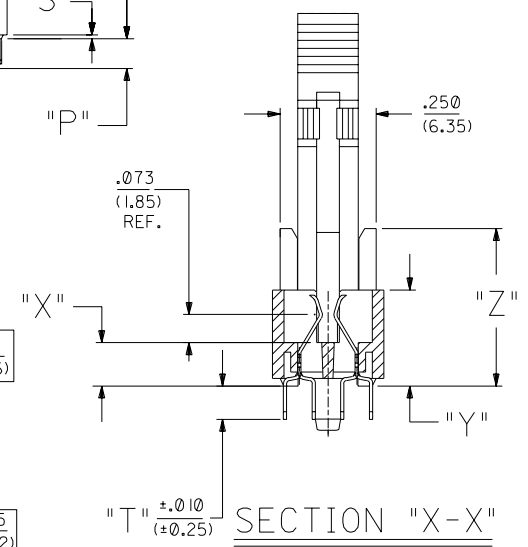
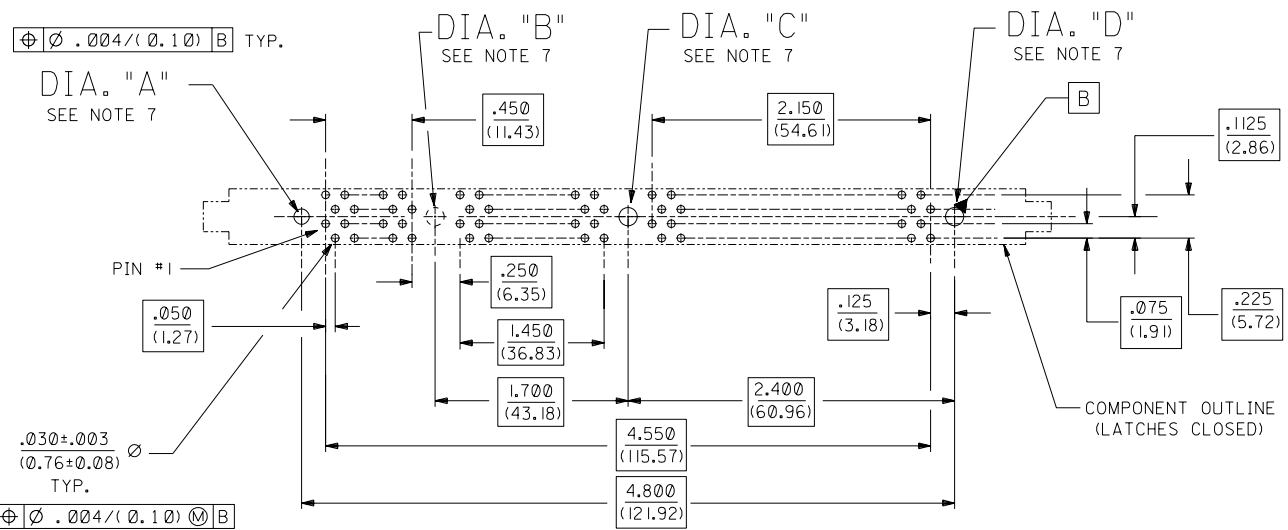
SOLDER TAILS: TIN (Sn); THICKNESS=150 MICROINCH/(3.81 MICROMETER) MINIMUM.
 UNDERPLATE: NICKEL (Ni) OVER ENTIRE CONTACT.



NOTE FOR LEAD FREE CONVERSION:
 THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO RoHS DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC". CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH LEAD.



71251-0004 SHOWN



SECTION "X-X"

SCALE 4:1

REV.	DESCRIPTION	DATE
3	H	
2	F3	
1	HI	

REV.	DESCRIPTION	DATE
HI	ADD LEAD FREE NOTE ECR# UC2004-1732 DMORGAN 04/03/09	
H	RELEASE -0027 ECR# UD2000-1151 DMORGAN 00/05/19	
H	RELEASE -0027 ECR# UD2000-1151 DMORGAN 00/05/19	
G	CHG Sn-Pb THICKNESS ECR# UD1999-0454 DMORGAN 99/08/09	
F3	REVISED PER ECR# U80435 97-8-8 LAURX	
F2	REVISED PER ECR# U71162 97-4-15 LAURX	
F1	REVISED PER ECR# U70361 96-10-9 LAURX	
F	REVISED PER ECR# U61133 96-9-25 LAURX	
E1	REVISED PER ECR# U60836 1-19-96 JCL	
E	REVISED PER ECR# U60682 12-7-95 JCL	
D2	REVISED PER ECR# U60452 10-10-95 JCL	
D1	REVISED PER ECR# U60361 9-18-95 JCL	
D	REVISED PER ECR# U51288 8-4-95 JCL	

DIMENSIONS SHOWN (METRIC) INCH UNLESS OTHERWISE SPECIFIED TOLERANCES: ANGULAR $\pm 1/2^\circ$		REVISE ONLY ON CAD SYSTEM	
3 PLACE $\pm .005$	INCH	TITLE	
2 PLACE $\pm \dots \pm 0.13$	METRIC	.050/(1.27) PITCH MULTI-KEY	
1 PLACE $\dots \pm 0.25$		168 CKT DIMM RAM ASSY	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		17.8mm LATCH SALES DWG	
DRWG. BY DCB	CHK'D BY DCB	FILE NAME	SHEET NO. DATE
APPR'D BY DCB	SCALE 2:1	ST125101 DGN	MOLEX INCORPORATED U.S.A. 1 OF 3 10/10/94
PART NO. 71251-0004		DRWG. NO. SDA-71251-0***	
SEE CHART			
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			

RECOMMENDED P.C. BOARD HOLE PATTERN (CONNECTOR SIDE)

13		12		11		10		9		8		7		6		71251		4		3		2		1	
PART NUMBER	COLOR	DESCRIPTION	FUNCTION KEY	VOLTAGE KEY	DIA. "A"	DIA. "B"	DIA. "C"	DIA. "D"	DIM. "H"	DIM. "P"	DIM. "S"	DIM. "T"	DIM. "W"	DIM. "X"	DIM. "Y"	DIM. "Z"	CONTACT AREA PLATING								
71251-0001	BLACK	3.3 VOLT STD DRAM	SEE SHEET 2																						OPTION A
71251-0002	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0003	BLACK	5.0 VOLT STD DRAM	CENTER	OFFSET LEFT	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0004	BLACK	5.0 VOLT STD DRAM	SEE SHEET 2																						OPTION A
71251-0005	BLACK	5.0 VOLT UNBUFFERED	OFFSET RIGHT	OFFSET LEFT	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.661 (42.19)	.125 (3.18)	.035 (0.89)	.102 (2.59)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0006	BLACK	5.0 VOLT STD DRAM	CENTER	OFFSET LEFT	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0007	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.140 (3.56)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0008	BLACK	5.0 VOLT STD DRAM	CENTER	OFFSET LEFT	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0009	BLACK	5.0 VOLT STD DRAM	CENTER	OFFSET LEFT	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.125 (3.18)	.035 (0.89)	.102 (2.59)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0010	BEIGE	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.125 (3.18)	.035 (0.89)	.112 (2.84)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0011	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0012	BLACK	3.3 VOLT UNBUFFERED	SEE SHEET 2																						OPTION A
71251-0013	BLACK	5.0 VOLT UNBUFFERED	SEE SHEET 2																						OPTION A
71251-0014	BLACK	3.3 VOLT UNBUFFERED	OFFSET RIGHT	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.661 (42.19)	.125 (3.18)	.035 (0.89)	.112 (2.84)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0015	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.117 (2.97)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0016	BLACK	3.3 VOLT SYNCHRONOUS	SEE SHEET 2																						OPTION A
71251-0017	BLACK	5.0 VOLT SYNCHRONOUS	SEE SHEET 2																						OPTION A
71251-0018	BLACK	3.3 VOLT UNBUFFERED	OFFSET RIGHT	CENTER	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.661 (42.19)	.140 (3.56)	.035 (0.89)	.102 (2.59)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0019	BLACK	5.0 VOLT STD DRAM	CENTER	OFFSET LEFT	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION B
71251-0020	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.140 (3.56)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION B
71251-0021	BLACK	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.700 (43.18)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION B
71251-0022	BLACK	3.3 VOLT UNBUFFERED	OFFSET RIGHT	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.661 (42.19)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION B
71251-0023	BLACK	3.3 VOLT UNBUFFERED	OFFSET RIGHT	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.661 (42.19)	.125 (3.18)	.035 (0.89)	.102 (2.59)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0024	BLACK	3.3 VOLT UNBUFFERED	OFFSET RIGHT	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	1.661 (42.19)	.155 (3.94)	.020 (0.51)	.105 (2.67)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A
71251-0026	BEIGE	3.3 VOLT STD DRAM	CENTER	CENTER	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	NONE	NONE	$\frac{.080 \pm .002}{(2.03 \pm 0.05)}$	1.700 (43.18)	.125 (3.18)	.035 (0.89)	.090 (2.29)	.985 (25.02)	.128 (3.25)	.265 (6.73)	.425 (10.79)									OPTION A
71251-0027	BLACK	3.3 VOLT SYNCHRONOUS	OFFSET LEFT	CENTER	NONE	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	$\frac{.093 + .003 / -.000}{(2.36 + 0.08 / -.000)}$	NONE	1.739 (44.17)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.970 (24.64)	.113 (2.87)	.250 (6.35)	.410 (10.41)									OPTION A

										DIMENSIONS SHOWN (METRIC) INCH UNLESS OTHERWISE SPECIFIED TOLERANCES ANGULAR ± 1/2° INCH METRIC 3 PLACE ± .005 --- 2 PLACE ± .01 ± 0.13 1 PLACE --- ± 0.25 DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS H SEE SHEET I F3 SEE SHEET I LTR. REVISIONS LTR. REVISIONS DRWG. NO. DCB CHK'D BY DCB FILE NAME ST 05103 APP'D BY DCB SCALE 2: 1 THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION									
										REVISE ONLY ON CAD SYSTEM TITLE .050/(1.27) PITCH MULTI-KEY 168 CKT DIMM RAM ASSY 17.8mm LATCH SALES DWG. MOLEX INCORPORATED SHEET NO. 3 DATE 12/07/95 U.S.A. PART NO. SEE CHART DRWG. NO. SDA-71251-0-000 DIV. CP									