80-M2122PA200SC01-K709F42
datasheet

MiniSKiiP® DUAL 2 **1200 V / 200 A**

Features

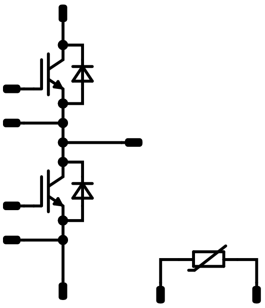
- Half-bridge topology
- Trench IGBT and CAL diode chip technology
- Si_3N_4 DCB and preapplied phasechange material for superior thermal performance
- Integrated NTC sensor
- Solderless spring contact mounting system

MiniSKiiP® 2 16 mm housing

Target applications

- General

Schematic



Types

- 80-M2122PA200SC01-K709F42

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	**Symbol**	**Conditions**		**Value**	**Unit**
Half-Bridge Switch					
Collector-emitter voltage	V_{CES}			1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	247	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		600	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	679	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 800 V	T_j = 150 °C	10	µs
Maximum junction temperature	T_{jmax}			175	°C
Half-Bridge Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	159	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	1100	A
Surge current capability	I^2t			6052	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	380	W
Maximum junction temperature	T_{jmax}			175	°C
Module Properties					
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	5500	V
Isolation voltage	V_{isol}	AC Voltage	t_p = 1 min	2500	V
Creepage distance		With std lid For more informations see handling instructions		6,3	mm
Clearance		With std lid For more informations see handling instructions		6,3	mm
Comparative Tracking Index	CTI			≥ 600	

*100 % tested in production

Characteristic Values

Parameter	**Symbol**	**Conditions**					**Values**			**Unit**
			V_{GE} **[V]** V_{GS} **[V]**	V_{CE} **[V]** V_{DS} **[V]** V_F **[V]**	I_C **[A]** I_D **[A]** I_F **[A]**	T_j **[°C]**	**Min**	**Typ**	**Max**	
Half-Bridge Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0076	25	5,1	5,8	6,4	V
Collector-emitter saturation voltage	V_{CEsat}		15		200	25 125 150	1,53	1,94 2,23 2,31	1,97(1)	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			2,6	µA
Gate-emitter leakage current	I_{GES}		20	0		25			240	nA
Internal gate resistance	r_g							3,75		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		12600		pF
Reverse transfer capacitance	C_{res}							540		pF
Gate charge	Q_g		15		0	25		1600		nC
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,14		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 2 Ω R_{goff} = 2 Ω	±15	600	200	25 125 150		185,6 196,4 200,6		ns
Rise time	t_r					25 125 150		37,8 43,4 44,2		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		304,4 378 398,4		ns
Fall time	t_f					25 125 150		54,95 104,28 117,66		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=13,68 µC Q_{rFWD}=28,16 µC Q_{rFWD}=33,67 µC				25 125 150		13,31 19,06 21,39		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		12,17 18,93 21,49		mWs

Characteristic Values

Parameter	**Symbol**	**Conditions**					**Values**			**Unit**
			V_{GE} **[V]** V_{GS} **[V]**	V_{CE} **[V]** V_{DS} **[V]** V_F **[V]**	I_C **[A]** I_D **[A]** I_F **[A]**	T_j **[°C]**	**Min**	**Typ**	**Max**	
Half-Bridge Diode										
Static										
Forward voltage	V_F				200	25		2,47	2,52(1)	V
						125		2,68		
						150		2,6	2,47(1)	
Reverse leakage current	I_R	V_r = 1200 V				25			240	µA
						150		17600	35400	
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,25		K/W
Dynamic										
Peak recovery current	I_{RRM}	d*i*/d*t*=6810 A/µs d*i*/d*t*=4754 A/µs d*i*/d*t*=5374 A/µs	±15	600	200	25		174,91		A
						125		212,96		
						150		230,71		
Reverse recovery time	t_{rr}					25		119,67		ns
						125		298,97		
						150		316,19		
Recovered charge	Q_r					25		13,68		µC
						125		28,16		
						150		33,67		
Reverse recovered energy	E_{rec}					25		4,78		mWs
						125		10,73		
						150		12,76		
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25		5315		A/µs
						125		3577		
						150		3745		

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Thermistor										
Static										
Rated resistance	R					25		5		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	R_{100} = 493 Ω				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. ±2 %						3375		K
B-value	$B_{(25/100)}$	Tol. ±2 %						3437		K
Vincotech Thermistor Reference									K	

(1) Value at chip level

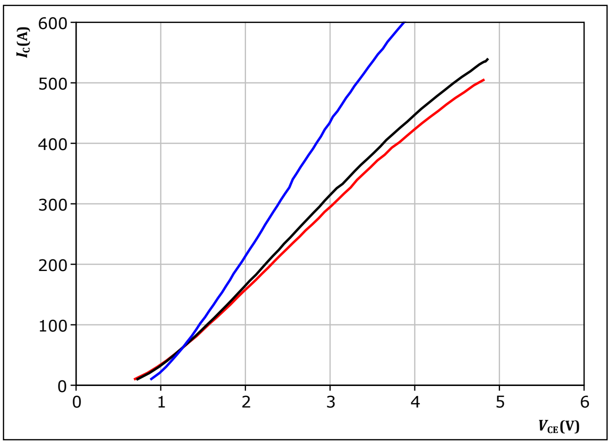
(2) Only valid with pre-applied Vincotech thermal interface material.

Half-Bridge Switch Characteristics

figure 1. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



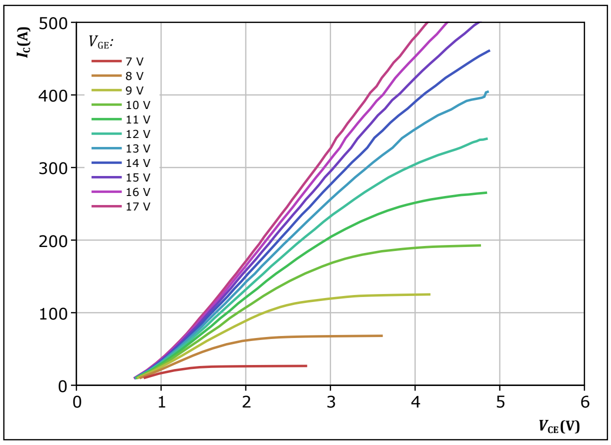
t_p = 250 µs
V_{GE} = 15 V

T_j:
25 °C
125 °C
150 °C

figure 2. **IGBT**

Typical output characteristics

I_C = f(V_{CE})

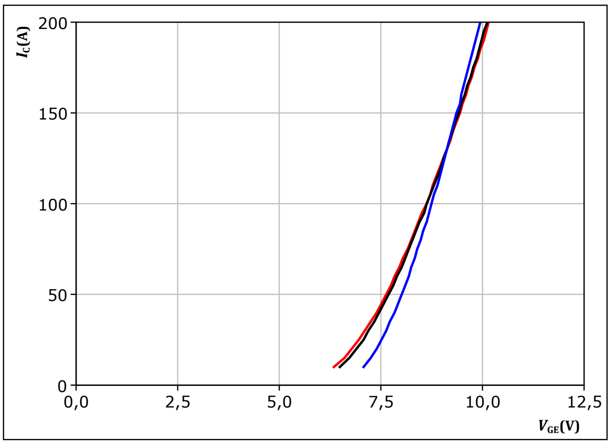


t_p = 250 µs
T_j = 150 °C
V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})



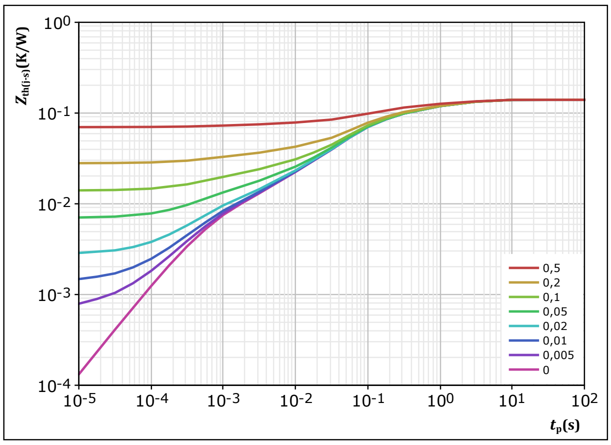
t_p = 250 µs
V_{CE} = 10 V

T_j:
25 °C
125 °C
150 °C

figure 4. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T
$R_{th(j-s)}$ = 0,14 K/W

IGBT thermal model values

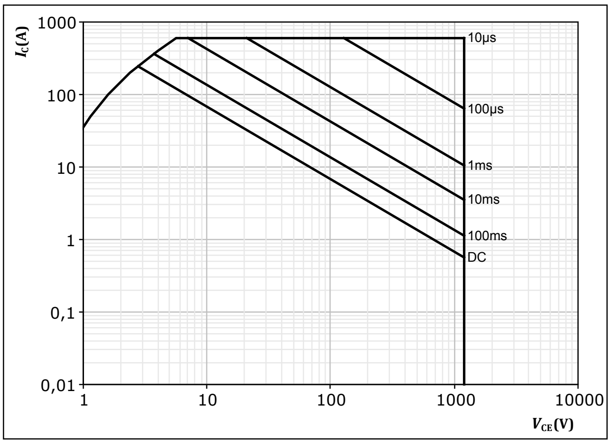
R (K/W)	τ (s)
3,05E-02	2,02E+00
3,53E-02	3,53E-01
5,98E-02	6,95E-02
8,25E-03	4,84E-03
6,13E-03	5,80E-04

Half-Bridge Switch Characteristics

figure 5.	**IGBT**

Safe operating area

I_C = f(V_{CE})



D = single pulse

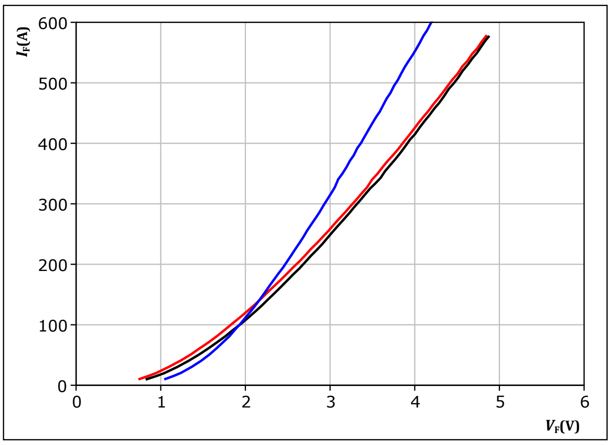
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Half-Bridge Diode Characteristics

figure 6. **FWD**

Typical forward characteristics

I_F = f(V_F)



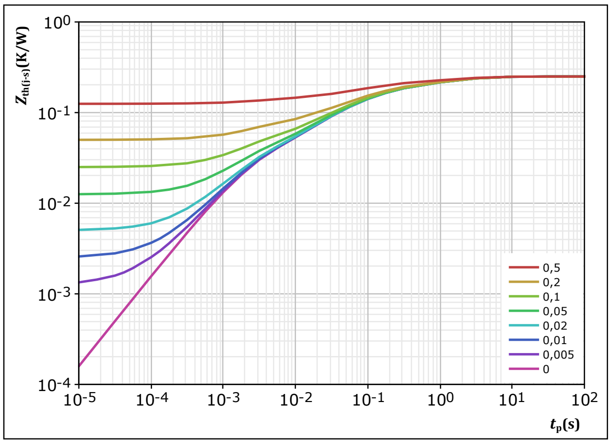
t_p = 250 µs

T_j: 25 °C
125 °C
150 °C

figure 7. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 0,25 K/W

FWD thermal model values

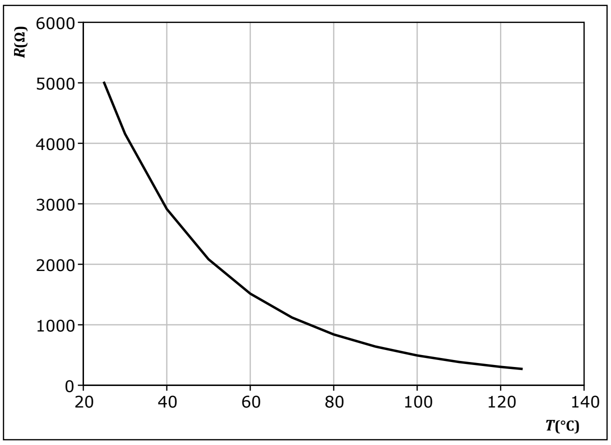
R (K/W)	τ (s)
2,65E-02	3,39E+00
5,14E-02	7,88E-01
8,51E-02	1,17E-01
6,01E-02	2,61E-02
2,68E-02	2,09E-03

Thermistor Characteristics

figure 8. **Thermistor**

Typical NTC characteristic as function of temperature

$R_T = f(T)$

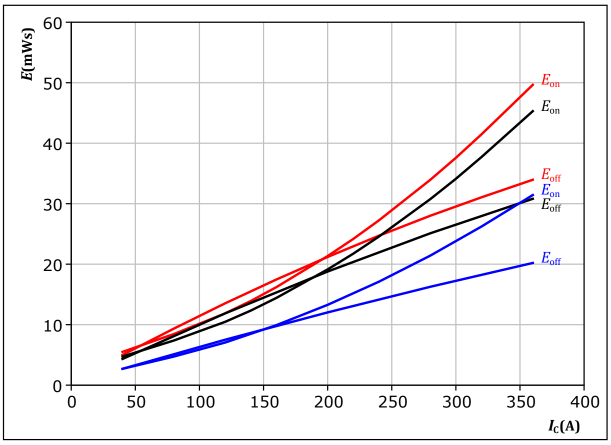


Half-Bridge Switching Characteristics

figure 9. **IGBT**

Typical switching energy losses as a function of collector current

$E = f(I_C)$



With an inductive load at

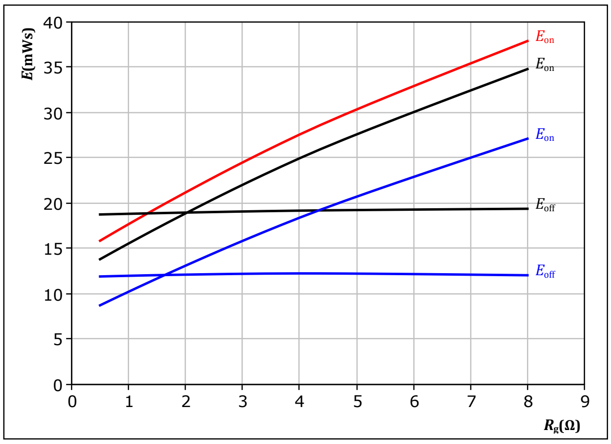
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω
R_{goff} = 2 Ω

T_j:
25 °C
125 °C
150 °C

figure 10. **IGBT**

Typical switching energy losses as a function of gate resistor

$E = f(R_g)$



With an inductive load at

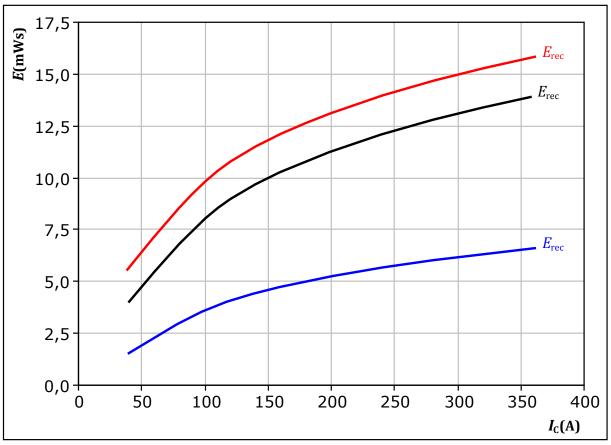
V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

T_j:
25 °C
125 °C
150 °C

figure 11. **FWD**

Typical reverse recovered energy loss as a function of collector current

$E_{rec} = f(I_C)$



With an inductive load at

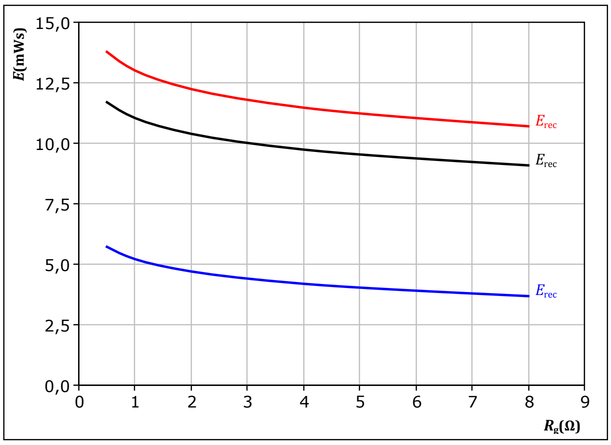
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω

T_j:
25 °C
125 °C
150 °C

figure 12. **FWD**

Typical reverse recovered energy loss as a function of gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

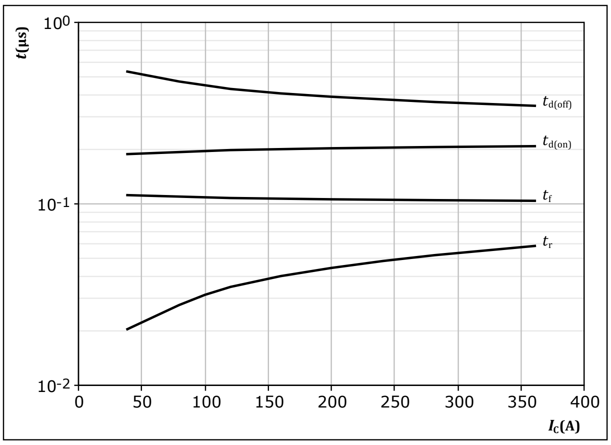
T_j:
25 °C
125 °C
150 °C

Half-Bridge Switching Characteristics

figure 13. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



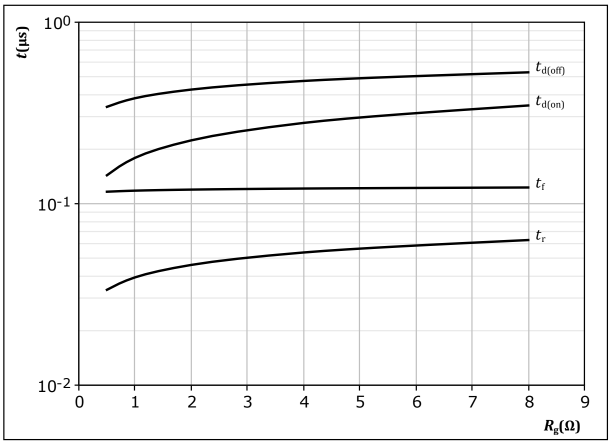
With an inductive load at

T_j = 150 °C
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω
R_{goff} = 2 Ω

figure 14. **IGBT**

Typical switching times as a function of gate resistor

t = f(R_g)



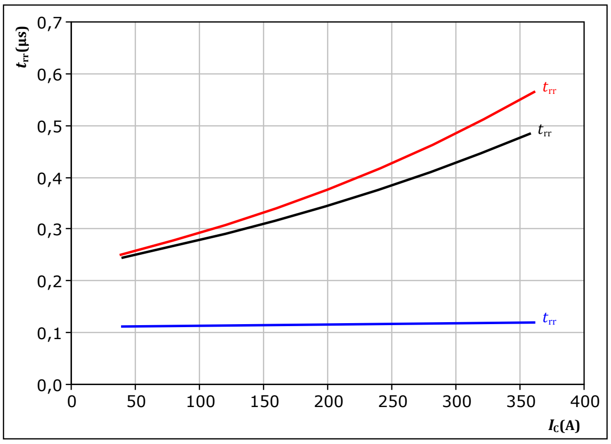
With an inductive load at

T_j = 150 °C
V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

figure 15. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

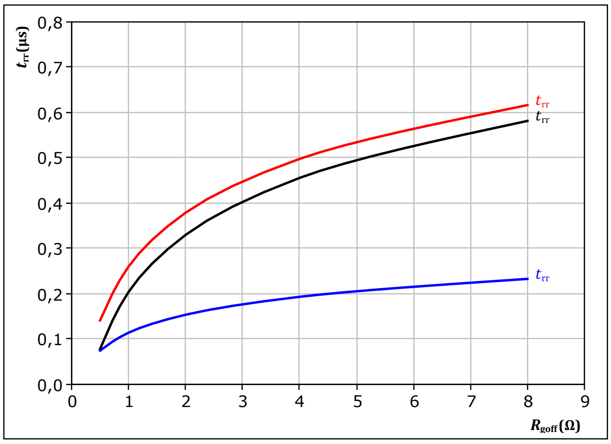
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω

T_j:
25 °C
125 °C
150 °C

figure 16. **FWD**

Typical reverse recovery time as a function of IGBT turn off gate resistor

t_{rr} = f(R_{goff})



With an inductive load at

V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

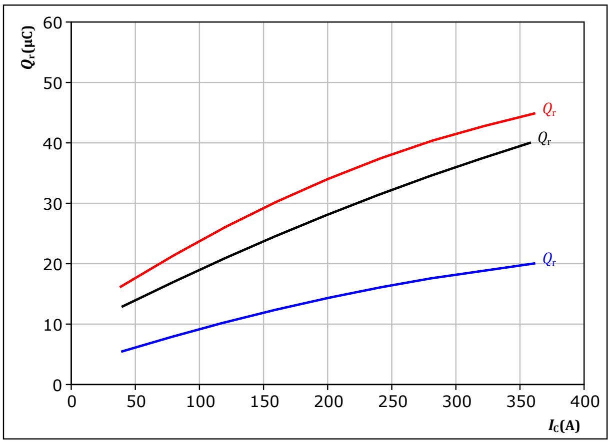
T_j:
25 °C
125 °C
150 °C

Half-Bridge Switching Characteristics

figure 17. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



With an inductive load at

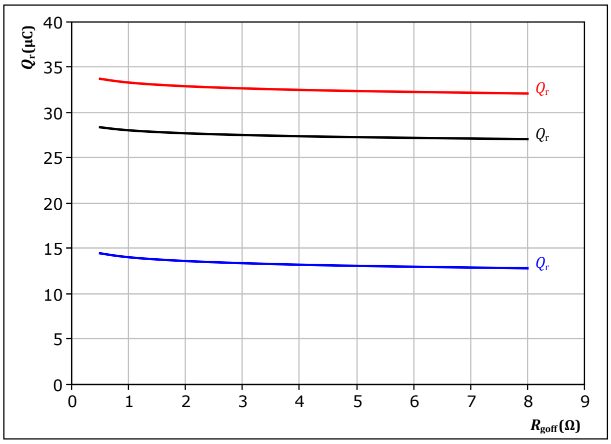
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω

T_j:
25 °C
125 °C
150 °C

figure 18. **FWD**

Typical recovered charge as a function of turn off gate resistor

Q_r = f(R_{goff})



With an inductive load at

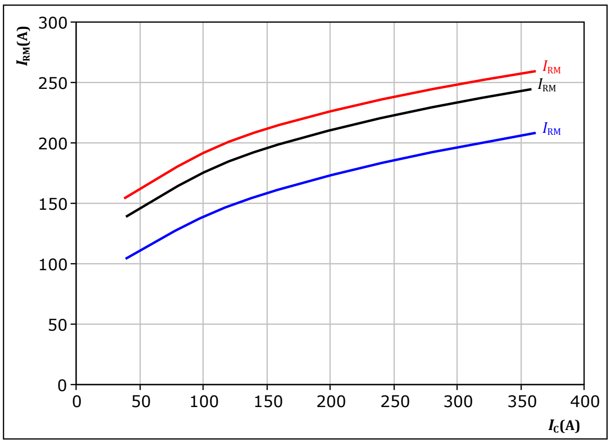
V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

T_j:
25 °C
125 °C
150 °C

figure 19. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



With an inductive load at

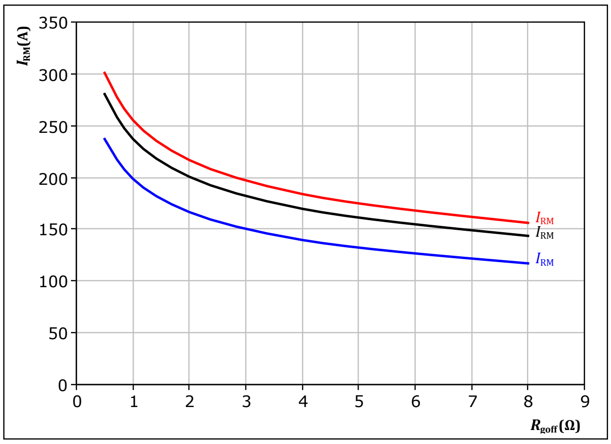
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω

T_j:
25 °C
125 °C
150 °C

figure 20. **FWD**

Typical peak reverse recovery current as a function of turn off gate resistor

I_{RM} = f(R_{goff})



With an inductive load at

V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

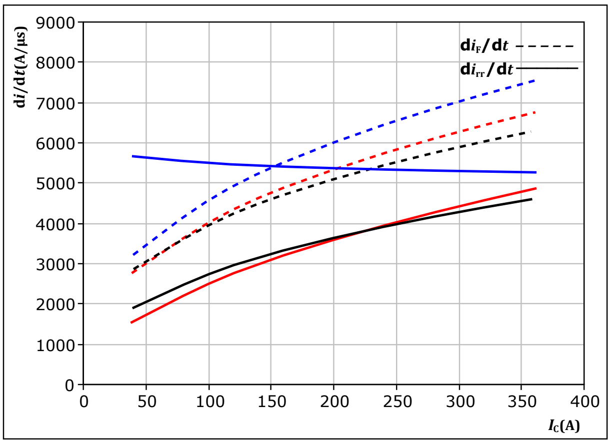
T_j:
25 °C
125 °C
150 °C

Half-Bridge Switching Characteristics

figure 21. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



With an inductive load at

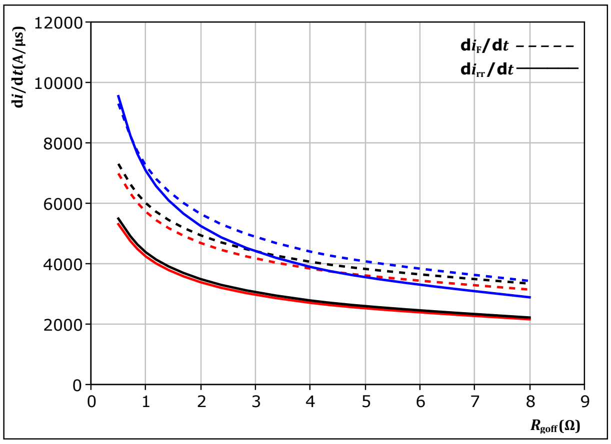
V_{CE} = 600 V
V_{GE} = ±15 V
R_{gon} = 2 Ω

T_j:
— 25 °C
— 125 °C
— 150 °C

figure 22. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor

di_F/dt, di_{rr}/dt = f(R_{goff})



With an inductive load at

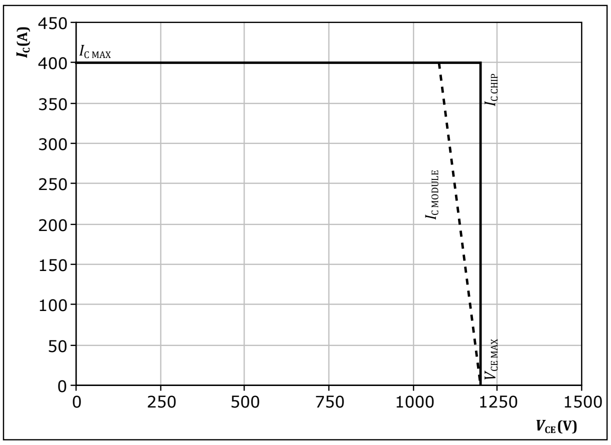
V_{CE} = 600 V
V_{GE} = ±15 V
I_C = 200 A

T_j:
— 25 °C
— 125 °C
— 150 °C

figure 23. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



At T_j = 150 °C
R_{gon} = 2 Ω
R_{goff} = 2 Ω

Half-Bridge Switching Definitions

figure 24. **IGBT**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} **(** t_{tEoff} **= integrating time for** E_{off}**)**

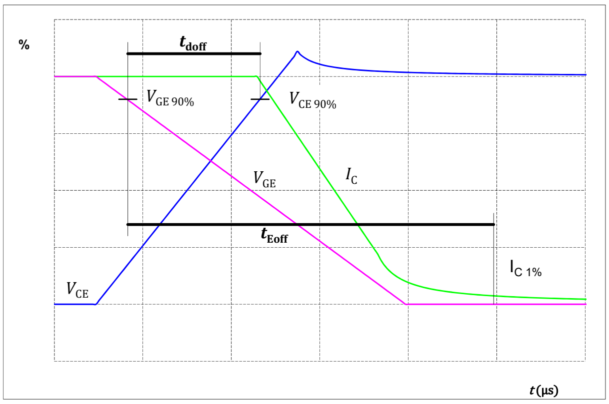


figure 25. **IGBT**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} **(** t_{Eon} **= integrating time for** E_{on}**)**

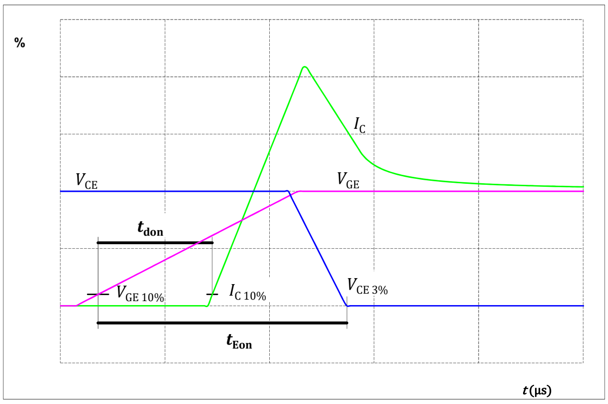


figure 26. **IGBT**

Turn-off Switching Waveforms & definition of t_{f}

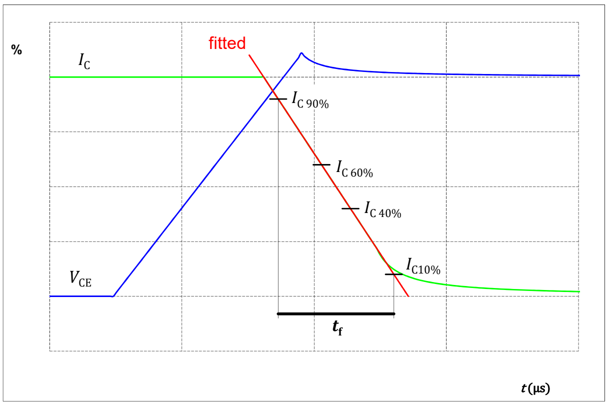
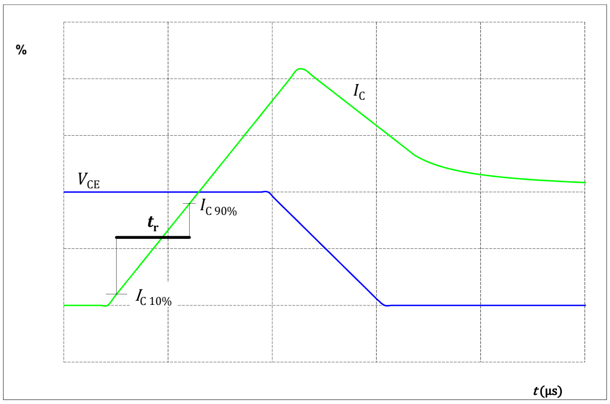


figure 27. **IGBT**

Turn-on Switching Waveforms & definition of t_{r}



Half-Bridge Switching Definitions

figure 28. **FWD**

Turn-off Switching Waveforms & definition of t_{rr}

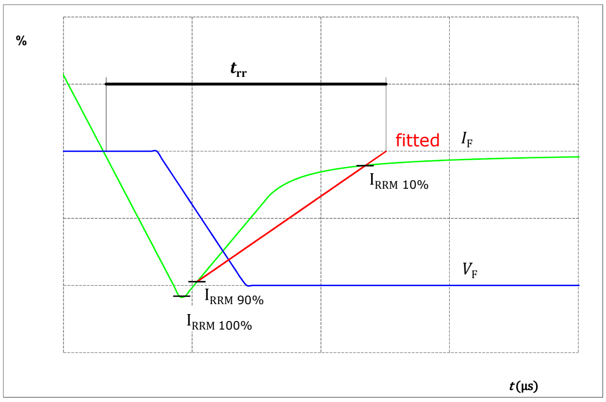
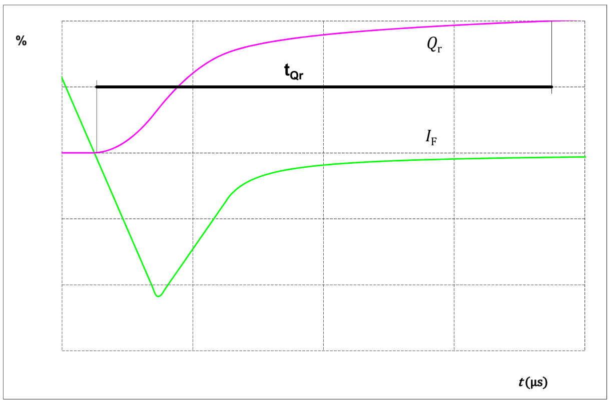


figure 29. **FWD**

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



80-M2122PA200SC01-K709F42

datasheet

Ordering Code

Version	Ordering Code
With std lid (6.5mm height) + thermal grease (3,4 W/mK, PSX-P7, silicone-free)	80-M2122PA200SC01-K709F42-/3A/
With thin lid (2.8mm height) + thermal grease (3,4 W/mK, PSX-P7, silicone-free)	80-M2122PA200SC01-K709F42-/3B/
With std lid (6.5mm height) + thermal grease (2,5 W/mK, HPTP, silicone-based)	80-M2122PA200SC01-K709F42-/5A/
With thin lid (2.8mm height) + thermal grease (2,5 W/mK, HPTP, silicone-based)	80-M2122PA200SC01-K709F42-/5B/

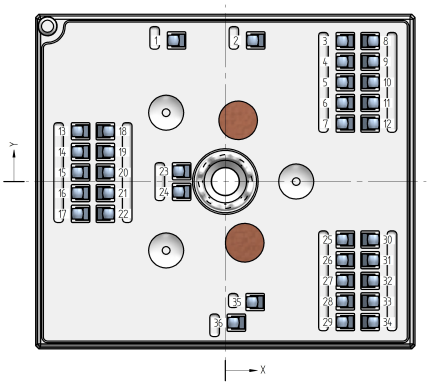
Marking

	**Text**	**Name**		**Date code**	**UL & VIN**	**Lot**	**Serial**
		NN-NNNNNNNNNNNNNN-TTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
	Datamatrix	**Type&Ver**	**Lot number**	**Serial**	**Date code**		
		TTTTTTTVV	LLLLL	SSSS	WWYY		

Outline

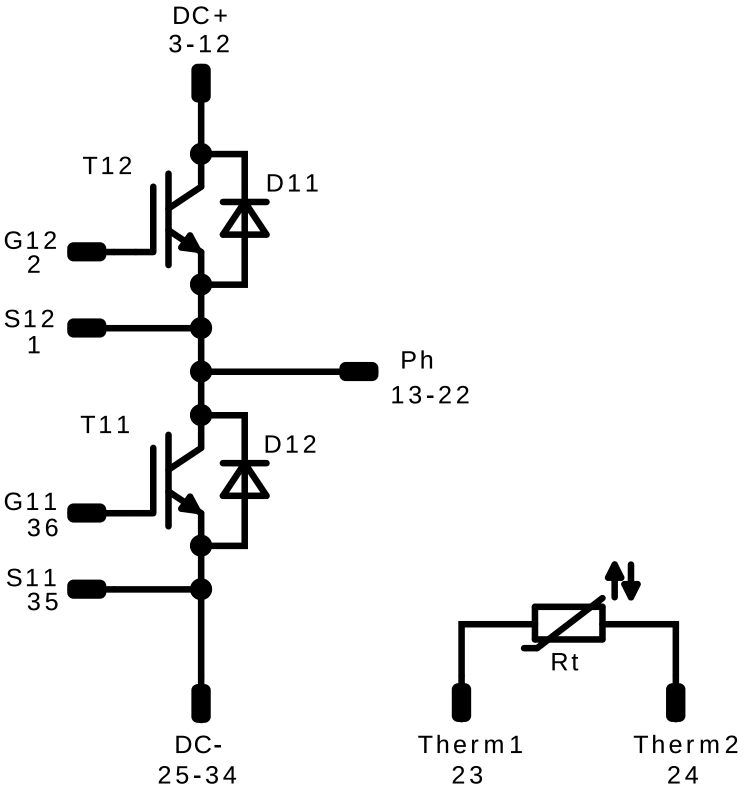
Pin table [mm]

Pin	X	Y	Function
1	-7,6	21,9	S12
2	4,7	21,9	G12
3	18,6	21,8	DC+
4	18,6	18,6	DC+
5	18,6	15,4	DC+
6	18,6	12,2	DC+
7	18,6	9	DC+
8	22,5	21,8	DC+
9	22,5	18,6	DC+
10	22,5	15,4	DC+
11	22,5	12,2	DC+
12	22,5	9	DC+
13	-22,5	7,8	Ph
14	-22,5	4,6	Ph
15	-22,5	1,4	Ph
16	-22,5	-1,8	Ph
17	-22,5	-5	Ph
18	-18,6	7,8	Ph
19	-18,6	4,6	Ph
20	-18,6	1,4	Ph
21	-18,6	-1,8	Ph
22	-18,6	-5	Ph
23	-6,8	1,6	Therm1
24	-6,8	-1,6	Therm2
25	18,6	-9	DC-
26	18,6	-12,2	DC-
27	18,6	-15,4	DC-
28	18,6	-18,6	DC-
29	18,6	-21,8	DC-
30	22,5	-9	DC-
31	22,5	-12,2	DC-
32	22,5	-15,4	DC-
33	22,5	-18,6	DC-
34	22,5	-21,8	DC-
35	4,6	-18,7	S11
36	1,7	-21,9	G11



Pad positions refers to center point. For more informations on pad design please see package data

Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12	IGBT	1200 V	200 A	Half-Bridge Switch	
D11, D12	FWD	1200 V	200 A	Half-Bridge Diode	
Rt	NTC			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 72	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for MiniSKiiP® 2 packages see vincotech.com website.

Package data
Package data for MiniSKiiP® 2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
80-M2122PA200SC01-K709F42-D1-14	22 Jul. 2021		

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