

## FMF600DXZ-24B

# HIGH POWER SWITCHING USE INSULATED TYPE

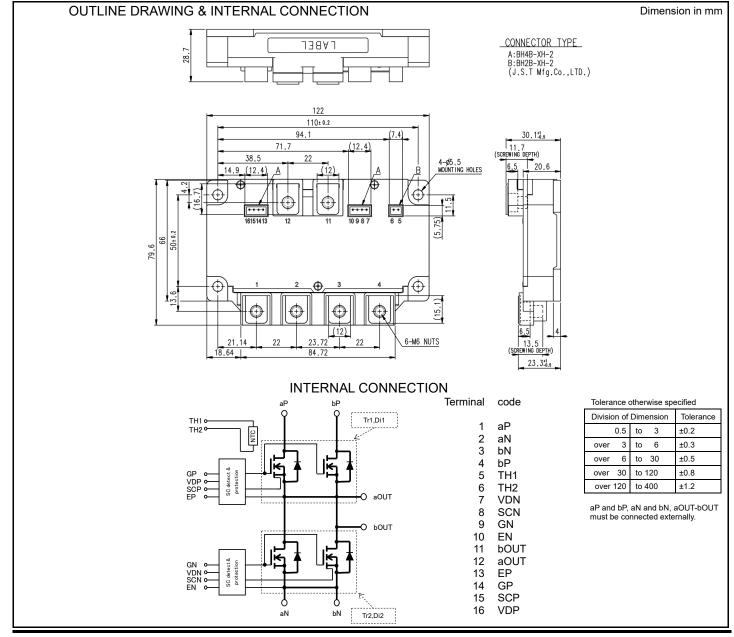


Dual switch (Half-Bridge)

- •Silicon Carbide MOSFET + Silicon Carbide Schottky Barrier Diode
- •Flat base Type
- Copper base plate
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

#### **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.



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MAXIMUM RATINGS ( $T_{vj}$  =25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V <sub>DSX</sub>	Drain-source voltage	V <sub>GS</sub> =-15 V	1200	V
V <sub>GSS</sub>	Gate-source voltage	D-S short-circuited	±20	V
I <sub>D</sub>	Durain assument	DC, T <sub>C</sub> =62°C (Note.2)	600	
I <sub>DRM</sub>	Drain current	Pulse, Repetitive (Note.3), T <sub>vj</sub> =150°C(Note.4)	900	Α
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note. 2)	2380	W
ls (Note.1)	Courses	DC		
I <sub>SRM</sub> (Note.1)	Source current	Pulse, Repetitive (Note.3), T <sub>vj</sub> =150°C	900	A
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	5000	V
T <sub>vjmax</sub>	Maximum junction temperature	Instantaneous event (overload) (Note.10)	175	°C
T <sub>vjop</sub>	Operating junction temperature	Continuous oepration (under switching) (Note.10)	-40~+150	°C
T <sub>cmax</sub>	Maximum case temperature	(Note.2, 10)	125	°C
T <sub>stg</sub>	Storage temperature	-	-40~+125	°C

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions (note9)			Limits		Unit
Gyllibol	item	Conditions			Тур.	Max.	Offic
I <sub>DSX</sub>	Drain-source cut-off current	$V_{DS}=V_{DSX}$ , $V_{GS}=-15$ V		-	-	6	mA
IDSX	Diain-source cut-on current	V <sub>DS</sub> =800V, V <sub>GS</sub> =-15 V		-	-	0.6	ША
$V_{GS(th)}$	Gate-source threshold voltage	I <sub>D</sub> =171 mA, V <sub>DS</sub> =10 V		1.8	2.5	3.2	V
I <sub>GSS</sub>	Gate-source leakage current	V <sub>GS</sub> =V <sub>GSS</sub> , D-S short-circuited		-	-	0.5	μA
		T <sub>vj</sub> =25 °	T <sub>vj</sub> =25 °C	-	1.65	2.30	V
V <sub>DS(on)</sub>	Drain-source on-state voltage	I <sub>D</sub> =600 A, V <sub>GS</sub> =15V (Note.6)	T <sub>vj</sub> =125 °C	-	2.10		
(terminal)			T <sub>vj</sub> =150 °C	-	2.20	-	
			T <sub>vj</sub> =25 °C	-	1.35	-	
$V_{DS(on)}$	Drain-source on-state voltage	I <sub>D</sub> =600 A, V <sub>GS</sub> =15V (Note.6)	T <sub>vj</sub> =125 °C	-	1.80	-	V
(chip)			T <sub>vj</sub> =150 °C	-	1.90		
			T <sub>vj</sub> =25 °C	-	2.3	-	
$r_{DS(on)}$	Drain-source on-state resistance I <sub>D</sub> =600 A, V <sub>GS</sub> =15V (Note.6)	I <sub>D</sub> =600 A, V <sub>GS</sub> =15V (Note.6)	T <sub>vj</sub> =125 °C	-	3.0	-	mΩ
(chip)			T <sub>vj</sub> =150 °C	-	3.2	-	
Ciss	Input capacitance					-	†
Coss	Output capacitance	V <sub>DS</sub> =10 V, V <sub>GS</sub> =0V		-	37	-	nF
Crss	Reverse transfer capacitance			-	2.6	-	
Q <sub>G</sub>	Gate charge	V <sub>DD</sub> =600 V, I <sub>D</sub> =600 A, V <sub>GS</sub> =0→1	V <sub>DD</sub> =600 V, I <sub>D</sub> =600 A, V <sub>GS</sub> =0→15 V		1463	-	nC
t <sub>d(on)</sub>	Turn-on delay time			-	170	-	ns mJ
tr	Rise time			-	70	-	
t <sub>d(off)</sub>	Turn-off delay time			-	350	-	
t <sub>f</sub>	Fall time	V <sub>DD</sub> =600 V, I <sub>D</sub> =600 A, V <sub>GS</sub> =±15 V		-	40	-	
Eon	Turn-on switching energy	$R_G=1.5\Omega$ , $L_{s\_ext}=16$ nH, Inductive	load, per puise	-	17	-	
E <sub>off</sub>	Turn-off switching energy			-	16	-	
Qc	Drain-source charge			-	3	-	μC
			T <sub>vi</sub> =25 °C	-	1.90	2.45	
V <sub>SD</sub> (Note.1)	Source-drain voltage	I <sub>S</sub> =600 A <sup>(Note.6)</sup> V <sub>GS</sub> =-15 V	T <sub>vi</sub> =125 °C	-	2.70	-	V
(terminal)		VGS13 V	T <sub>vi</sub> =150 °C	-	2.90	-	
			T <sub>vj</sub> =25 °C	-	1.60	-	
$V_{\text{SD}}^{\text{(Note.1)}}$	Source-drain voltage	I <sub>S</sub> =600 A (Note.6)	T <sub>vj</sub> =125 °C	-	2.40	-	V
(chip)		V <sub>GS</sub> =-15 V	T <sub>vi</sub> =150 °C	-	2.60	-	
R <sub>DD'+SS'</sub>	Internal lead resistance	P-N, T <sub>C</sub> =25°C <sup>(Note.2)</sup>		-	0.5	-	mΩ
Ls	Internal stray inductance	P-N		-	10	-	nH
r <sub>g(on)</sub>	Internal gate resistance (on)	Per switch		-	1.32	-	Ω
r <sub>g(off)</sub>	Internal gate resistance (off)	Per switch		_	6.15	-	Ω

## FMF600DXZ-24B

### HIGH POWER SWITCHING USE

#### INSULATED TYPE

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
$R_{th(j-c)Q}$	Thermal resistance <sup>(Note, 2)</sup>	Junction to case, per inverter switch	-	1	63	K/kW
$R_{th(j-c)D}$		Junction to case, per inverter FWD		•	75	IN/KVV
R <sub>th(c-s)</sub>	Contact thermal resistance <sup>(Note.2)</sup>	Case to heat sink, per 1 module,	-	12	-	K/kW
		Thermal grease applied (Note.8, 10)				IV/KVV

#### NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
			Min.	Тур.	Max.	Offic
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note.2)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	T <sub>C</sub> =100 °C <sup>(Note.2)</sup> ,R <sub>100</sub> =493 Ω	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note.7)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note.2)	-	-	10	mW

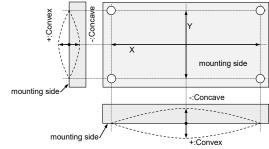
#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits			Unit
				Min.	Тур.	Max.	UIIIL
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms		Mounting to heat sink	M 5 screw	2.5	3.0	6.0	
m	mass	-		-	500	-	g
da	Clearance			10	-	-	mm
ds	Creepage distance			17	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note.5)		-100	-	+100	μm
-	Connector insertion force	2 pin type		0	-	25	N
		4 pin type		0	-	35	N

<sup>\*:</sup> This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU)2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, source-drain free wheeling diode (FWD).

- 2. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  does not exceed  $T_{vjmax}$  rating.
- 4. Junction temperature (T<sub>vi</sub>) should not increase beyond T<sub>vimax</sub> rating.
- 5. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



6. Pulse width and repetition rate should be such as to cause negligible temperature rise.

7. 
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

R<sub>25</sub>: resistance at absolute temperature T<sub>25</sub> [K]; T<sub>25</sub>=25 [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}\text{=}50$  [°C]+273.15=323.15 [K]

- 8. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K)/D<sub>(C-S)</sub>=100 $\mu$ m.
- 9. Per switch (ex. Tr1 chips total in page.6)
- 10. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T<sub>vj max</sub>, T<sub>vj op</sub>, T<sub>C max</sub>) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

## FMF600DXZ-24B

HIGH POWER SWITCHING USE

INSULATED TYPE

### RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Unit		
		Collations	Min.	Тур.	Max.	Offic
$V_{DD}$	(DC) Supply voltage	Applied across aP -aN/ bP-bN terminals	-	600	850	V
V <sub>D</sub>	DC supply voltage (control)	Applied across VDP-EP/ VDN-EN terminals	13.5	15.0	16.5	V
V <sub>GS(+)</sub>	Gate-Source positive drive voltage	Applied across GP-EP/ GN-EN terminals	13.5	15.0	16.5	V
V <sub>GS(-)</sub>	Gate-Source negative drive voltage	Applied across GP-EP/ GN-EN terminals	-16.5	-15.0	-7.0	V
$R_G$	External gate resistance (Note.11)	Per switch	1.5	-	7.5	Ω
f <sub>c</sub>	Switching frequency	$V_{GS}$ =±15V, $R_{G}$ =1.5 $\Omega$ , $V_{DD}$ =600V, $T_{vj}$ =150 $^{\circ}$ C	-	-	50	kHz
$t_{d(SCoff)}$	Gate cutoff delay time after SC output	V <sub>GS</sub> =15V, R <sub>G</sub> =1.5Ω, V <sub>DD</sub> =600V, T <sub>vj</sub> =150°C	-	-	3	μs

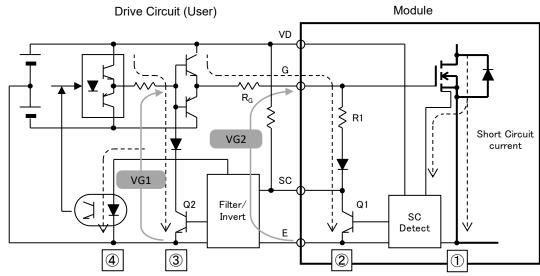
Note 11. The value of external gate resistance should be considered the surge voltage not to exceed the rating voltage in the worst system condition.

#### SHORT CIRCUIT DETECTION & PROTECTION CHARACTERISTICS

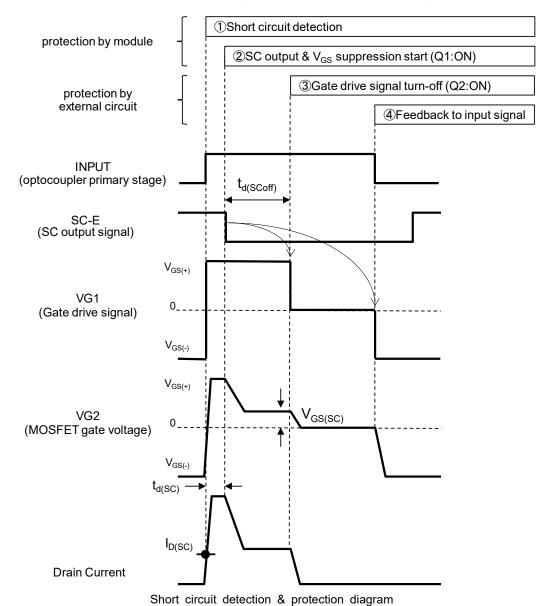
Symbol	Item	Conditions		Unit		
			Min.	Тур.	Max.	Offic
I <sub>D(SC)</sub>	SC detect drain current	T <sub>vj</sub> =150°C, V <sub>GS</sub> =15V	900	1200	-	Α
t <sub>d(SC)</sub>	SC detect delay time	$T_{vj}$ =150°C, $V_{GS}$ =15V, $R_{G}$ =1.5 $\Omega$	-	1	-	μs
V <sub>GS(SC)</sub>	SC protection gate limit voltage	$T_{vj}$ =150°C, $V_{GS}$ =15V, $R_{G}$ =1.5 $\Omega$	-	10	-	V
R1	SC protection gate limit resistance	-	1	2.35	-	Ω

Refer to the circuit in page.5

#### **SHORT CIRCUIT DETECTION & PROTECTION**



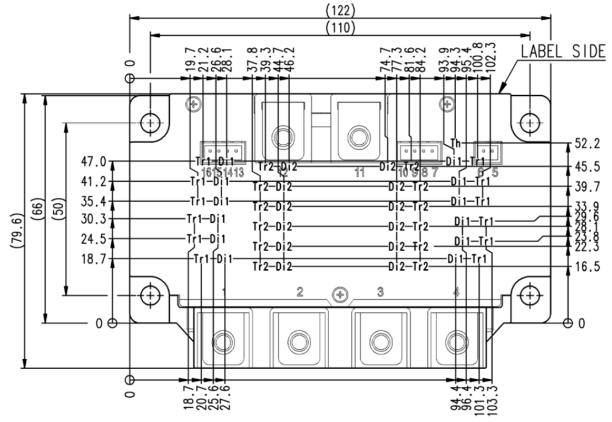
Example of application (Short circuit detection & protection)



INSULATED TYPE

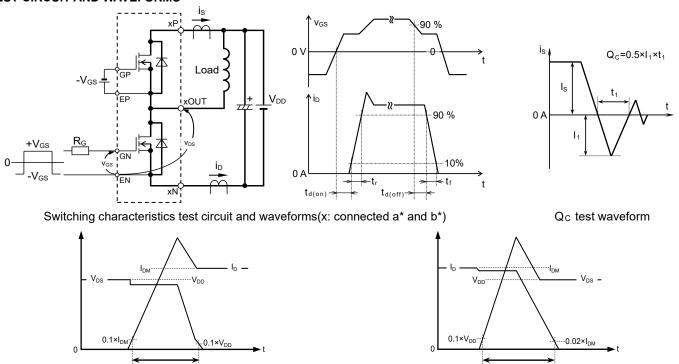
#### **CHIP LOCATION (Top view)**

Dimension in mm, tolerance: ±1 mm



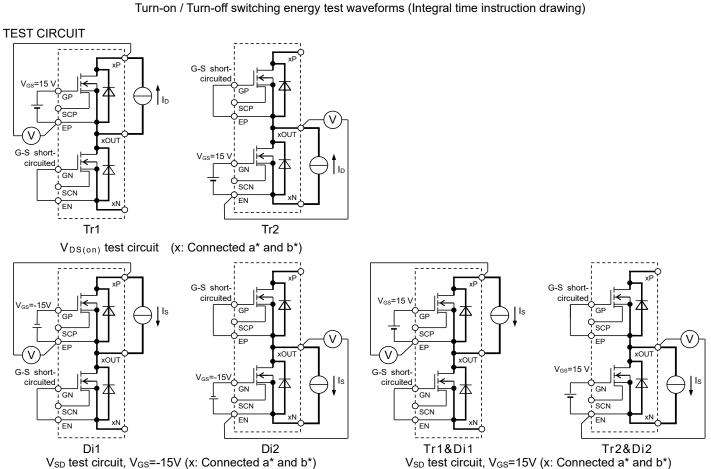
Tr1,Tr2: SiC-MOSFET, Di1,Di2: SiC-SBD, Th: NTC thermistor

#### **TEST CIRCUIT AND WAVEFORMS**



MOSFET Turn-on switching energy

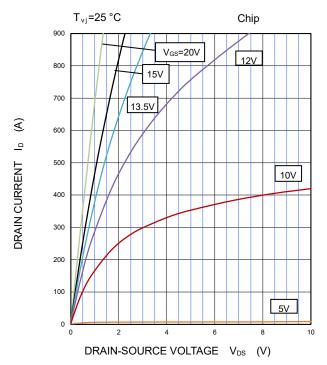
MOSFET Turn-off switching energy



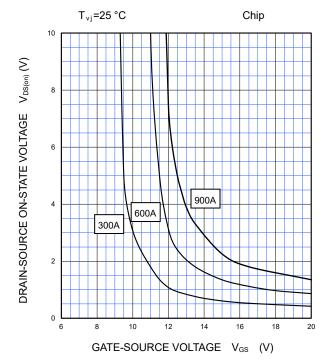
#### INSULATED TYPE

## PERFORMANCE CURVES OUTPUT

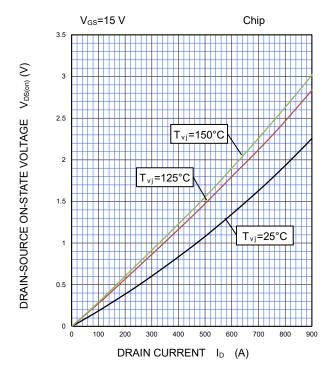
OUTPUT CHARACTERISTICS (TYPICAL)



#### DRAIN-SOURCE ON STATE VOLTAGE CHARACTERISTICS (TYPICAL)

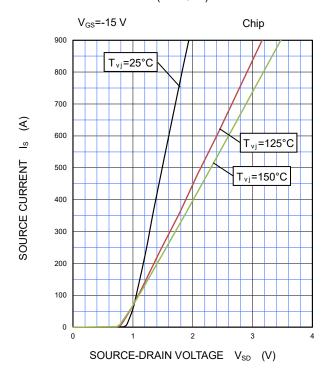


#### DRAIN-SOURCE ON STATE VOLTAGE CHARACTERISTICS (TYPICAL)

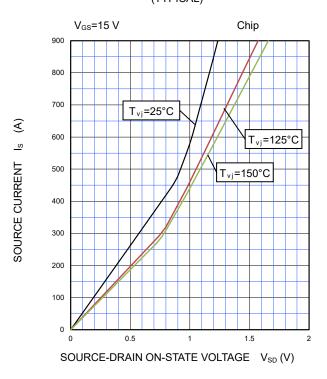


#### **PERFORMANCE CURVES**

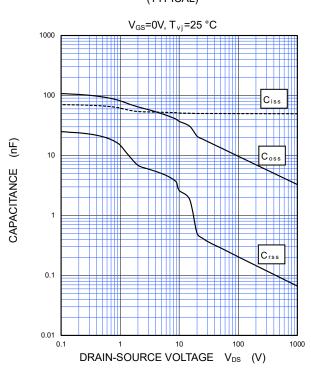
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



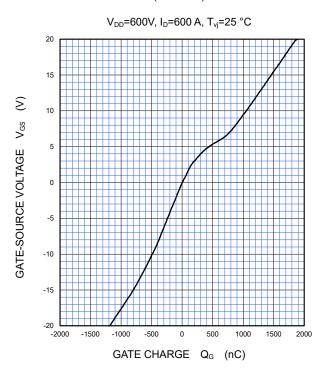
SOURCE-DRAIN ON STATE VOLTAGE CHARACTERISTICS (TYPICAL)



CAPACITANCE CHARACTERISTICS (TYPICAL)



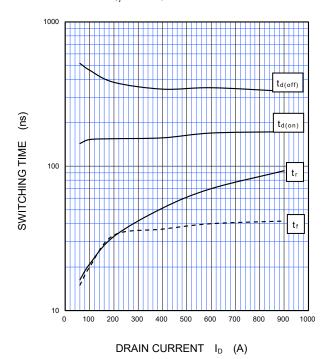
GATE CHARGE CHARACTERISTICS (TYPICAL)



#### **PERFORMANCE CURVES**

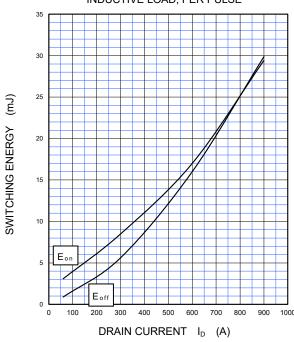
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $\rm V_{DD}\!=\!600$  V,  $\rm V_{GS}\!=\!\pm15$  V,  $\rm R_{G}\!=\!1.5~\Omega,~L_{s\_ext}\!=\!16nH$   $\rm T_{vj}\!=\!150~^{\circ}C,~INDUCTIVE~LOAD$ 



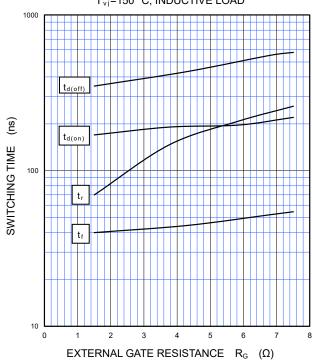
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{DD}$ =600 V,  $V_{GS}$ =±15 V,  $R_G$ =1.5 $\Omega$ ,  $T_{vj}$ =150 °C,  $L_{s\_ext}$ =16nH INDUCTIVE LOAD, PER PULSE



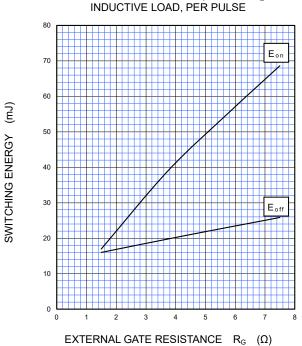
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{DD}$ =600 V,  $V_{GS}$ =±15 V,  $I_D$ =600 A,  $L_{s\_ext}$ =16nH  $T_{vi}$ =150 °C, INDUCTIVE LOAD



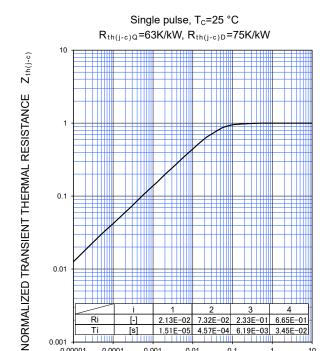
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{DD}$ =600 V,  $V_{GS}$ =±15 V,  $I_{D}$ =600 A,  $T_{Vj}$ =150 °C,  $L_{s\_ext}$ =16nH INDUCTIVE LOAD. PER PULSE



## PERFORMANCE CURVES

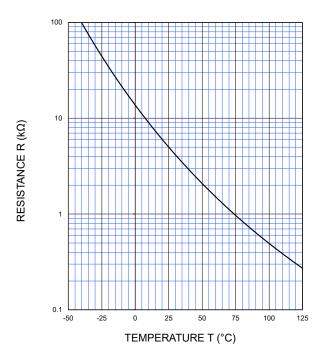
#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



TIME (S)

#### NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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