



fastPACK E2 SiC

1200 V / 11 mΩ

Features

- Compact and low inductive design
- High frequency SiC MOSFET
- Integrated NTC

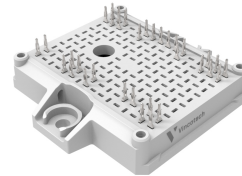
Target applications

- Charging Stations
- Power Supply
- Welding & Cutting

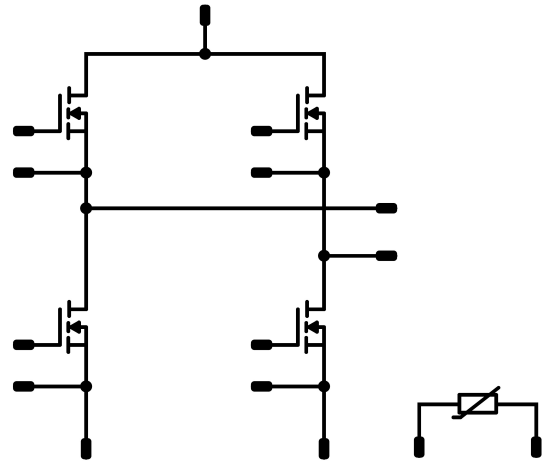
Types

- 10-EY124PA011ME-LP40F18T

flow E2 12 mm housing



Schematic





Vincotech

10-EY124PA011ME-LP40F18T
datasheet

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
H-Bridge Switch				
Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	113	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	360	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	204	W
Gate-source voltage	V_{GS}		-4 / 15	V
		dynamic	-8 / 19	
Maximum Junction Temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Isolation voltage	V_{isol}	AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			>12,7	mm
Clearance			9.14	mm
Comparative Tracking Index	CTI		≥ 600	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

H-Bridge Switch

Static

Drain-source on-state resistance	$r_{DS(on)}$		15		120	25 125 150	7,47	11 14 15	13,87 ⁽¹⁾	mΩ
Gate-source threshold voltage	$V_{GS(th)}$		0		0,0345	25	1,8	2,5	3,6	V
Gate to Source Leakage Current	I_{GSS}		15	0		25		30	750	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	1200		25		3	57	μA
Internal gate resistance	r_g							0,567		Ω
Gate charge	Q_g		-4/15	800	120	25		354		nC
Short-circuit input capacitance	C_{iss}	$f = 100$ kHz	0	1000	0	25		10071		pF
Short-circuit output capacitance	C_{oss}							387		
Reverse transfer capacitance	C_{rss}							24		
Diode forward voltage	V_{SD}		0		60	25		4,6		V

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,47		K/W
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Vincotech

10-EY124PA011ME-LP40F18T
datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Dynamic										
Turn-on delay time	$t_{d(on)}$					25 125 150		39,13 33,97 33,58		ns
Rise time	t_r	$R_{gon} = 4 \Omega$ $R_{goff} = 4 \Omega$				25 125 150		26,39 21,91 20,74		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		89,51 96,81 99,11		ns
Fall time	t_f					25 125 150		16,16 17,24 18,43		ns
Turn-on energy (per pulse)	E_{on}	$Q_{rFWD}=0,606 \mu C$ $Q_{tFWD}=1,36 \mu C$ $Q_{rFWD}=1,63 \mu C$	-4/15	600	80	25 125 150		1,5 1,6 1,64		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,65 0,623 0,606		mWs
Peak recovery current	I_{RRM}					25 125 150		48,48 75,65 85,62		A
Reverse recovery time	t_{rr}				25 125 150		21,28 28,72 30,58		ns	
Recovered charge	Q_r	$di/dt=4201 A/\mu s$ $di/dt=4595 A/\mu s$ $di/dt=4798 A/\mu s$				25 125 150		0,606 1,36 1,63		μC
Reverse recovered energy	E_{rec}					25 125 150		0,121 0,381 0,486		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		6995,73 9639,8 14285,39		A/ μs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	V_{CE} [V]	T_j [°C]	Min	Typ	Max	

Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R_{100}	$A_{R/R}$	$R_{100} = 493 \Omega$				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 2 \%$						3375		K
B-value	$B_{(25/100)}$	Tol. $\pm 2 \%$						3437		K
Vincotech Thermistor Reference									K	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



H-Bridge Switch Characteristics

figure 1. MOSFET

Typical output characteristics
 $I_D = f(V_{DS})$

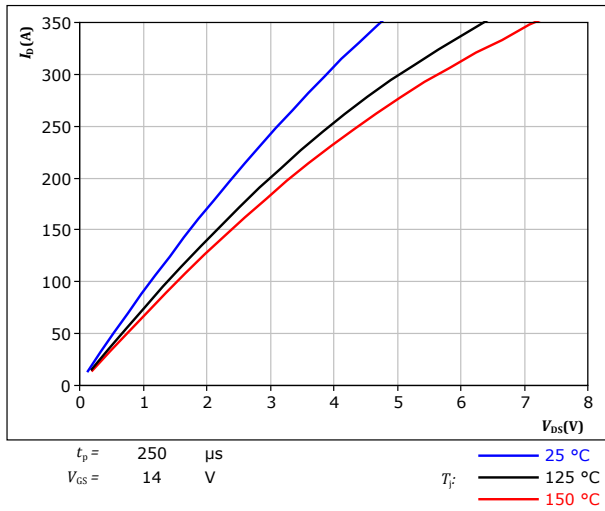


figure 2. MOSFET

Typical output characteristics
 $I_D = f(V_{DS})$

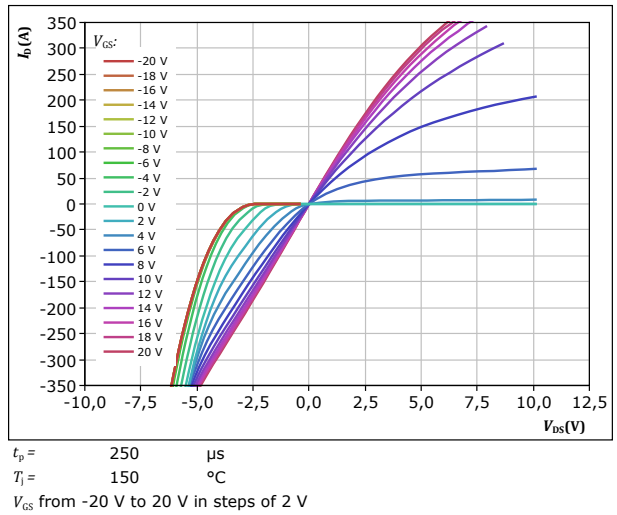


figure 3. MOSFET

Typical transfer characteristics
 $I_D = f(V_{GS})$

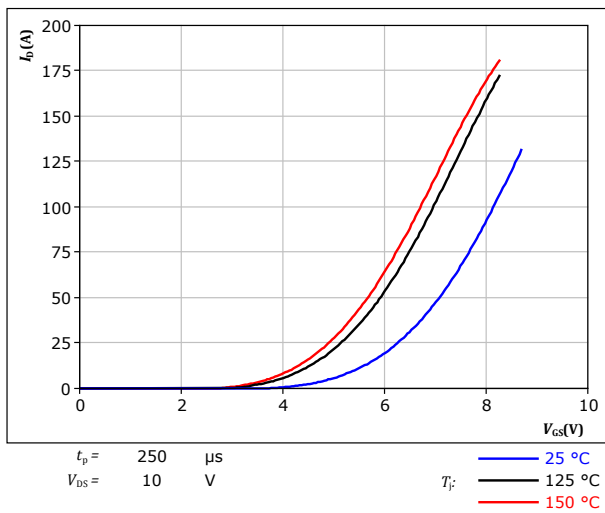
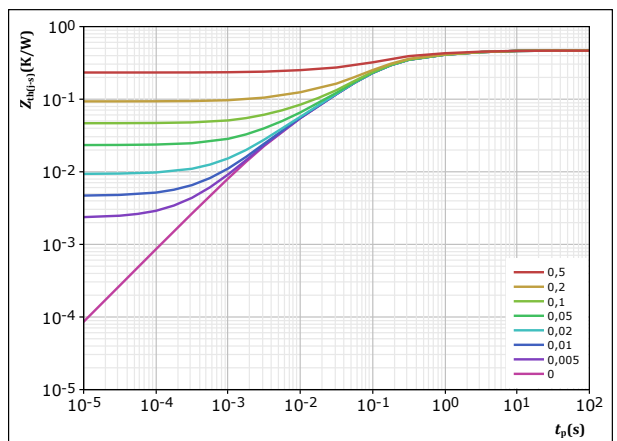


figure 4. MOSFET

Transient thermal impedance as a function of pulse width
 $Z_{th(j-s)} = f(t_p)$



$D = t_p / T$
 $R_{th(j-s)} = 0,466 \text{ K/W}$
 MOSFET thermal model values

R (K/W)	τ (s)
4,34E-02	4,04E+00
8,79E-02	7,13E-01
2,49E-01	1,27E-01
6,29E-02	3,86E-02
2,30E-02	4,73E-03

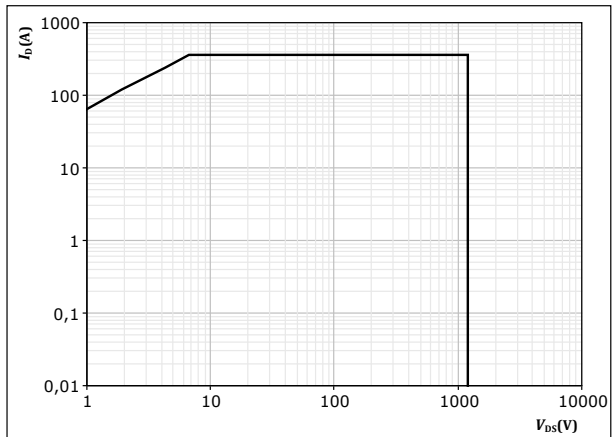


H-Bridge Switch Characteristics

figure 5. MOSFET

Safe operating area

$$I_D = f(V_{DS})$$



$D =$ single pulse

$T_s = 80$ °C

$V_{GS} = 14$ V

$T_j = T_{jmax}$

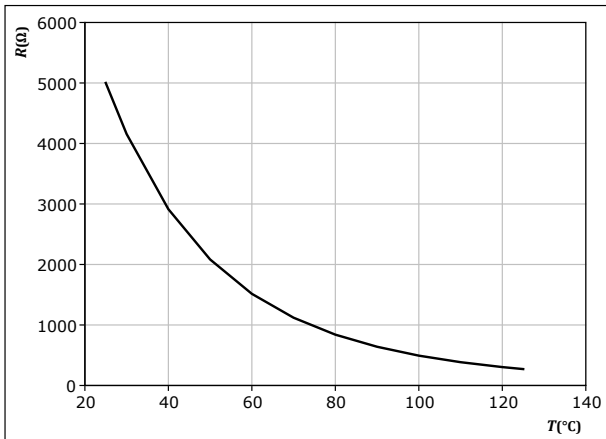


Thermistor Characteristics

figure 6. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$

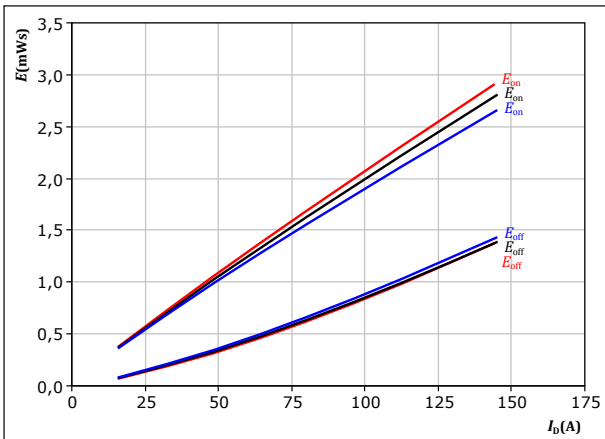




H-Bridge Switching Characteristics

figure 7. MOSFET

Typical switching energy losses as a function of drain current
 $E = f(I_D)$

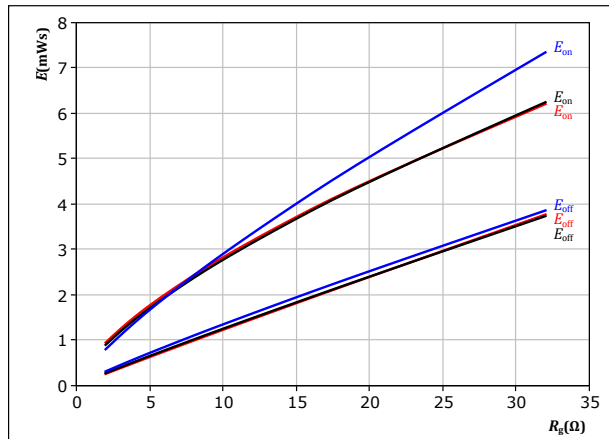


With an inductive load at

$V_{DS} =$	600	V	$T_j:$	— 25 °C
$V_{GS} =$	-4/15	V		— 125 °C
$R_{gon} =$	4	Ω		— 150 °C
$R_{goff} =$	4	Ω		

figure 8. MOSFET

Typical switching energy losses as a function of gate resistor
 $E = f(R_g)$

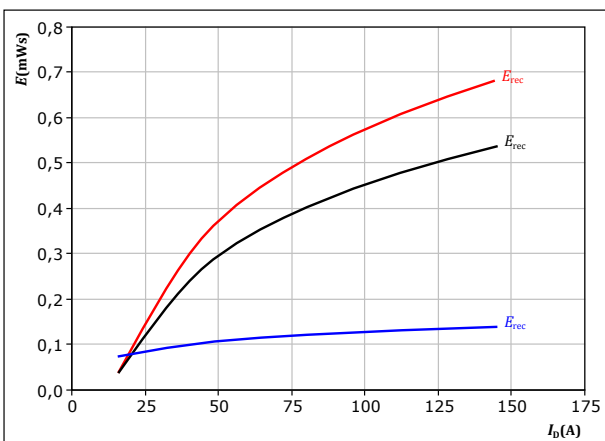


With an inductive load at

$V_{DS} =$	600	V	$T_j:$	— 25 °C
$V_{GS} =$	-4/15	V		— 125 °C
$I_D =$	80	A		— 150 °C

figure 9. MOSFET

Typical reverse recovered energy loss as a function of drain current
 $E_{rec} = f(I_D)$

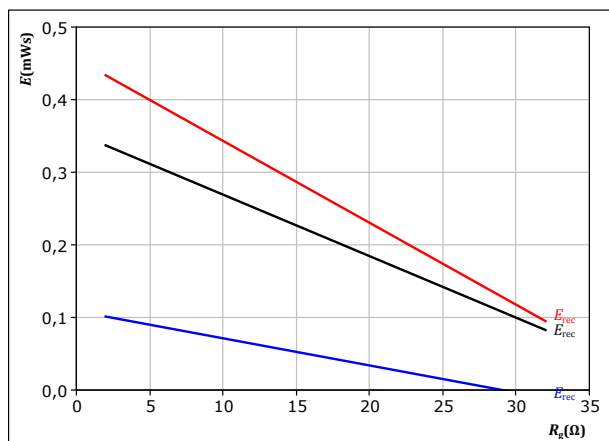


With an inductive load at

$V_{DS} =$	600	V	$T_j:$	— 25 °C
$V_{GS} =$	-4/15	V		— 125 °C
$R_{gon} =$	4	Ω		— 150 °C

figure 10. MOSFET

Typical reverse recovered energy loss as a function of gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at

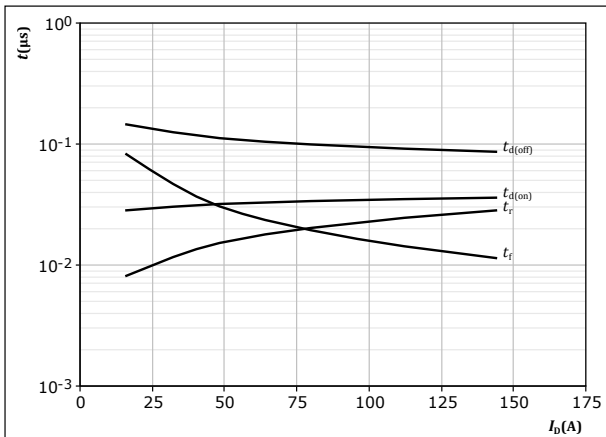
$V_{DS} =$	600	V	$T_j:$	— 25 °C
$V_{GS} =$	-4/15	V		— 125 °C
$I_D =$	80	A		— 150 °C



H-Bridge Switching Characteristics

figure 11. MOSFET

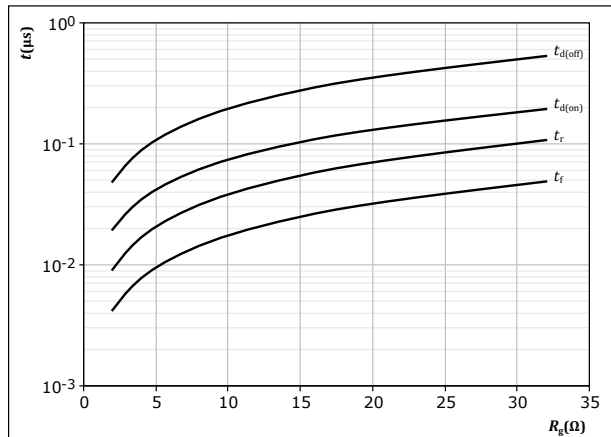
Typical switching times as a function of drain current
 $t = f(I_D)$



With an inductive load at
 $T_j = 150$ °C
 $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $R_{g(on)} = 4$ Ω
 $R_{g(off)} = 4$ Ω

figure 12. MOSFET

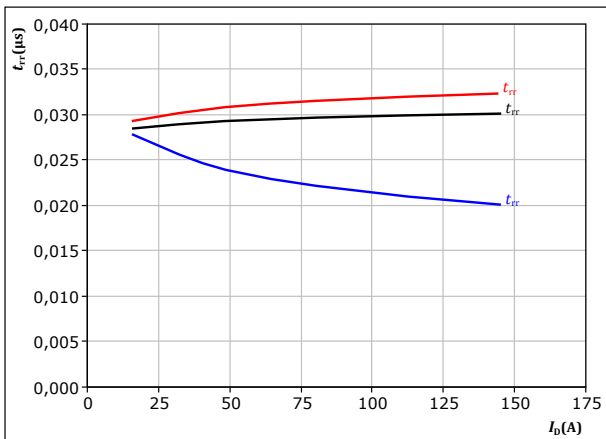
Typical switching times as a function of gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150$ °C
 $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $I_D = 80$ A

figure 13. MOSFET

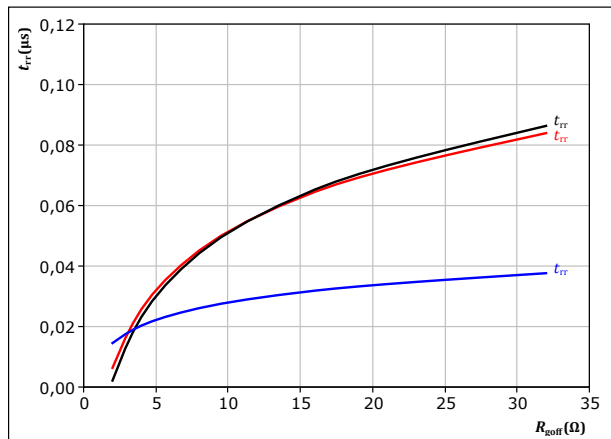
Typical reverse recovery time as a function of drain current
 $t_{rr} = f(I_D)$



At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $R_{g(on)} = 4$ Ω
 T_j : — 25 °C
— 125 °C
— 150 °C

figure 14. MOSFET

Typical reverse recovery time as a function of turn off gate resistor
 $t_{rr} = f(R_{g(off)})$



At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $I_D = 80$ A
 T_j : — 25 °C
— 125 °C
— 150 °C

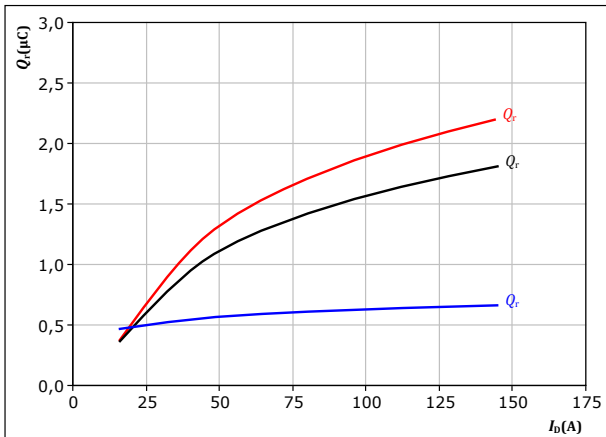


H-Bridge Switching Characteristics

figure 15. MOSFET

Typical recovered charge as a function of drain current

$$Q_r = f(I_D)$$



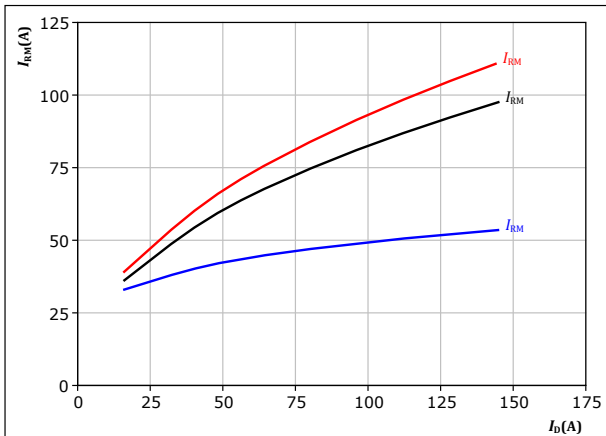
At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $R_{goff} = 4$ Ω

T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)

figure 17. MOSFET

Typical peak reverse recovery current as a function of drain current

$$I_{RM} = f(I_D)$$



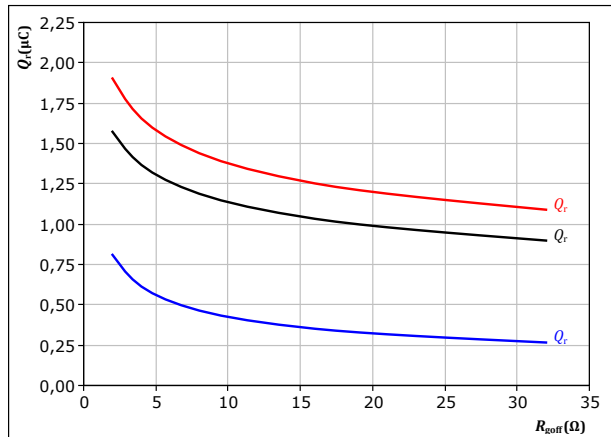
At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $R_{goff} = 4$ Ω

T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)

figure 16. MOSFET

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



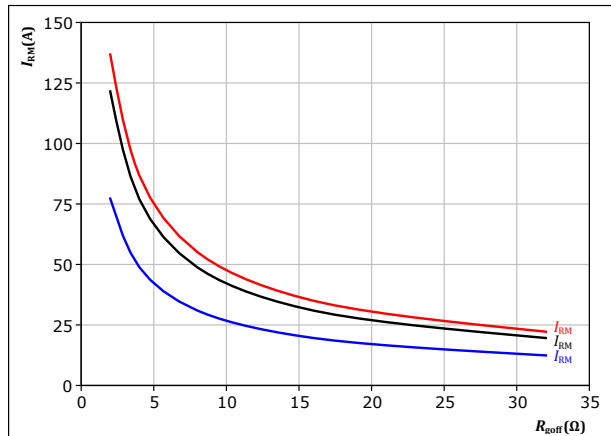
At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $I_D = 80$ A

T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)

figure 18. MOSFET

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $I_D = 80$ A

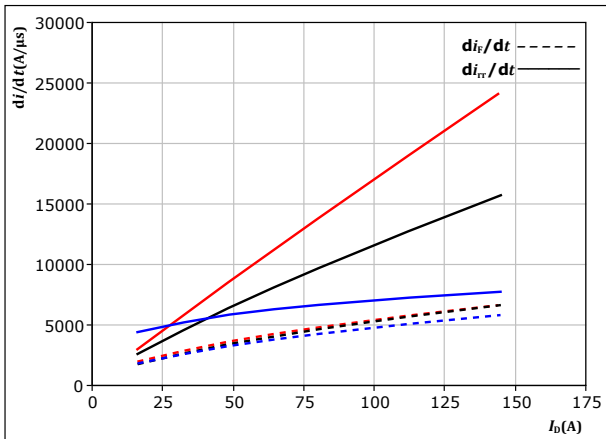
T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)



H-Bridge Switching Characteristics

figure 19. MOSFET

Typical rate of fall of forward and reverse recovery current as a function of drain current
 $di_f/dt, di_{rr}/dt = f(I_D)$

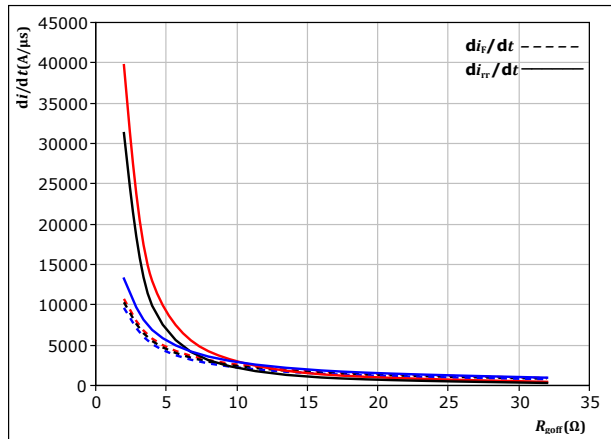


At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $R_{goff} = 4$ Ω

T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)

figure 20. MOSFET

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{goff})$



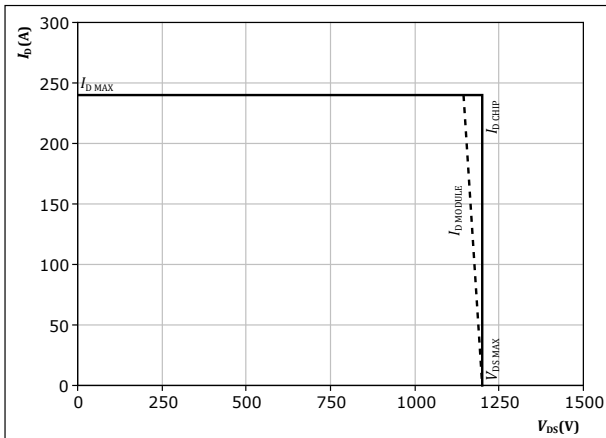
At $V_{DS} = 600$ V
 $V_{GS} = -4/15$ V
 $I_D = 80$ A

T_j : 25 °C (blue)
 125 °C (black)
 150 °C (red)

figure 21. MOSFET

Reverse bias safe operating area

$I_D = f(V_{DS})$



At $T_j = 150$ °C
 $R_{goff} = 4$ Ω
 $R_{goff} = 4$ Ω



H-Bridge Switching Definitions

figure 22. MOSFET

Turn-off Switching Waveforms & definition of t_{doff} t_{Eoff} (t_{Eoff} = integrating time for E_{off})

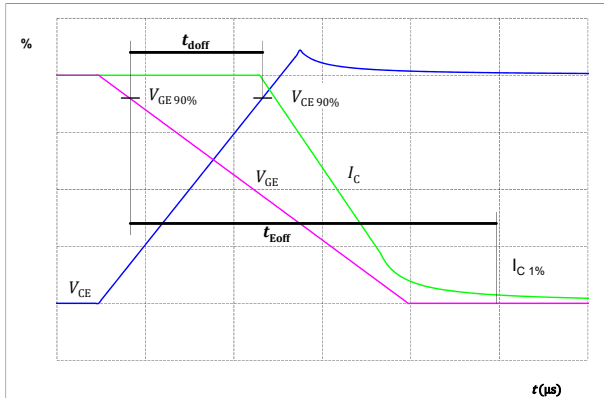


figure 23. MOSFET

Turn-on Switching Waveforms & definition of t_{don} t_{Eon} (t_{Eon} = integrating time for E_{on})

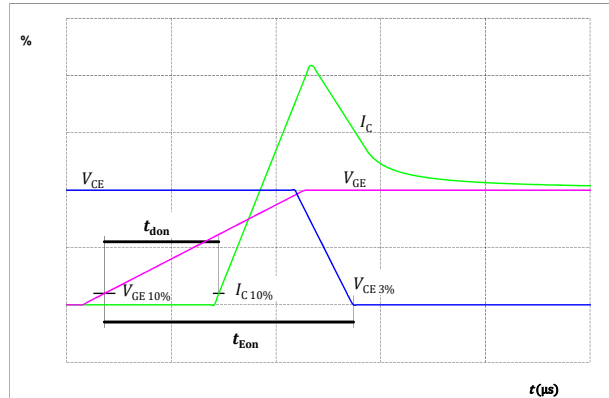


figure 24. MOSFET

Turn-off Switching Waveforms & definition of t_f

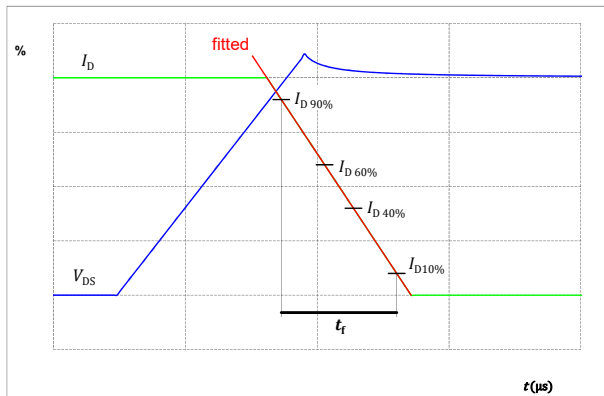
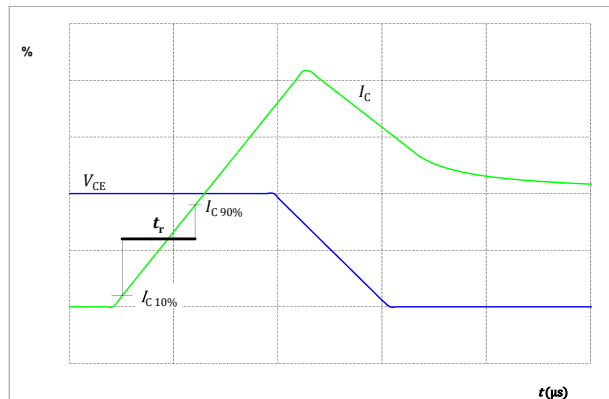


figure 25. MOSFET

Turn-on Switching Waveforms & definition of t_r





H-Bridge Switching Definitions

figure 26. FWD

Turn-off Switching Waveforms & definition of t_{tr}

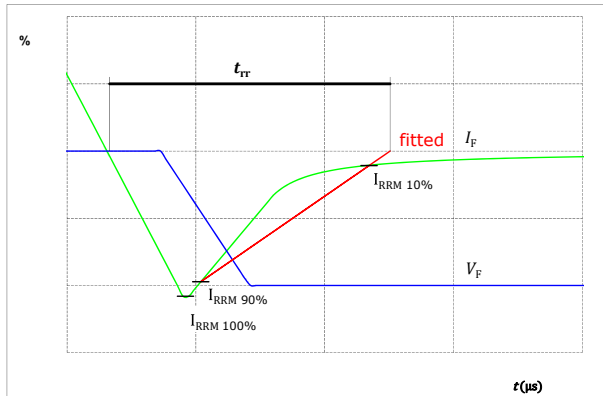


figure 27. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)

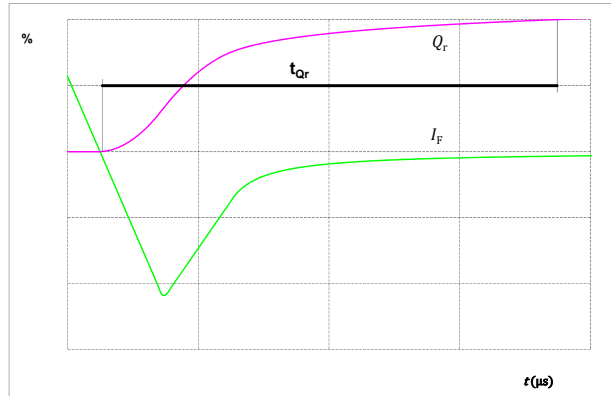
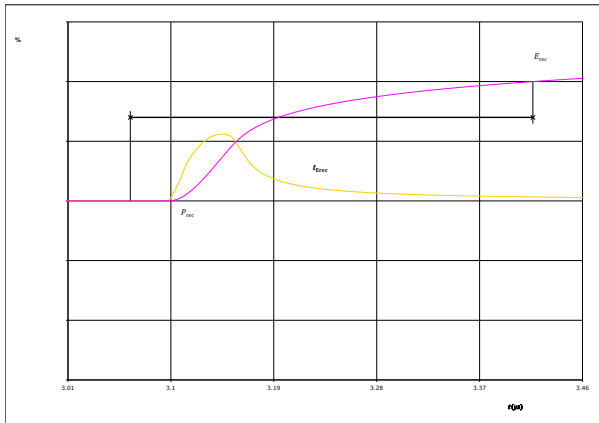


figure 28. FWD

Turn-on Switching Waveforms & definition of t_{Erec} (t_{Erec} = integrating time for E_{rec})






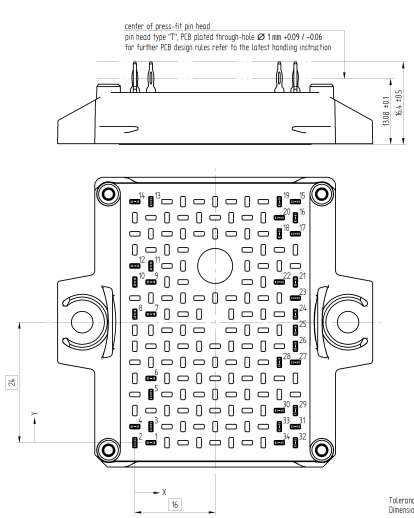
Vincotech

10-EY124PA011ME-LP40F18T
datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-EY124PA011ME-LP40F18T
With thermal paste (3,4 W/mK, PSX-P7)	10-EY124PA011ME-LP40F18T-/3/

Marking						
	Text	Name NN-NNNNNNNNNNNNNN- TTTTTVV	Date code WWYY	UL & VIN UL VIN	Lot LLLLL	Serial SSSS
	Datamatrix	Type&Ver TTTTTTTV	Lot number LLLLL	Serial SSSS	Date code WWYY	

Outline				
Pin table [mm]				
Pin	X	Y	Function	
1	3,2	0	AC2	
2	0	0	AC2	
3	3,2	3,2	AC2	
4	0	3,2	AC2	
5	3,2	9,6	S3	
6	3,2	12,8	G3	
7	3,2	25,6	S1	
8	0	25,6	G1	
9	3,2	32	AC1	
10	0	32	AC1	
11	3,2	35,2	AC1	
12	0	35,2	AC1	
13	3,2	48	T1	
14	0	48	T2	
15	32	48	DC-1	
16	32	44,8	DC-1	
17	32	41,6	DC-1	
18	28,8	41,6	DC-1	
19	28,8	48	G2	
20	28,8	44,8	S2	
21	32	32	DC+	
22	28,8	32	DC+	
23	32	28,8	DC+	
24	32	25,6	DC+	
25	32	22,4	DC+	
26	32	19,2	DC+	
27	32	16	DC+	
28	28,8	16	DC+	
29	32	6,4	DC-2	
30	28,8	6,4	DC-2	
31	32	3,2	DC-2	
32	32	0	DC-2	
33	28,8	3,2	S4	
34	28,8	0	G4	

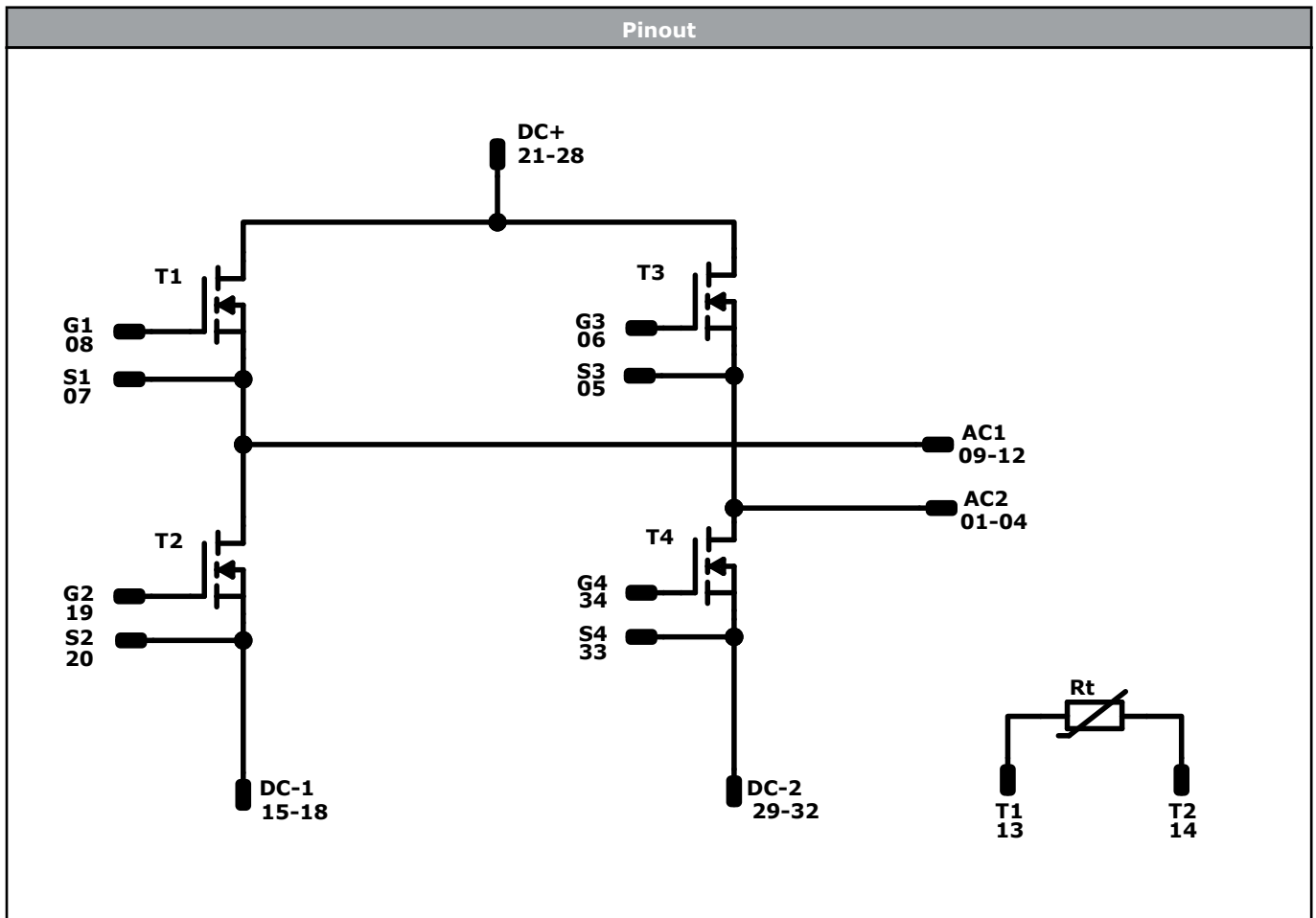


center of press-fit pin head
pin head type "1", PCB plated through-hole $\varnothing 1\text{mm} \pm 0,09\text{mm}$
for further PCB design rules refer to the latest handling instruction

Tolerance of pinposition: $\pm 0,4\text{mm}$ at the end of pins
Dimension of coordinate axis is only offset without tolerance



Vincotech



Identification					
ID	Component	Voltage	Current	Function	Comment
T2, T1, T4, T3	MOSFET	1200 V	10,67 mΩ	H-Bridge Switch	
Rt	Thermistor			Thermistor	




Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.

Package data
Package data for <i>flow</i> E2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website. 

Document No.:	Date:	Modification:	Pages
10-EY124PA011ME-LP40F18T-D1-14	4 Nov. 2021	Initial Release	

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.