Vincotech

10-PC094PC035ME03-L629F46Y
datasheet

fastPACK 0 SiC **900 V / 35 mΩ**

Topology features

- Dual halfbridge
- Integrated DC capacitor
- Kelvin Emitter for improved switching performance
- Open Emitter configuration
- Temperature sensor

Component features

- High Blocking Voltage with low drain source on state resistance
- High speed SiC-MOSFET technology
- Resistant to Latch-up

Housing features

- Base isolation: Al_2O_3
- Convex shaped substrate for superior thermal contact
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

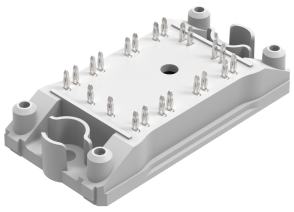
Target applications

- Power Supply

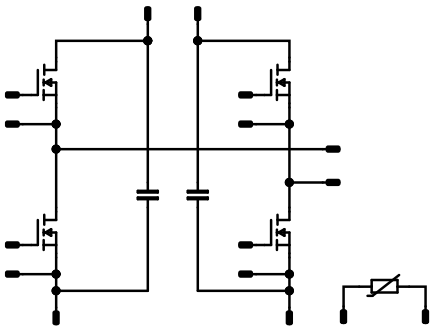
Types

- 10-PC094PC035ME03-L629F46Y

flow 0 12 mm housing



Schematic



Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
H-Bridge Switch				
Drain-source voltage	V_{DSS}		900	V
Drain current (DC current)	I_D	T_j = T_{jmax} T_s = 80 °C	41	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	180	A
Total power dissipation	P_{tot}	T_j = T_{jmax} T_s = 80 °C	76	W
Gate-source voltage	V_{GSS}		-4 / 15	V
		dynamic	-8 / 19	
Maximum Junction Temperature	T_{jmax}		175	°C
Capacitor (DC)				
Maximum DC voltage	V_{MAX}		1000	V
Operation Temperature	T_{op}		-55 ... 125	°C
Module Properties				
Thermal Properties				
Storage temperature	T_{stg}		-40…+125	°C
Operation temperature under switching condition	T_{jop}		-40…+(T_{jmax} - 25)	°C
Isolation Properties				
Isolation voltage	V_{isol}	DC Test Voltage* t_p = 2 s	4000	V
Isolation voltage	V_{isol}	AC Voltage t_p = 1 min	2500	V
Creepage distance			>12,7	mm
Clearance			9,19	mm
Comparative Tracking Index	CTI		≥ 200	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
H-Bridge Switch										
Static										
Drain-source on-state resistance	$r_{DS(on)}$		15		40	25 125 150		32,3 39,3 43	39(1)	mΩ
Gate-source threshold voltage	$V_{GS(th)}$		0		0,01	25	1,8	2,1	3,5	V
Gate to Source Leakage Current	I_{GSS}		15	0		25		20	500	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	900		25		2	200	µA
Internal gate resistance	r_g							1,75		Ω
Gate charge	Q_g							66		
Gate to source charge	Q_{GS}		-4/15	400	40	25		20		nC
Gate to drain charge	Q_{GD}							20		
Short-circuit input capacitance	C_{iss}							1520		
Short-circuit output capacitance	C_{oss}	f = 1 Mhz	0	600	0	25		140		pF
Reverse transfer capacitance	C_{rss}							10		
Diode forward voltage	V_{SD}		0		0	25		4,5		V
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,25		K/W

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 4 Ω R_{goff} = 4 Ω	-5/15	600	40	25 125 150		12,8 12,8 13,6		ns
Rise time	t_r					25 125 150		5,4 5 5		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		42,6 42,8 43,4		ns
Fall time	t_f					25 125 150		10,9 11,7 10,8		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=0,455 µC Q_{rFWD}=0,875 µC Q_{rFWD}=0,825 µC				25 125 150		0,459 0,447 0,471		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,082 0,055 0,048		mWs
Peak recovery current	I_{RRM}	di/dt=7340 A/µs di/dt=7860 A/µs di/dt=8440 A/µs				25 125 150		53,52 58,36 62,77		A
Reverse recovery time	t_{rr}					25 125 150		14,5 14,6 15,4		ns
Recovered charge	Q_r					25 125 150		0,455 0,875 0,825		µC
Reverse recovered energy	E_{rec}					25 125 150		0,028 0,196 0,106		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		11000 13700 15900		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Capacitor (DC)										
Static										
Capacitance	C	DC bias voltage = 0 V				25		94		nF
Tolerance							-20		20	%
Dissipation factor		f = 1 kHz				25		25		%
Thermistor										
Static										
Rated resistance	R					25		22		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	R_{100} = 1484 Ω				100	-5		5	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,5		mW/K
B-value	$B_{(25/50)}$	Tol. ±1 %						3962		K
B-value	$B_{(25/100)}$	Tol. ±1 %						4000		K
Vincotech Thermistor Reference									I	

(1) Value at chip level

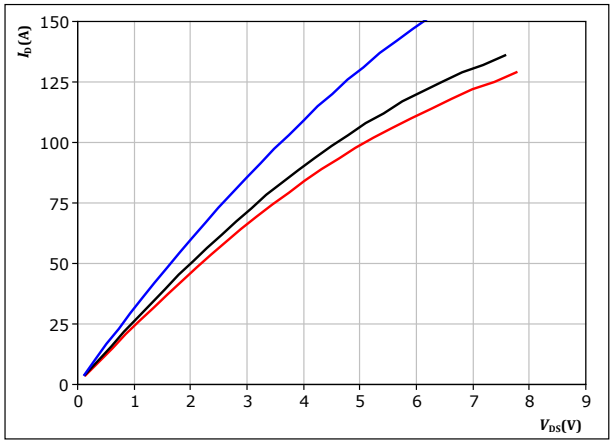
(2) Only valid with pre-applied Vincotech thermal interface material.

H-Bridge Switch Characteristics

figure 1. **MOSFET**

Typical output characteristics

$I_D = f(V_{DS})$

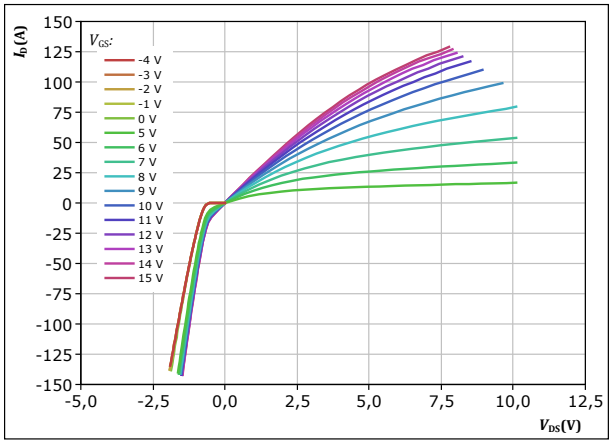


t_p =	250	µs		
V_{GS} =	15	V	T_j:	25 °C
				125 °C
				150 °C

figure 2. **MOSFET**

Typical output characteristics

$I_D = f(V_{DS})$



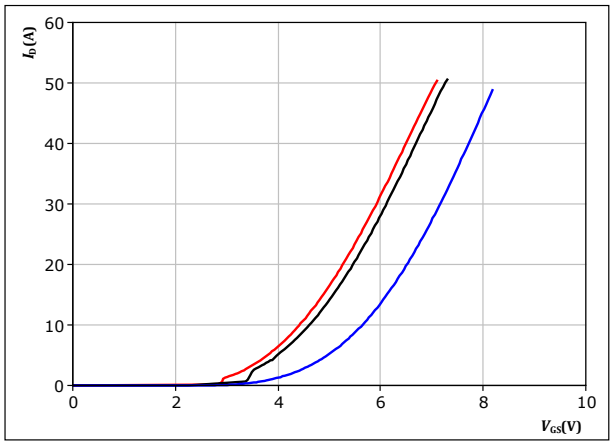
t_p =	250	µs
T_j =	150	°C

V_{GS} from -4 V to 15 V in steps of 1 V

figure 3. **MOSFET**

Typical transfer characteristics

$I_D = f(V_{GS})$

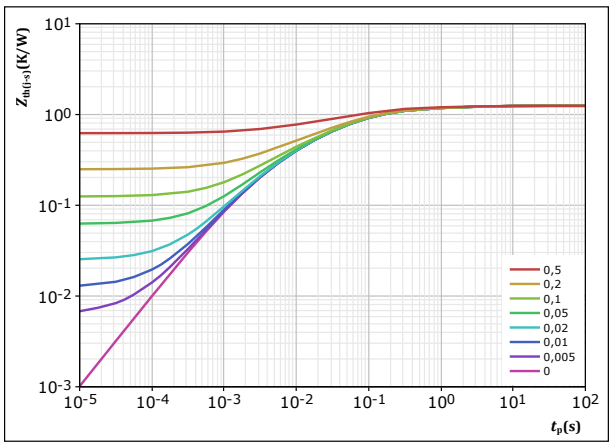


t_p =	250	µs		
V_{DS} =	10	V	T_j:	25 °C
				125 °C
				150 °C

figure 4. **MOSFET**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D =	t_p / T	
$R_{th(j-s)}$ =	1,247	K/W

MOSFET thermal model values

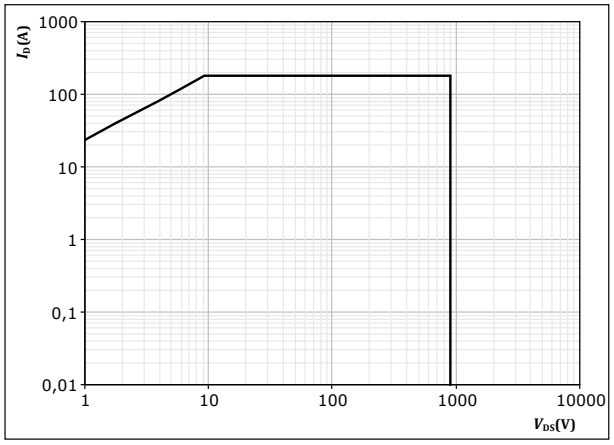
R (K/W)	τ (s)
6,45E-02	3,56E+00
1,36E-01	5,08E-01
4,22E-01	9,62E-02
3,45E-01	2,46E-02
2,11E-01	5,94E-03
6,79E-02	1,44E-03

H-Bridge Switch Characteristics

figure 5. **MOSFET**

Safe operating area

$I_D = f(V_{DS})$

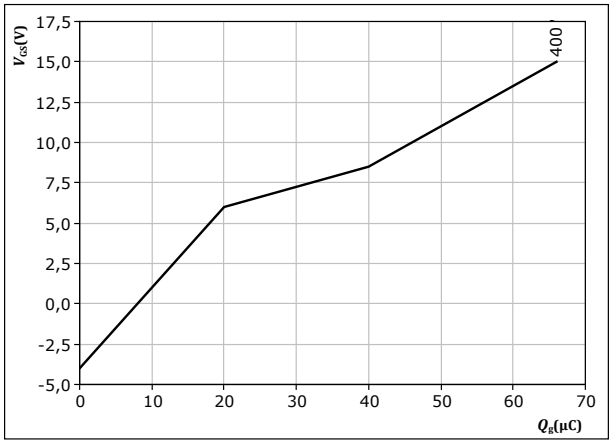


$D =$ single pulse
$T_s =$ 80 °C
$V_{GS} =$ 15 V
$T_j =$ T_{jmax}

figure 6. **MOSFET**

Gate voltage vs gate charge

$V_{GS} = f(Q_g)$



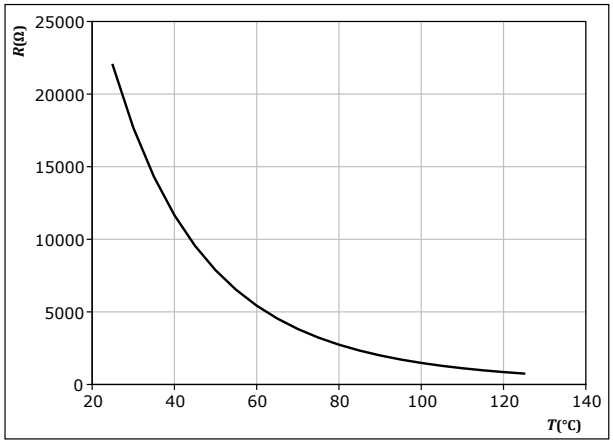
$I_D =$ 20 A
$T_j =$ 25 °C

Thermistor Characteristics

figure 7. **Thermistor**

Typical NTC characteristic as function of temperature

$R_T = f(T)$

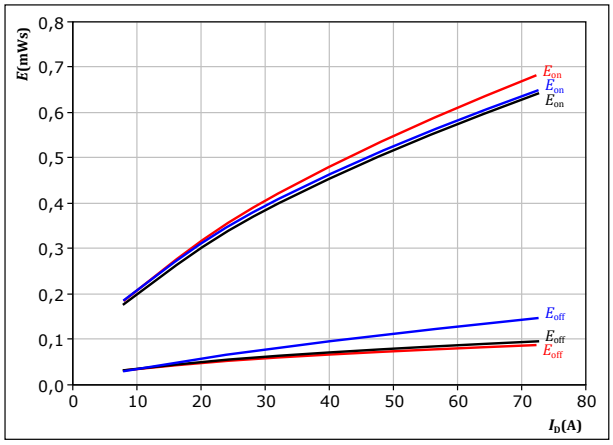


H-Bridge Switching Characteristics

figure 8. **MOSFET**

Typical switching energy losses as a function of drain current

$E = f(I_D)$



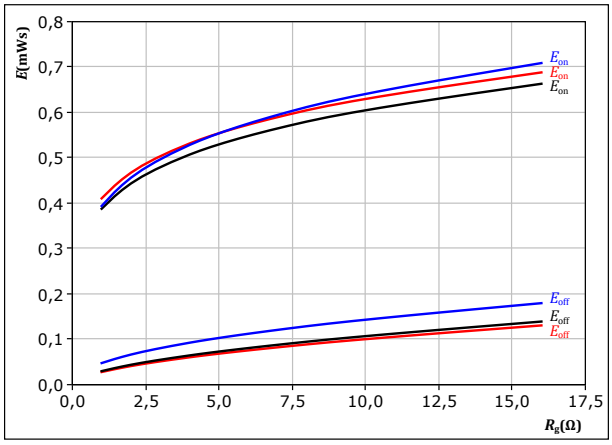
With an inductive load at

			T_j:	
V_{DS} =	600	V		25 °C
V_{GS} =	-5/15	V		125 °C
R_{gon} =	4	Ω		150 °C
R_{goff} =	4	Ω		

figure 9. **MOSFET**

Typical switching energy losses as a function of MOSFET turn on gate resistor

$E = f(R_g)$



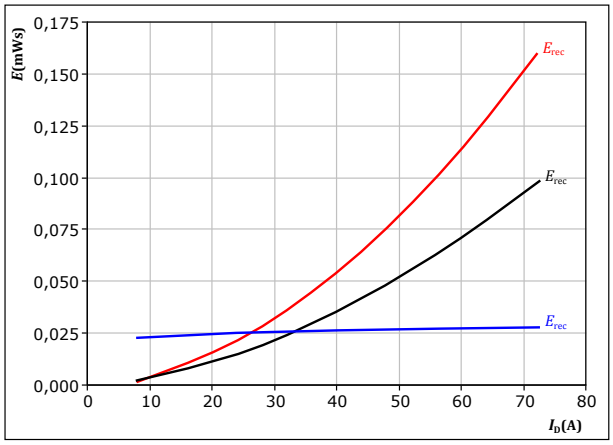
With an inductive load at

			T_j:	
V_{DS} =	600	V		25 °C
V_{GS} =	-5/15	V		125 °C
I_D =	40	A		150 °C

figure 10. **MOSFET**

Typical reverse recovered energy loss as a function of drain current

$E_{rec} = f(I_D)$



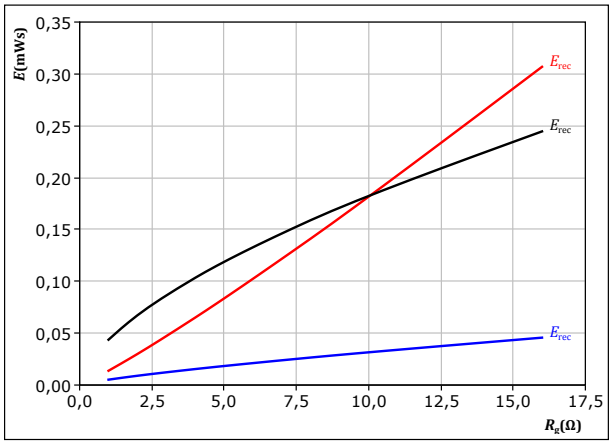
With an inductive load at

			T_j:	
V_{DS} =	600	V		25 °C
V_{GS} =	-5/15	V		125 °C
R_{gon} =	4	Ω		150 °C

figure 11. **MOSFET**

Typical reverse recovered energy loss as a function of MOSFET turn on gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

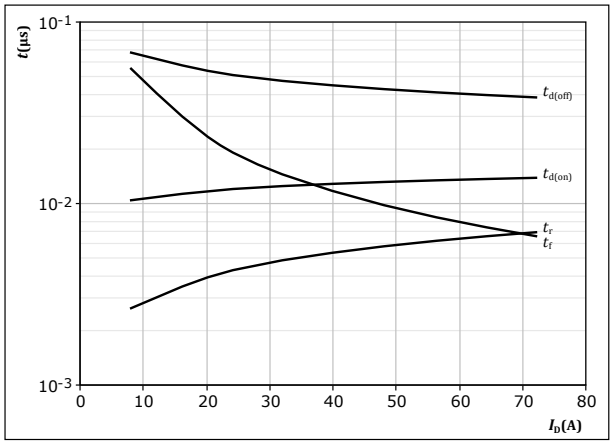
			T_j:	
V_{DS} =	600	V		25 °C
V_{GS} =	-5/15	V		125 °C
I_D =	40	A		150 °C

H-Bridge Switching Characteristics

figure 12. **MOSFET**

Typical switching times as a function of drain current

$t = f(I_D)$



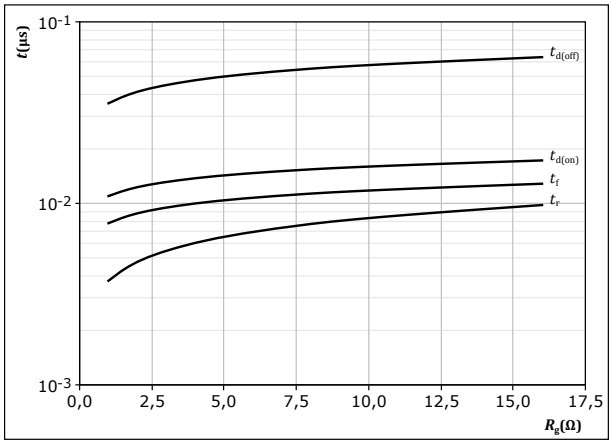
With an inductive load at

$T_j =$	150	°C
$V_{DS} =$	600	V
$V_{GS} =$	-5/15	V
$R_{gon} =$	4	Ω
$R_{goff} =$	4	Ω

figure 13. **MOSFET**

Typical switching times as a function of MOSFET turn on gate resistor

$t = f(R_g)$



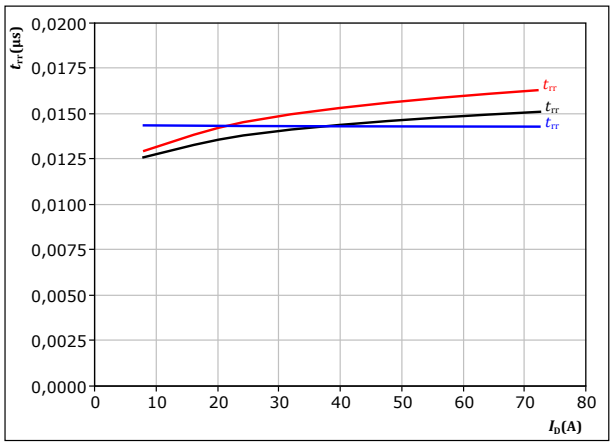
With an inductive load at

$T_j =$	150	°C
$V_{DS} =$	600	V
$V_{GS} =$	-5/15	V
$I_D =$	40	A

figure 14. **MOSFET**

Typical reverse recovery time as a function of drain current

$t_{rr} = f(I_D)$



At

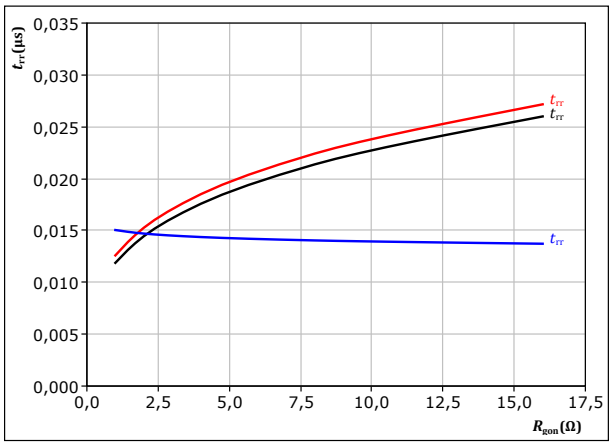
$V_{DS} =$	600	V
$V_{GS} =$	-5/15	V
$R_{gon} =$	4	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 15. **MOSFET**

Typical reverse recovery time as a function of MOSFET turn on gate resistor

$t_{rr} = f(R_{gon})$



At

$V_{DS} =$	600	V
$V_{GS} =$	-5/15	V
$I_D =$	40	A

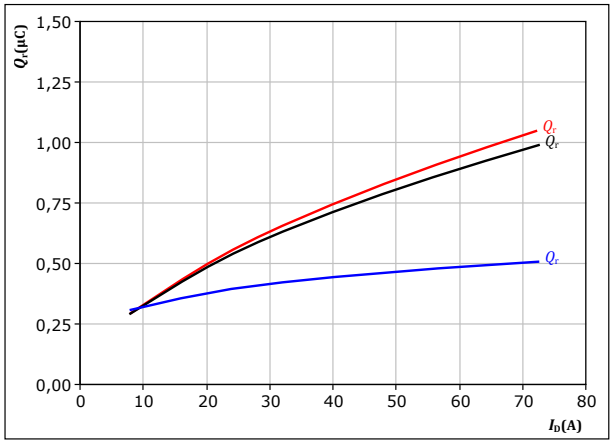
T_j: 25 °C, 125 °C, 150 °C

H-Bridge Switching Characteristics

figure 16. **MOSFET**

Typical recovered charge as a function of drain current

$Q_r = f(I_D)$

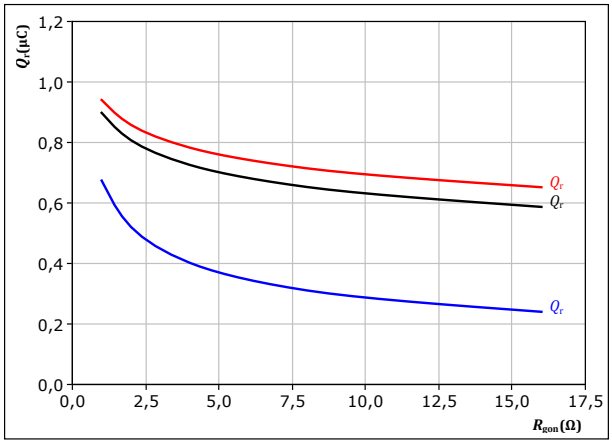


At					
	V_{DS} =	600	V		
	V_{GS} =	-5/15	V	T_j:	25 °C
	R_{gon} =	4	Ω		125 °C
					150 °C

figure 17. **MOSFET**

Typical recovered charge as a function of MOSFET turn on gate resistor

$Q_r = f(R_{gon})$

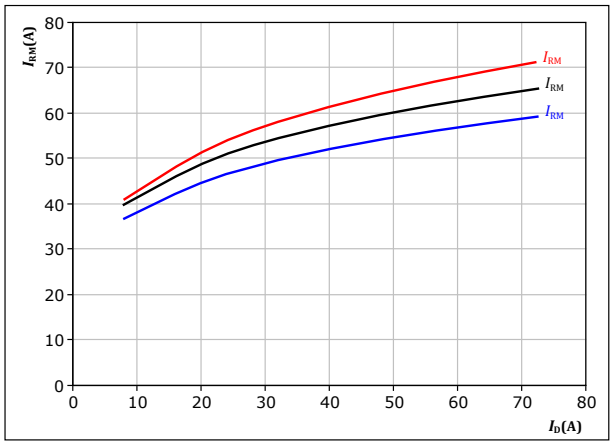


At					
	V_{DS} =	600	V		
	V_{GS} =	-5/15	V	T_j:	25 °C
	I_D =	40	A		125 °C
					150 °C

figure 18. **MOSFET**

Typical peak reverse recovery current as a function of drain current

$I_{RM} = f(I_D)$

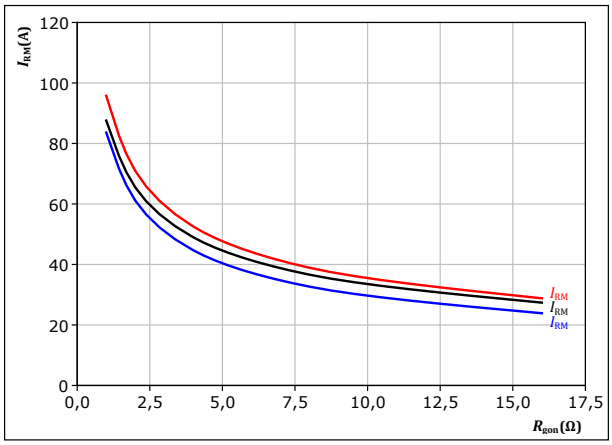


At					
	V_{DS} =	600	V		
	V_{GS} =	-5/15	V	T_j:	25 °C
	R_{gon} =	4	Ω		125 °C
					150 °C

figure 19. **MOSFET**

Typical peak reverse recovery current as a function of MOSFET turn on gate resistor

$I_{RM} = f(R_{gon})$



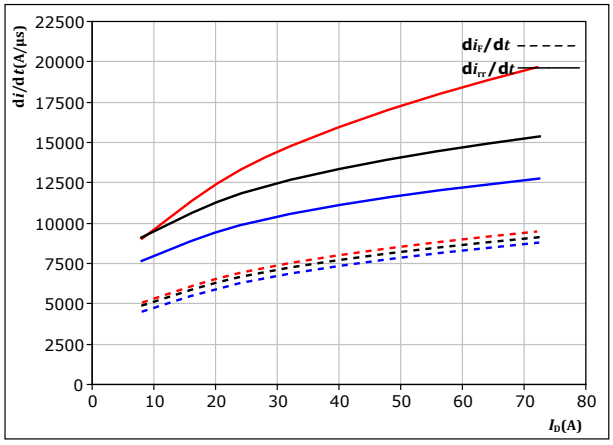
At					
	V_{DS} =	600	V		
	V_{GS} =	-5/15	V	T_j:	25 °C
	I_D =	40	A		125 °C
					150 °C

H-Bridge Switching Characteristics

figure 20. **MOSFET**

Typical rate of fall of forward and reverse recovery current as a function of drain current

di_F/dt, di_{rr}/dt = f(I_D)

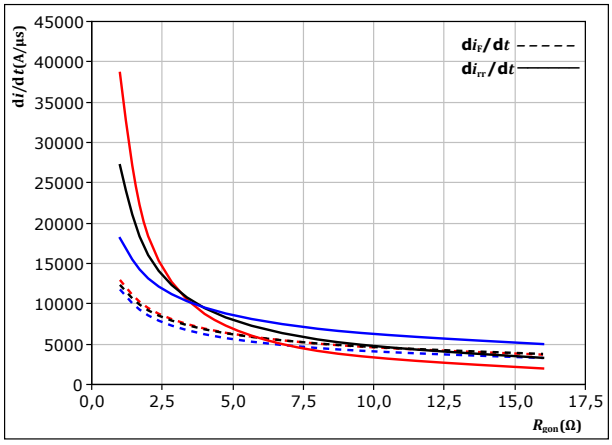


At				
	V_{DS} =	600	V	
	V_{GS} =	-5/15	V	
	R_{gon} =	4	Ω	
	T_j:			25 °C
				125 °C
				150 °C

figure 21. **MOSFET**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

di_F/dt, di_{rr}/dt = f(R_{gon})

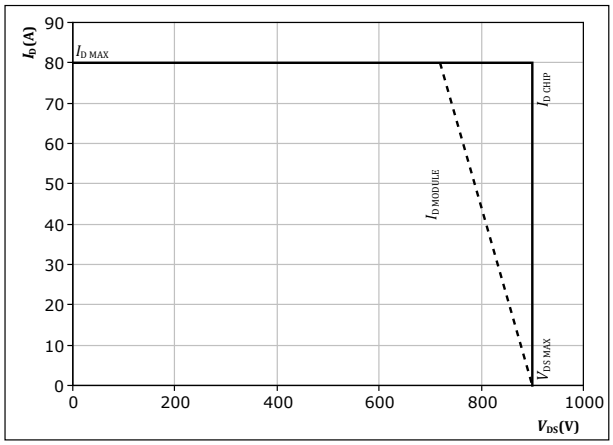


At				
	V_{DS} =	600	V	
	V_{GS} =	-5/15	V	
	I_D =	40	A	
	T_j:			25 °C
				125 °C
				150 °C

figure 22. **MOSFET**

Reverse bias safe operating area

I_D = f(V_{DS})



At			
	T_j =	150	°C
	R_{gon} =	4	Ω
	R_{goff} =	4	Ω

H-Bridge Switching Definitions

figure 23. **MOSFET**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{Eoff} = integrating time for E_{off})

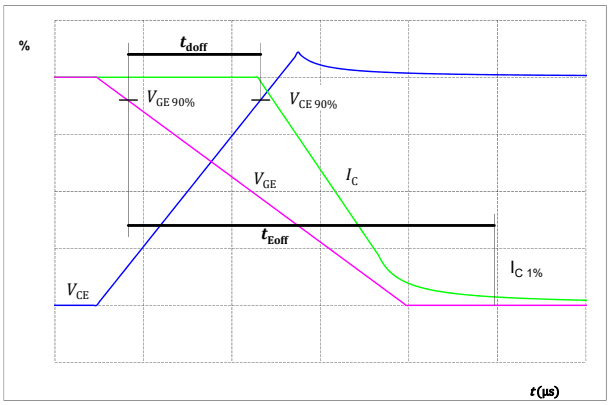


figure 24. **MOSFET**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} = integrating time for E_{on})

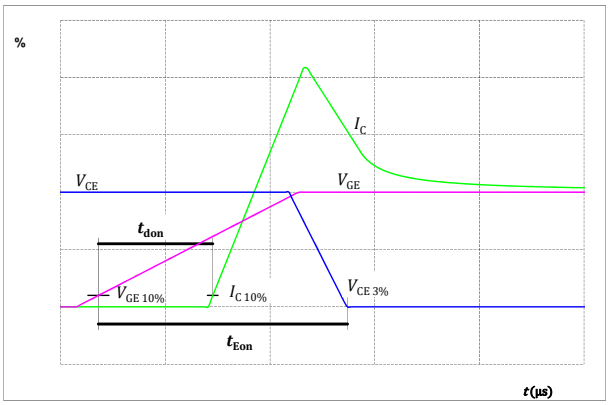


figure 25. **MOSFET**

Turn-off Switching Waveforms & definition of t_f

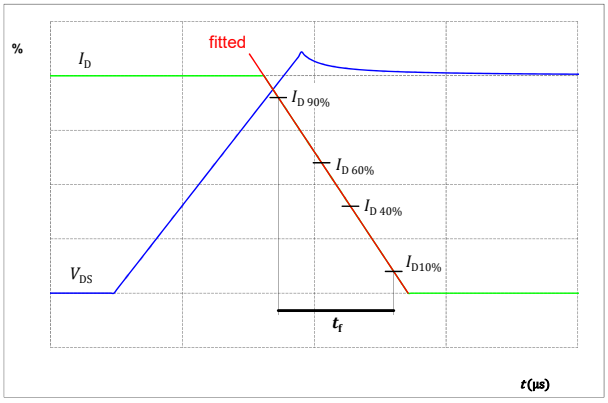
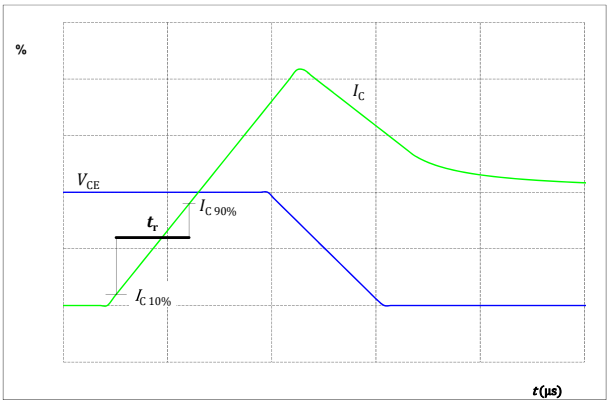


figure 26. **MOSFET**

Turn-on Switching Waveforms & definition of t_r



H-Bridge Switching Definitions

figure 27. **FWD**

Turn-off Switching Waveforms & definition of t_{trr}

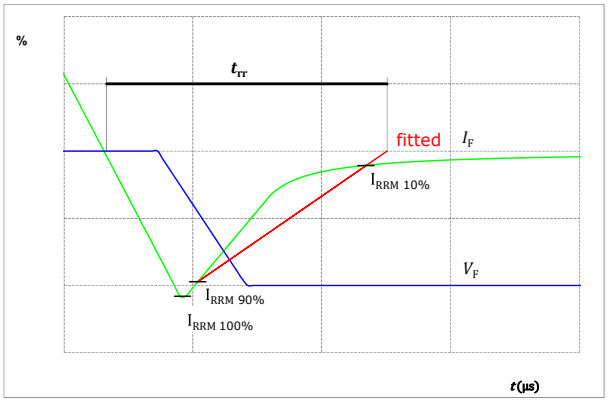


figure 28. **FWD**

Turn-on Switching Waveforms & definition of t_{Qrr} (t_{Qrr} = integrating time for Q_{rr})

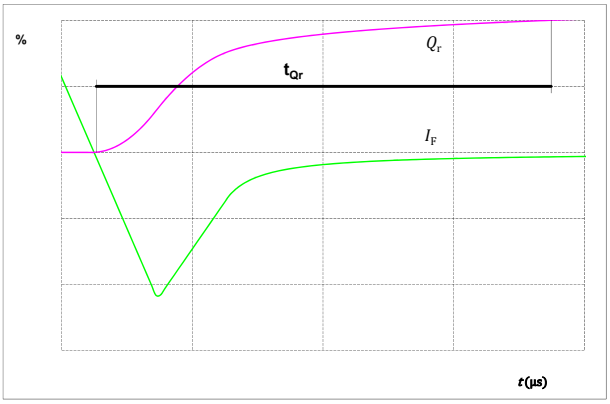
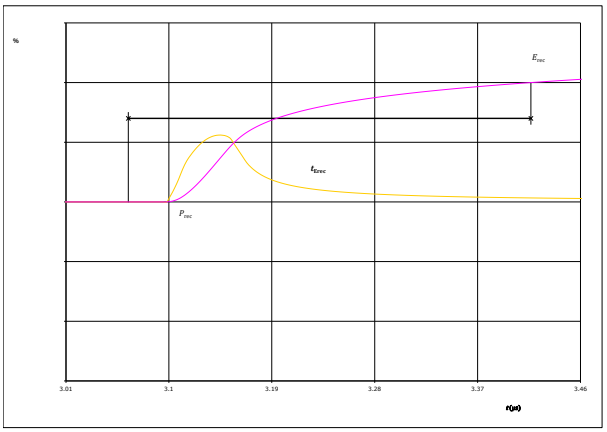


figure 29. **FWD**

Turn-on Switching Waveforms & definition of t_{Erec} (t_{Erec} = integrating time for E_{rec})



Ordering Code

Version	Ordering Code
Without thermal paste	10-PC094PC035ME03-L629F46Y
With thermal paste (5,2 W/mK, PTM6000HV)	10-PC094PC035ME03-L629F46Y-/7/
With thermal paste (3,4 W/mK, PSX-P7)	10-PC094PC035ME03-L629F46Y-/3/

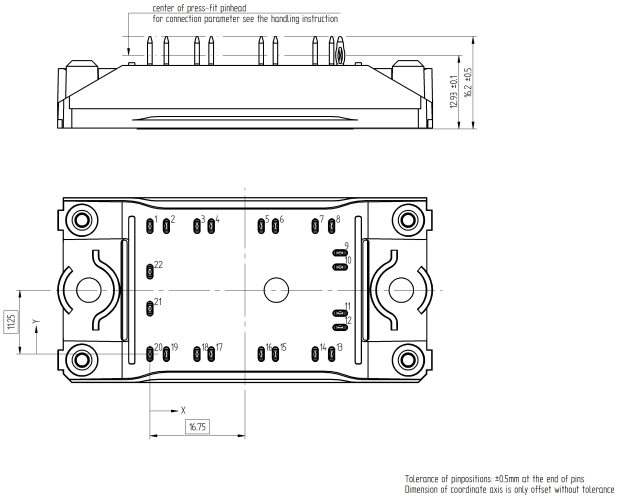
Marking

	Name		Date code	UL & VIN	Lot	Serial
Text	NN-NNNNNNNNNNNNN-TTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	**Type&Ver**	**Lot number**	**Serial**	**Date code**		
	TTTTTTTVV	LLLLL	SSSS	WWYY		

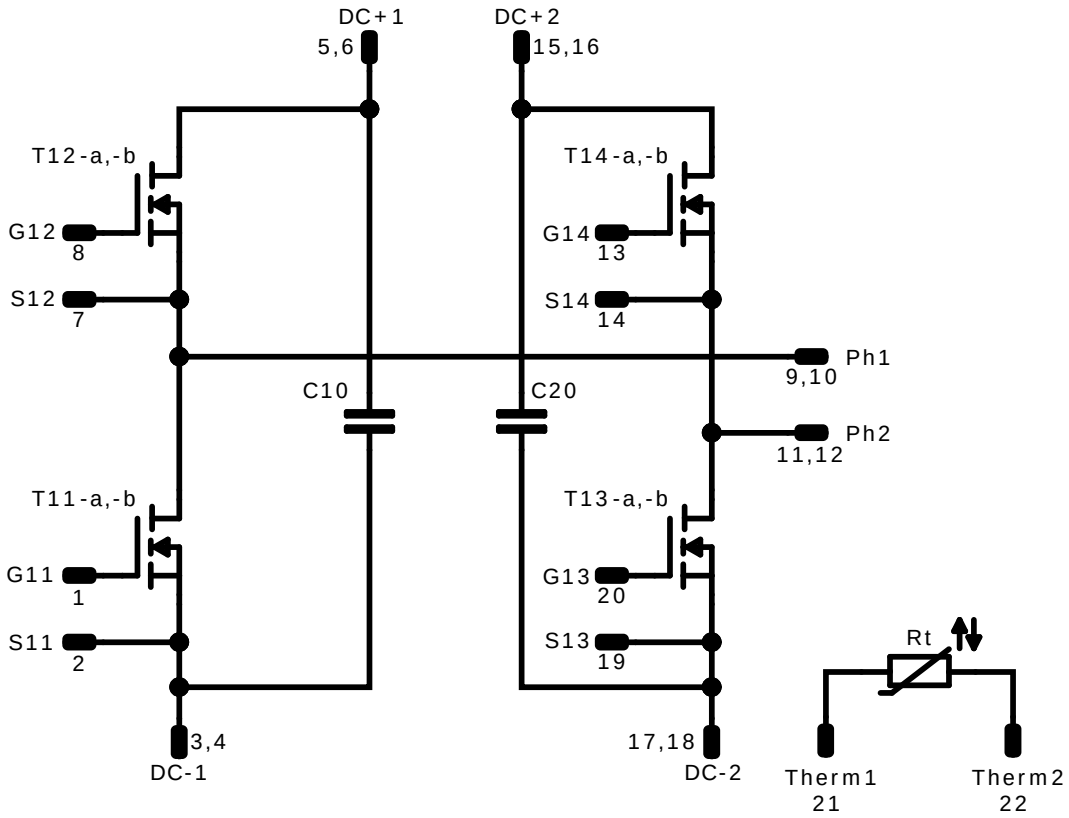
Outline

Pin table [mm]

Pin	X	Y	Function
1	0	22,5	G11
2	2,9	22,5	S11
3	8,3	22,5	DC-1
4	10,8	22,5	DC-1
5	19,6	22,5	DC+1
6	22,1	22,5	DC+1
7	29,1	22,5	S12
8	32	22,5	G12
9	33,5	17,8	Ph1
10	33,5	15,3	Ph1
11	33,5	7,2	Ph2
12	33,5	4,7	Ph2
13	32	0	G14
14	29,1	0	S14
15	22,1	0	DC+2
16	19,6	0	DC+2
17	10,8	0	DC-2
18	8,3	0	DC-2
19	2,9	0	S13
20	0	0	G13
21	0	8	Therm1
22	0	14,5	Therm2



Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14	MOSFET	900 V	32,5 mΩ	H-Bridge Switch	
C10, C20	Capacitor	1000 V		Capacitor (DC)	
Rt	NTC			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow* 0 packages see vincotech.com website.

Package data
Package data for *flow* 0 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
10-PC094PC035ME03-L629F46Y-D1-14	1 May. 2022		