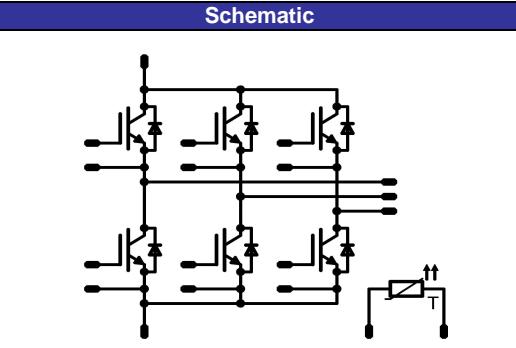


MiniSKiiP®2 PACK		1200V / 25A
<b>Features</b>	• Solderless interconnection • Trench Fieldstop IGBT4 technology	<b>MiniSKiiP® 2 housing</b> 
<b>Target Applications</b>	• Servo Drives • Industrial Motor Drives • UPS	<b>Schematic</b> 
<b>Types</b>	• V23990-K237-F40-PM	

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>T1,T2,T3,T4,T5,T6</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 40	A
Repetitive peak collector current	$I_{Cpulse}$	$t_p$ limited by $T_{j\max}$	75	A
Power dissipation	$P_{tot}$	$T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	89 135	W
Gate-emitter peak voltage	$V_{GE}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	$\pm 20$	V
Short circuit ratings	$t_{SC}$ $V_{CC}$		10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>D1,D2,D3,D4,D5,D6</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	25 32	A
Repetitive peak forward current	$I_{FRM}$	$t_p=10\text{ms}$ half sine	160	A
Power dissipation	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	62 95	W
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$

## Thermal Properties

Storage temperature	$T_{stg}$		-40...+125	$^\circ\text{C}$
Operation temperature under switching condition	$T_{op}$		-40...+( $T_j\max - 25$ )	$^\circ\text{C}$

## Insulation Properties

Insulation voltage	$V_{is}$	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm

**Characteristic Values**

Parameter	Symbol	Conditions				Value			Unit
		$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	

**T1,T2,T3,T4,T5,T6**

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,00085	$T_j=25^\circ C$ $T_j=150^\circ C$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		25	$T_j=25^\circ C$ $T_j=150^\circ C$	1,35	1,88 2,2	2,15	V
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200		$T_j=25^\circ C$ $T_j=150^\circ C$			0,05	mA
Gate-emitter leakage current	$I_{GES}$		20	0		$T_j=25^\circ C$ $T_j=150^\circ C$			300	nA
Integrated Gate resistor	$R_{gint}$							-		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=32\ \Omega$ $R_{gon}=32\ \Omega$				$T_j=25^\circ C$ $T_j=150^\circ C$		112 113		ns
Rise time	$t_r$					$T_j=25^\circ C$ $T_j=150^\circ C$		29,3 34,7		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ C$ $T_j=150^\circ C$		231 303		
Fall time	$t_f$					$T_j=25^\circ C$ $T_j=150^\circ C$		91 137		
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ C$ $T_j=150^\circ C$		1,87 2,77		mWs
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ C$ $T_j=150^\circ C$		1,49 2,43		
Input capacitance	$C_{ies}$	$f=1MHz$						1430		pF
Output capacitance	$C_{oss}$		0	25		$T_j=25^\circ C$		115		
Reverse transfer capacitance	$C_{rss}$							85		
Gate charge	$Q_{Gate}$	$V_{CC}=960V$	$\pm 15$		40	$T_j=25^\circ C$		120		nC
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=1W/mK$						1,2		K/W

**D1,D2,D3,D4,D5,D6**

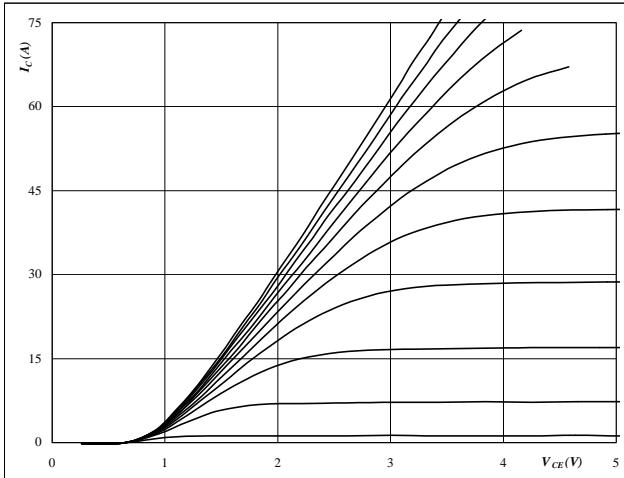
Diode forward voltage	$V_F$				25	$T_j=25^\circ C$ $T_j=150^\circ C$	1,5	2,47 2,49	2,75	V
Peak reverse recovery current	$I_{RRM}$					$T_j=25^\circ C$ $T_j=150^\circ C$		13,5 18,3		A
Reverse recovery time	$t_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$		319 544		ns
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$		1,48 3,69		$\mu C$
Peak rate of fall of recovery current	$di(rec)max/dt$					$T_j=25^\circ C$ $T_j=150^\circ C$		174 64		$A/\mu s$
Reverse recovered energy	$E_{rec}$					$T_j=25^\circ C$ $T_j=150^\circ C$		0,52 1,44		mWs
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=1W/mK$						1,52		K/W

**Thermistor**

Rated resistance	$R$					$T=25^\circ C$		1000		$\Omega$
Deviation of R100	$\Delta R/R$	$R_{100}=1670\ \Omega$				$T=100^\circ C$	-3		3	%
R100	$P$					$T=100^\circ C$		1670,313		$\Omega$
Power dissipation constant						$T=25^\circ C$				$mW/K$
A-value	$B(25/50)$	Tol. %				$T=25^\circ C$		7,635*10-3		$1/K$
B-value	$B(25/100)$	Tol. %				$T=25^\circ C$		1,731*10-5		$1/K^2$
Vincotech NTC Reference									E	

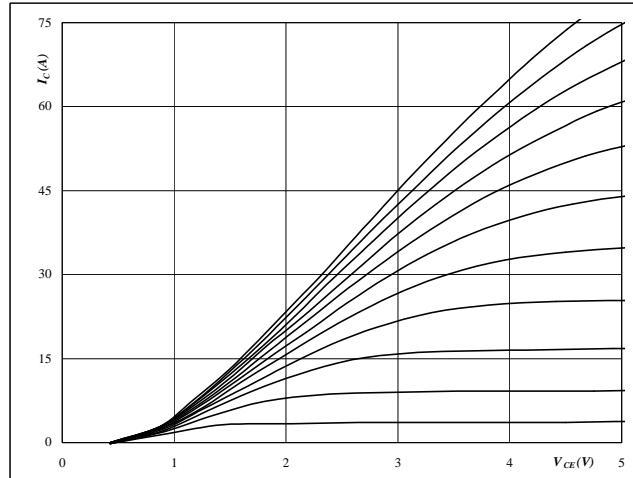
## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



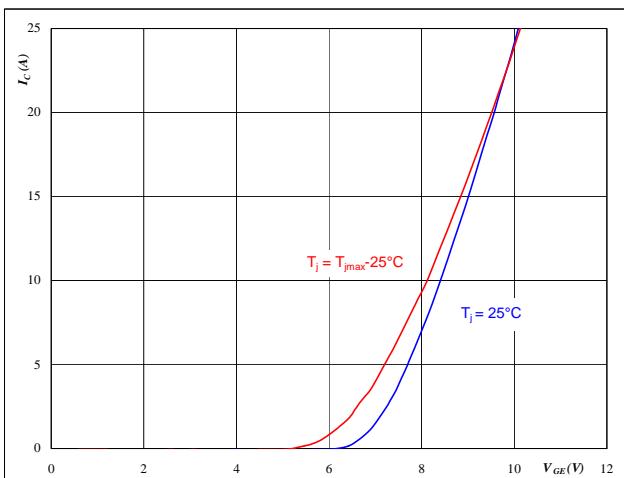
**At**  
 $t_p = 250 \mu s$   
 $T_j = 25 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



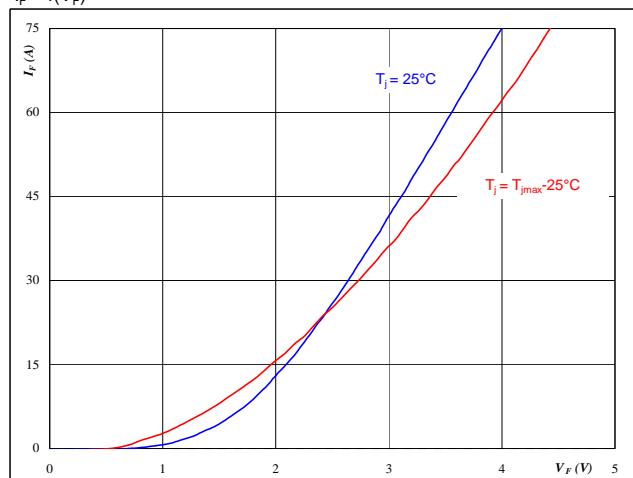
**At**  
 $t_p = 250 \mu s$   
 $T_j = 150 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



**At**  
 $t_p = 250 \mu s$

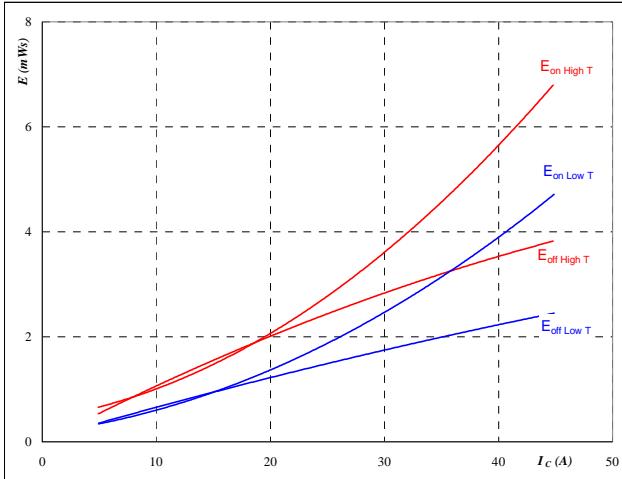
## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 5**

T1,T2,T3,T4,T5,T6 IGBT

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



With an inductive load at

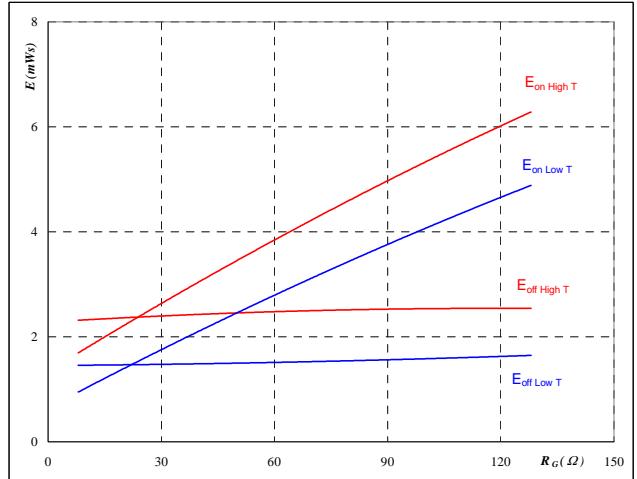
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Figure 6**

T1,T2,T3,T4,T5,T6 IGBT

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



With an inductive load at

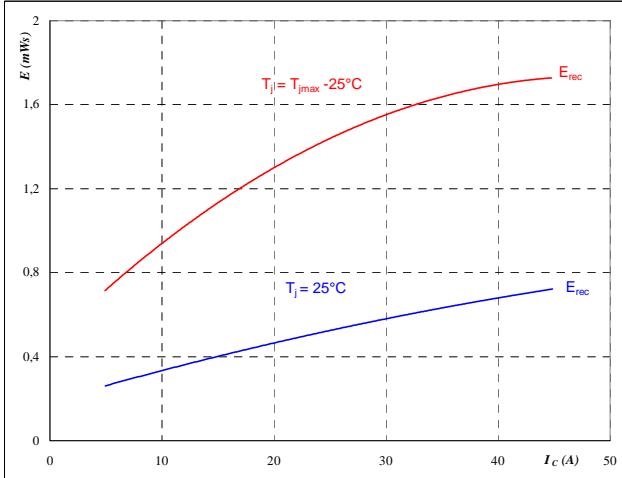
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 25 \quad \text{A} \end{aligned}$$

**Figure 7**

T1,T2,T3,T4,T5,T6 IGBT

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



With an inductive load at

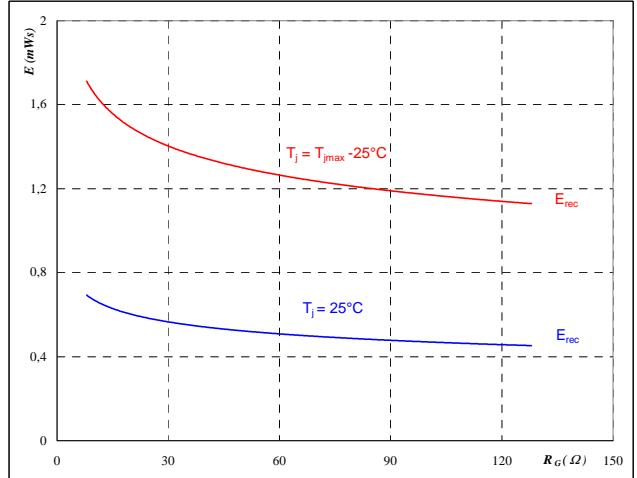
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Figure 8**

T1,T2,T3,T4,T5,T6 IGBT

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

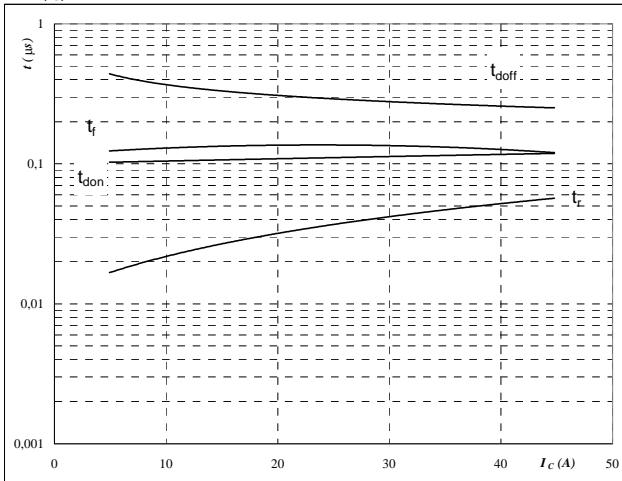
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 25 \quad \text{A} \end{aligned}$$

## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 9**

Typical switching times as a function of collector current

$$t = f(I_C)$$



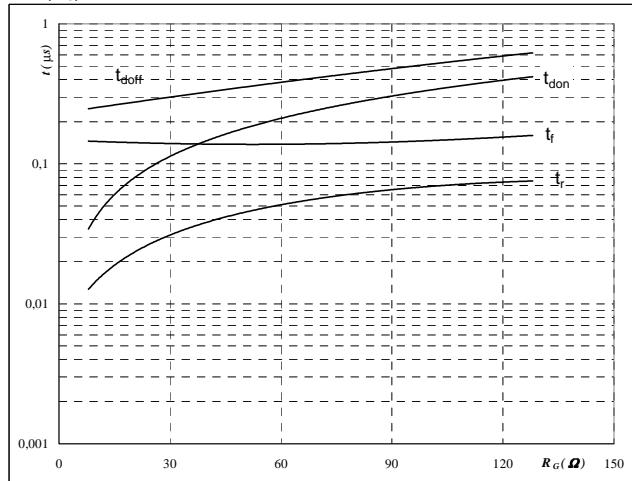
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$R_{gon} =$	32	Ω
$R_{goff} =$	32	Ω

**T1,T2,T3,T4,T5,T6 IGBT**
**Figure 10**

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



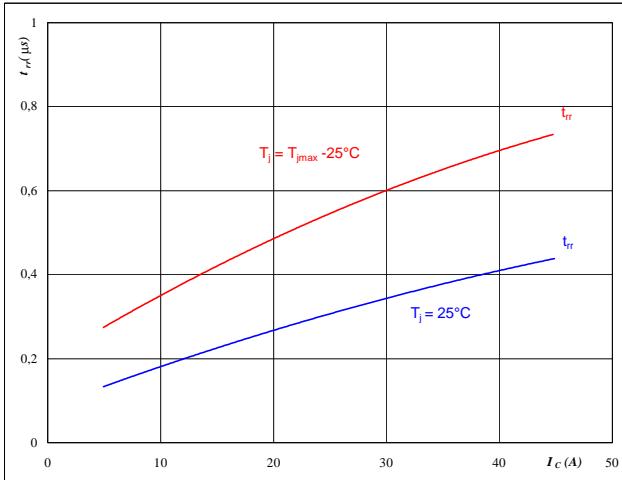
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$I_C =$	25	A

**Figure 11**
**D1,D2,D3,D4,D5,D6 FWD**

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



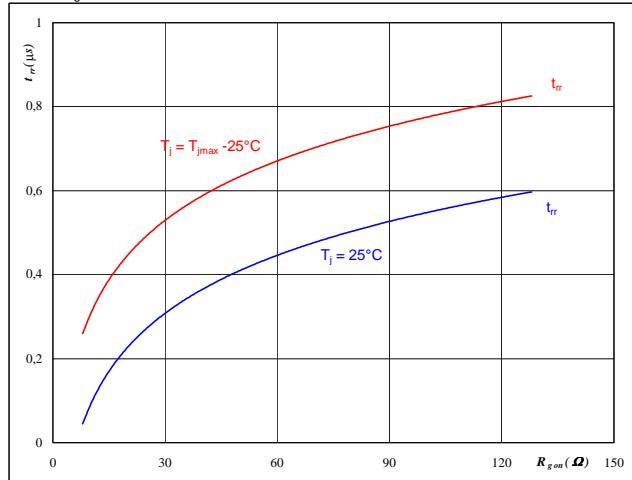
At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$R_{gon} =$	32	Ω

**Figure 12**
**D1,D2,D3,D4,D5,D6 FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	25	A
$V_{GE} =$	$\pm 15$	V

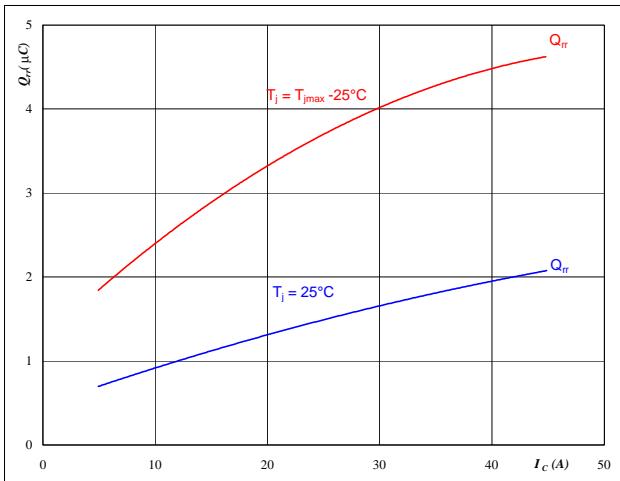
## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 13**

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


**At**

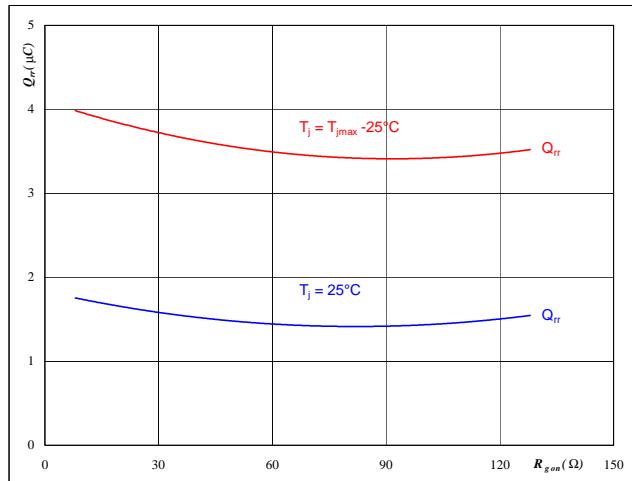
$T_j = \textcolor{red}{25/150} \quad {}^\circ C$   
 $V_{CE} = 600 \quad V$   
 $V_{GE} = \pm 15 \quad V$   
 $R_{gon} = 32 \quad \Omega$

**Figure 14**

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


**At**

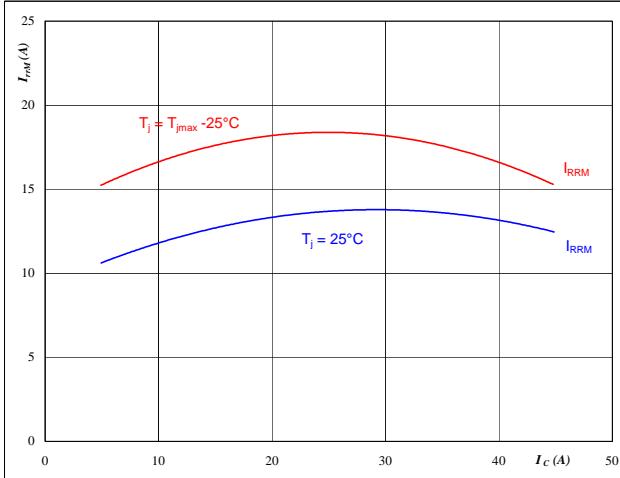
$T_j = \textcolor{red}{25/150} \quad {}^\circ C$   
 $V_R = 600 \quad V$   
 $I_F = 25 \quad A$   
 $V_{GE} = \pm 15 \quad V$

**Figure 15**

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


**At**

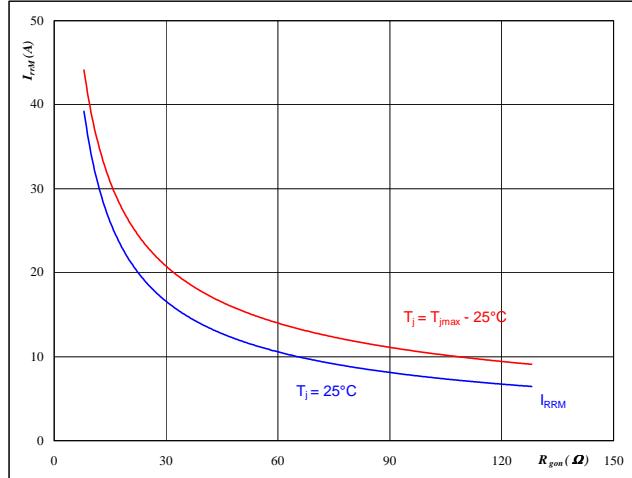
$T_j = \textcolor{red}{25/150} \quad {}^\circ C$   
 $V_{CE} = 600 \quad V$   
 $V_{GE} = \pm 15 \quad V$   
 $R_{gon} = 32 \quad \Omega$

**Figure 16**

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$


**At**

$T_j = \textcolor{red}{25/150} \quad {}^\circ C$   
 $V_R = 600 \quad V$   
 $I_F = 25 \quad A$   
 $V_{GE} = \pm 15 \quad V$

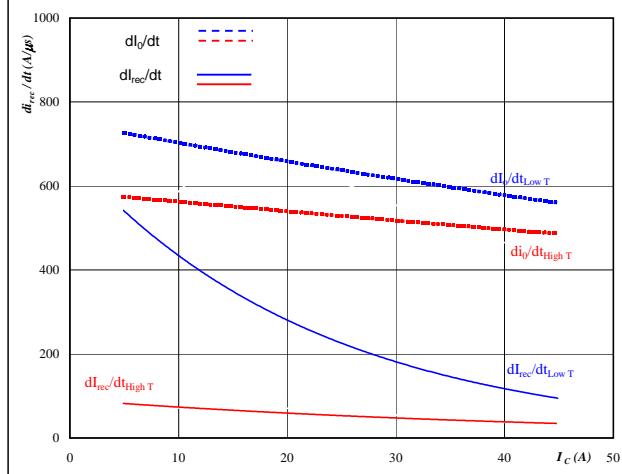
## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 17**

D1,D2,D3,D4,D5,D6 FWD

**Typical rate of fall of forward  
and reverse recovery current as a  
function of collector current**

$$dI_0/dt, dI_{rec}/dt = f(I_C)$$


**At**

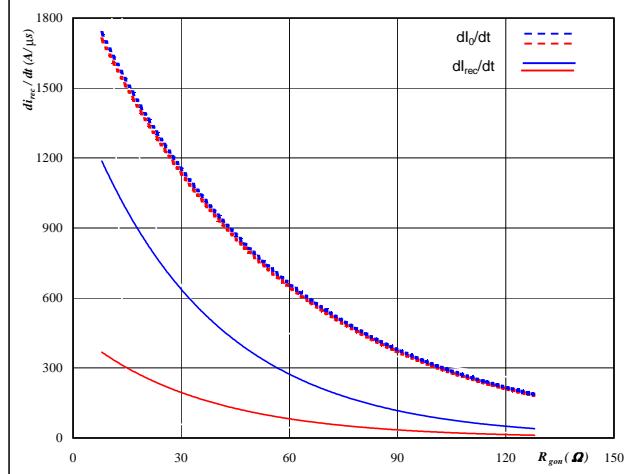
$T_j = \textcolor{blue}{25}/\textcolor{red}{150}$  °C  
 $V_{CE} = 600$  V  
 $V_{GE} = \pm 15$  V  
 $R_{gon} = 32$  Ω

**Figure 18**

D1,D2,D3,D4,D5,D6 FWD

**Typical rate of fall of forward  
and reverse recovery current as a  
function of IGBT turn on gate resistor**

$$dI_0/dt, dI_{rec}/dt = f(R_{gon})$$


**At**

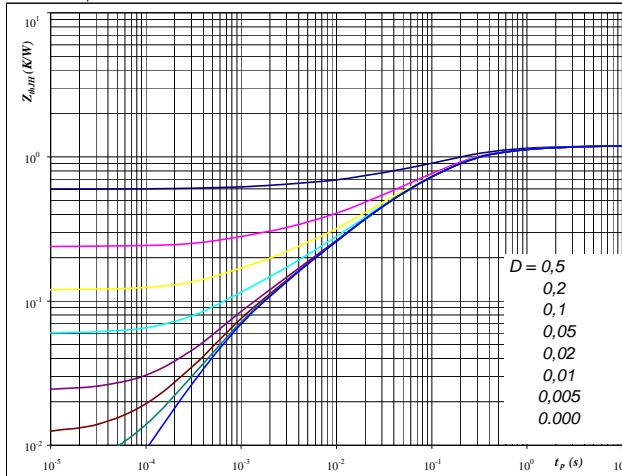
$T_j = \textcolor{blue}{25}/\textcolor{red}{150}$  °C  
 $V_R = 600$  V  
 $I_F = 25$  A  
 $V_{GE} = \pm 15$  V

**Figure 19**

T1,T2,T3,T4,T5,T6 IGBT

**IGBT transient thermal impedance  
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


**At**

$D = t_p / T$   
 $R_{thJH} = 1,20$  K/W

IGBT thermal model values

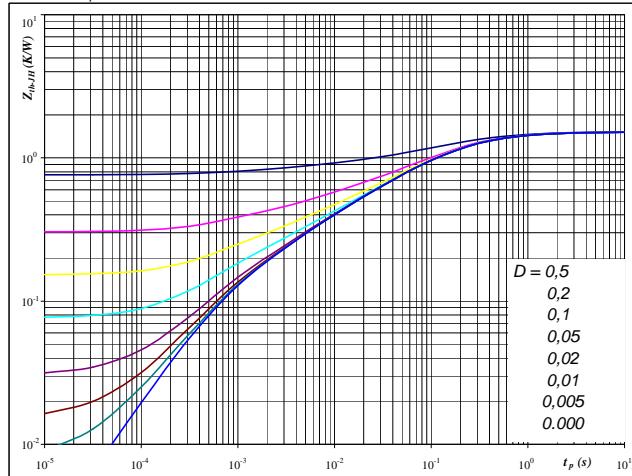
R (K/W)	Tau (s)
0,03	5,7E+00
0,14	8,1E-01
0,51	1,6E-01
0,27	4,9E-02
0,17	1,0E-02
0,07	9,8E-04

**Figure 20**

D1,D2,D3,D4,D5,D6 FWD

**FWD transient thermal impedance  
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


**At**

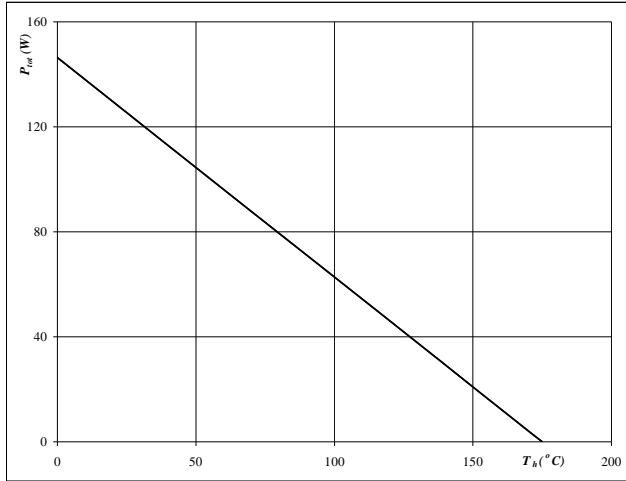
$D = t_p / T$   
 $R_{thJH} = 1,52$  K/W

FWD thermal model values

R (K/W)	Tau (s)
0,03	9,3E+00
0,22	7,6E-01
0,63	1,5E-01
0,37	3,0E-02
0,17	4,4E-03
0,10	6,5E-04

## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

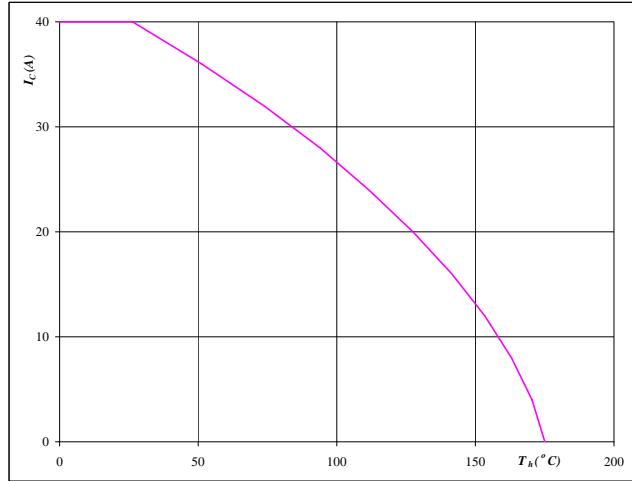
**Figure 21**  
**Power dissipation as a function of heatsink temperature**  
 $P_{\text{tot}} = f(T_h)$



At  
 $T_j = 175$  °C

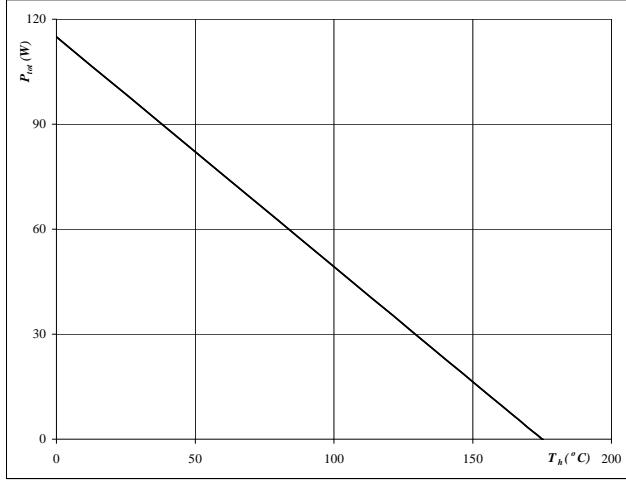
T1,T2,T3,T4,T5,T6 IGBT

**Figure 22**  
**Collector current as a function of heatsink temperature**  
 $I_C = f(T_h)$



At  
 $T_j = 175$  °C  
 $V_{GE} = 15$  V

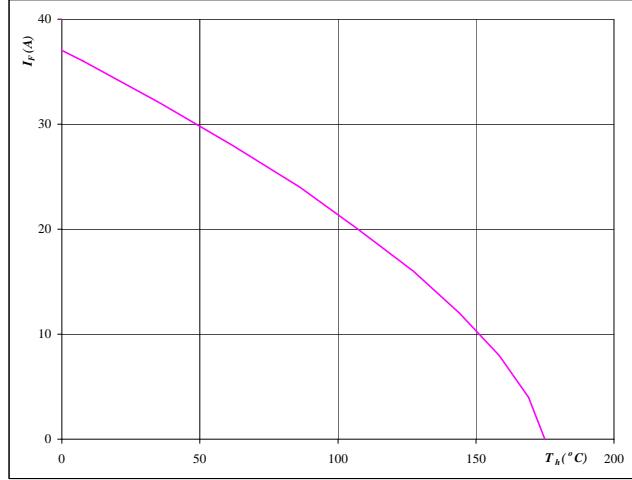
**Figure 23**  
**Power dissipation as a function of heatsink temperature**  
 $P_{\text{tot}} = f(T_h)$



At  
 $T_j = 175$  °C

D1,D2,D3,D4,D5,D6 FWD

**Figure 24**  
**Forward current as a function of heatsink temperature**  
 $I_F = f(T_h)$

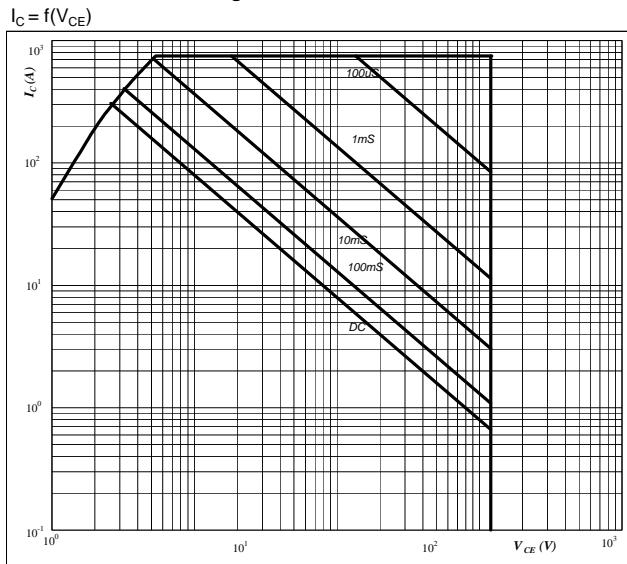


At  
 $T_j = 175$  °C

D1,D2,D3,D4,D5,D6 FWD

## T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

**Figure 25**  
Safe operating area as a function  
of collector-emitter voltage

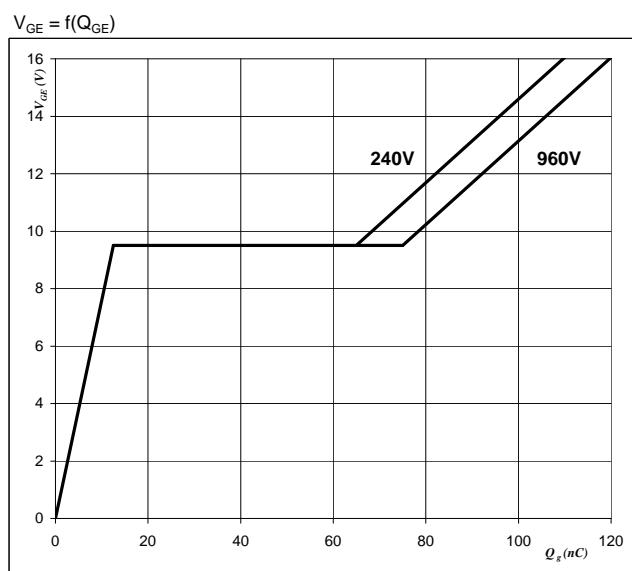


**At**

D = single pulse  
 $T_h = 80 \text{ } ^\circ\text{C}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $T_j = T_{j\max} \text{ } ^\circ\text{C}$

T1,T2,T3,T4,T5,T6 IGBT

**Figure 26**  
Gate voltage vs Gate charge



**At**

$I_C = 25 \text{ A}$

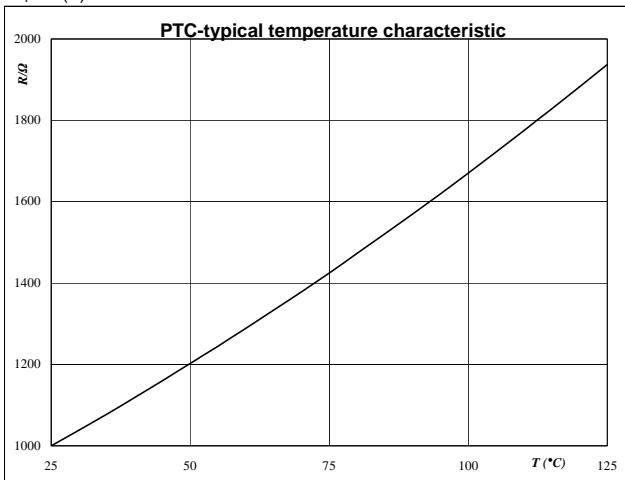
## Thermistor

**Figure 1**

Thermistor

Typical PTC characteristic  
as a function of temperature

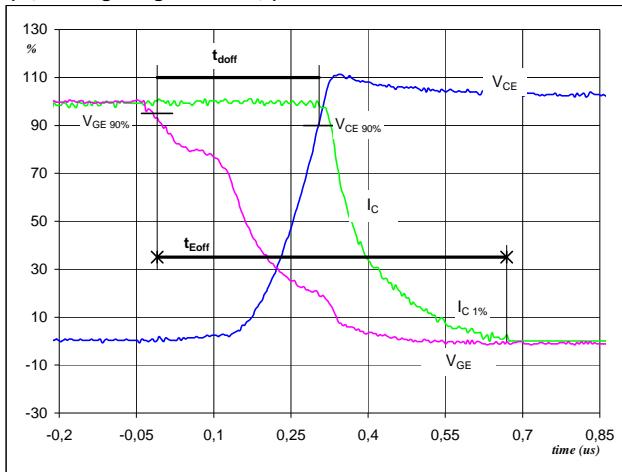
$$R_T = f(T)$$



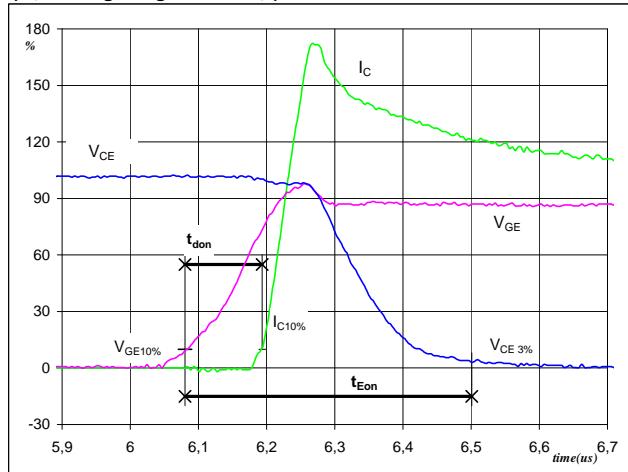
## Switching Definitions Output Inverter

**General conditions**

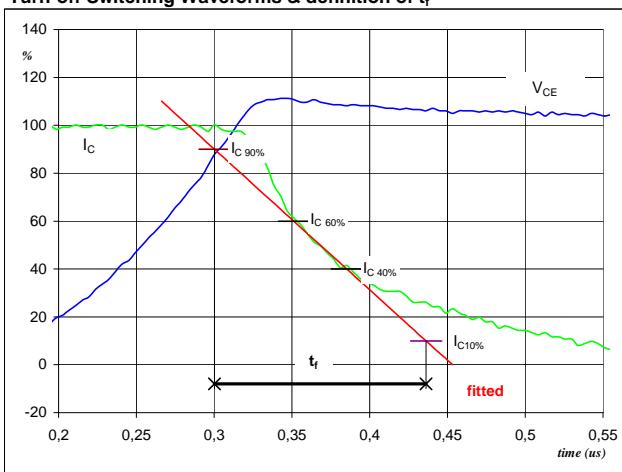
$T_j$	=	150 °C
$R_{gon}$	=	32 Ω
$R_{goff}$	=	32 Ω

**Figure 1**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$**   
 $(t_{Eoff} = \text{integrating time for } E_{off})$ 


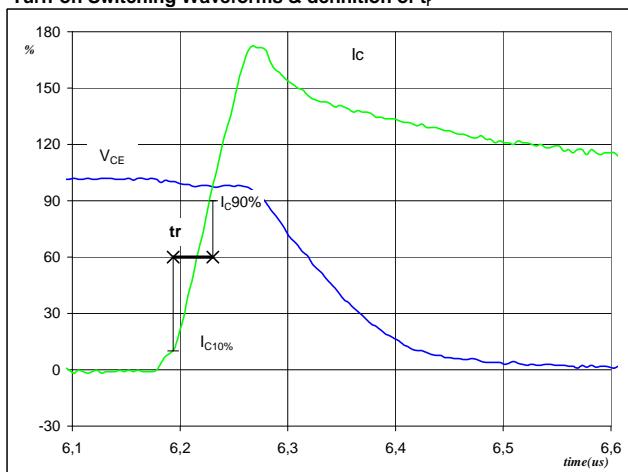
$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_{doff} = 0,30$  μs  
 $t_{Eoff} = 0,68$  μs

**Figure 2**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$**   
 $(t_{Eon} = \text{integrating time for } E_{on})$ 


$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_{don} = 0,11$  μs  
 $t_{Eon} = 0,42$  μs

**Figure 3**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_f$** 


$V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_f = 0,14$  μs

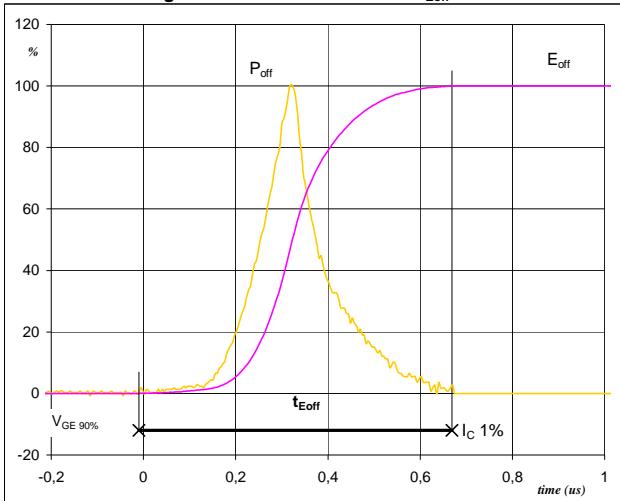
**Figure 4**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_r$** 


$V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_r = 0,03$  μs

## Switching Definitions Output Inverter

**Figure 5**

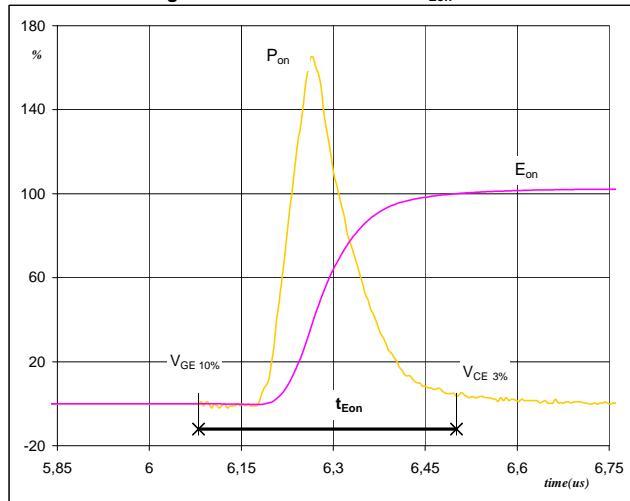
Output inverter IGBT

**Turn-off Switching Waveforms & definition of  $t_{Eoff}$** 


$P_{off} (100\%) = 14,95 \text{ kW}$   
 $E_{off} (100\%) = 2,43 \text{ mJ}$   
 $t_{Eoff} = 0,68 \mu\text{s}$

**Figure 6**

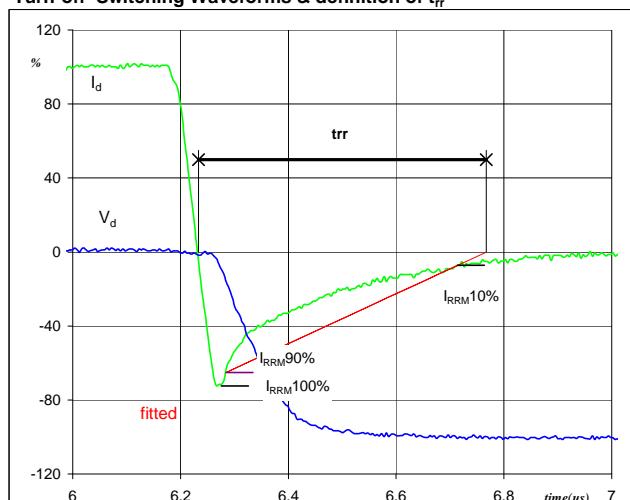
Output inverter IGBT

**Turn-on Switching Waveforms & definition of  $t_{Eon}$** 


$P_{on} (100\%) = 14,95 \text{ kW}$   
 $E_{on} (100\%) = 2,77 \text{ mJ}$   
 $t_{Eon} = 0,42 \mu\text{s}$

**Figure 7**

Output inverter FWD

**Turn-off Switching Waveforms & definition of  $t_{trr}$** 


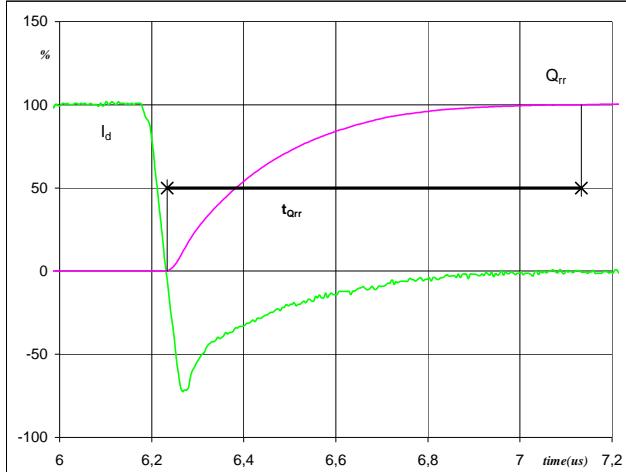
$V_d (100\%) = 600 \text{ V}$   
 $I_d (100\%) = 25 \text{ A}$   
 $I_{RRM} (100\%) = 18 \text{ A}$   
 $t_{trr} = 0,54 \mu\text{s}$

## Switching Definitions Output Inverter

**Figure 8**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

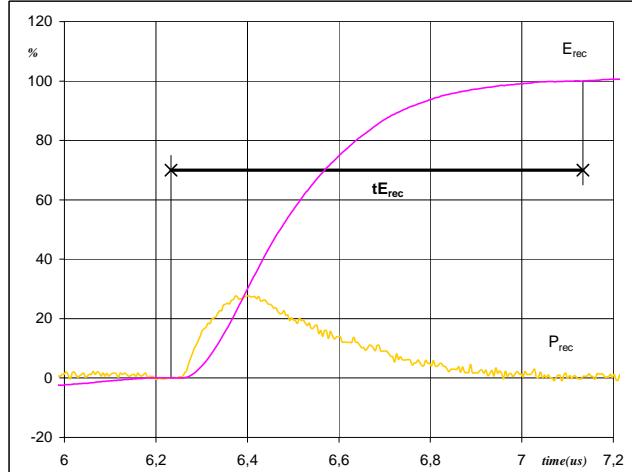


$I_d(100\%) = 25 \text{ A}$   
 $Q_{rr}(100\%) = 3,69 \mu\text{C}$   
 $t_{Qrr} = 0,90 \mu\text{s}$

**Figure 9**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



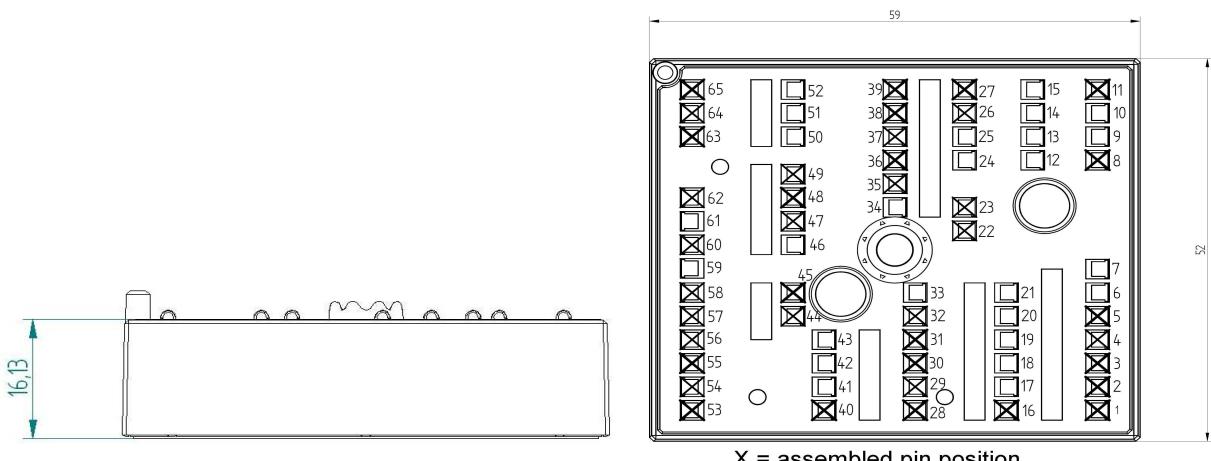
$P_{rec}(100\%) = 14,95 \text{ kW}$   
 $E_{rec}(100\%) = 1,44 \text{ mJ}$   
 $t_{Erec} = 0,90 \mu\text{s}$

### Ordering Code and Marking - Outline - Pinout

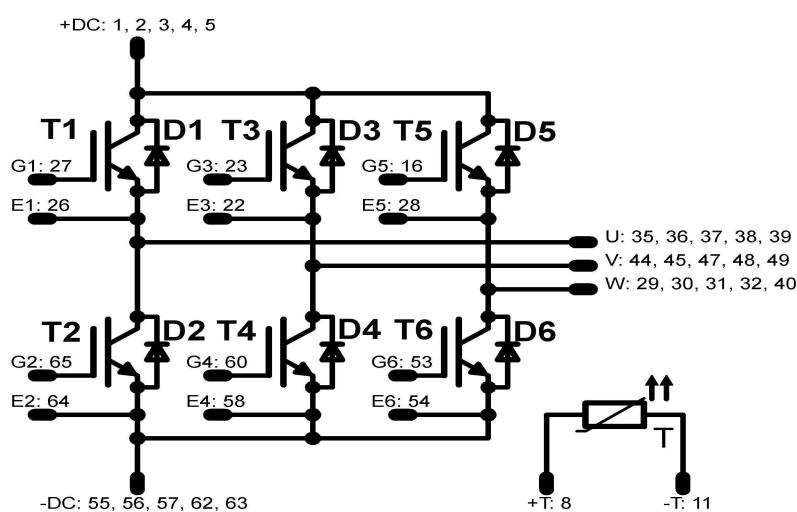
#### Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K22-T-PM)	V23990-K237-F40-/0A/-PM	K237F40	K237F40-/0A/
with std lid (black V23990-K22-T-PM) and P12	V23990-K237-F40-/1A/-PM	K237F40	K237F40-/1A/
with thin lid (white V23990-K23-T-PM)	V23990-K237-F40-/0B/-PM	K237F40	K237F40-/0B/
with thin lid (white V23990-K23-T-PM) and P12	V23990-K237-F40-/1B/-PM	K237F40	K237F40-/1B/

#### Outline



#### Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.